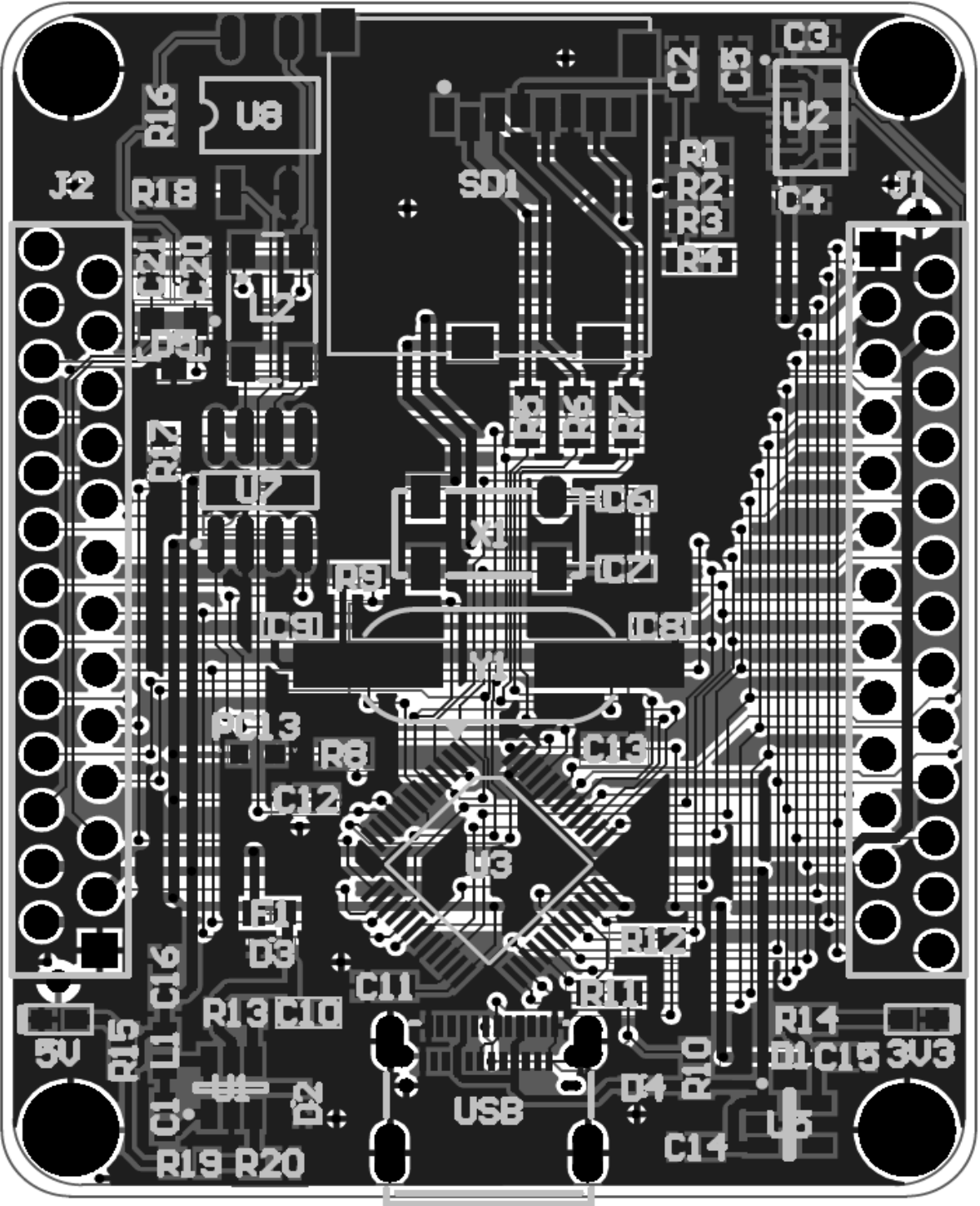


Tabelle1

PHYSICAL PIN
PIN NAME
CONTROL
ANALOG
USART
SPI
I2C
CAN BUS
PWM

			SWCLK	26
			SWDIO	25
PWM	RXD1		PA10	24
PWM	TXD1		PA9	23
PWM			PA8	22
	MOSI2		PB15	21
	MISO2		PB14	20
	SCK2		PB13	19
	CS2		PB12	18
	SDA2		PB11	17
	SCL2		PB10	16
PWM		ADC9	PB1	15
PWM		ADC8	PB0	14
PWM	MOSI1	ADC7	PA7	13
PWM	MISO1	ADC6	PA6	12
	SCK1	ADC5	PA5	11
	CS1	ADC4	PA4	10
PWM	RXD2	ADC3	PA3	9
PWM	TXD2	ADC2	PA2	8
			NRST	7
		CAN low		6
		CAN high		5
			Vin	4
			5V	3
			3V3	2
			GND	1



1	GND			
2	3V3			
3	5V			
4	Vin			
5		CAN high		
6		CAN low		
7	NRST			
8	PA2	ADC2	TXD2	PWM
9	PA3	ADC3	RXD2	PWM
10	PA4	ADC4	CS1	
11	PA5	ADC5	SCK1	
12	PA6	ADC6	MISO1	PWM
13	PA7	ADC7	MOSI1	PWM
14	PB0	ADC8		PWM
15	PB1	ADC9		PWM
16	PB10		SCL2	
17	PB11		SDA2	
18	PB12		CS2	
19	PB13		SCK2	
20	PB14		MISO2	
21	PB15		MOSI2	
22	PA8			PWM
23	PA9		TXD1	PWM
24	PA10		RXD1	PWM
25	SWDIO			
26	SWCLK			