## OSIMPLC V2.0

## PIC16F887 - PIC18F4520

by Daniel H. Mirkin



MCU			MCU		
INPUT	SIGNAL	PIN	OUTPUT	SIGNAL	PIN
X0	RA2	4	Y0	RB7	40
X1	RA3	5	Y1	RB6	39
X2	RA4	6	Y2	RB5	38
X3	RC0	15	Y3	RB4	37
X4	RC2	17	Y4	RB3	36
X5	RC3	18	Y5	RB2	35
X6	RD0	19	Y6	RB1	34
X7	RD1	20	Y7	RB0	33
X8	RC5	24	Y8	RD7	30
X9	RC4	23	Y9	RD6	29
XA	RD3	22	YA	RD5	28
XB	RD2	21	YB	RC1	16
A0	AN0	2	PWM	CCP2	16
A1	AN1	3	YDIR	RD4	27
RX	RC7	26	TX	RC6	25

E4DI		
INPUT	SIGNAL	PIN
XC	RE2	10
XD	RE1	9
XE	RE0	8
XF	RA5	7

E4AI		
INPUT	SIGNAL	PIN
A2	AN7	10
A3	AN6	9
A4	AN5	8
A5	AN4	7

E4CI	INVERTED!	
INPUT	SIGNAL	PIN
XC	RE2	10
XD	RE1	9
XE	RE0	8
XF	RA5	7

E4TI		
INPUT	SIGNAL	PIN
PT100-1	AN7	10
PT100-2	AN6	9
NTC1	AN5	8
NTC2	AN4	7

E4DO		
OUTPUT	SIGNAL	PIN
YC	RA5	7
YD	RE0	8
YE	RE1	9
YF	RE2	10

E2AI2DI		
INPUT	SIGNAL	PIN
XC	RA5	7
XD	RE0	8
A2	AN6	9
A3	AN7	10

E2AI2DO			E2AI2DO		
INPUT	SIGNAL	PIN	OUTPUT	SIGNAL	PIN
A2	AN4	7	YC	RE1	9
A3	AN5	8	YD	RE2	10

E2DI2DO			E2DI2DO		
INPUT	SIGNAL	PIN	OUTPUT	SIGNAL	PIN
XC	RA5	7	YC	RE1	9
XD	RE0	8	YD	RE2	10