

## 1. Description

## 1.1. Project

Project Name	Feather_RAK3172_LoRaWAN_End	
	_Node	
Board Name	custom	
Generated with:	STM32CubeMX 6.9.0	
Date	07/29/2023	

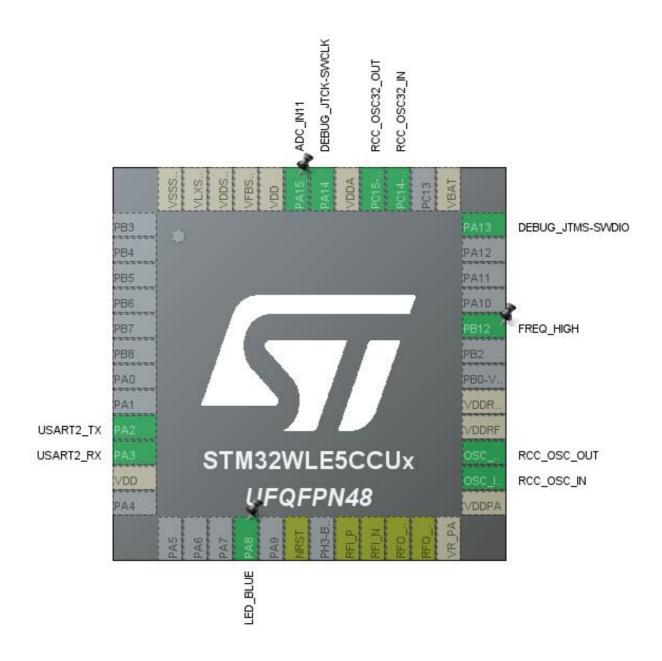
### 1.2. MCU

MCU Series	STM32WL
MCU Line	STM32WLEx
MCU name	STM32WLE5CCUx
MCU Package	UFQFPN48
MCU Pin number	48

## 1.3. Core(s) information

Core(s)	ARM Cortex-M4

## 2. Pinout Configuration

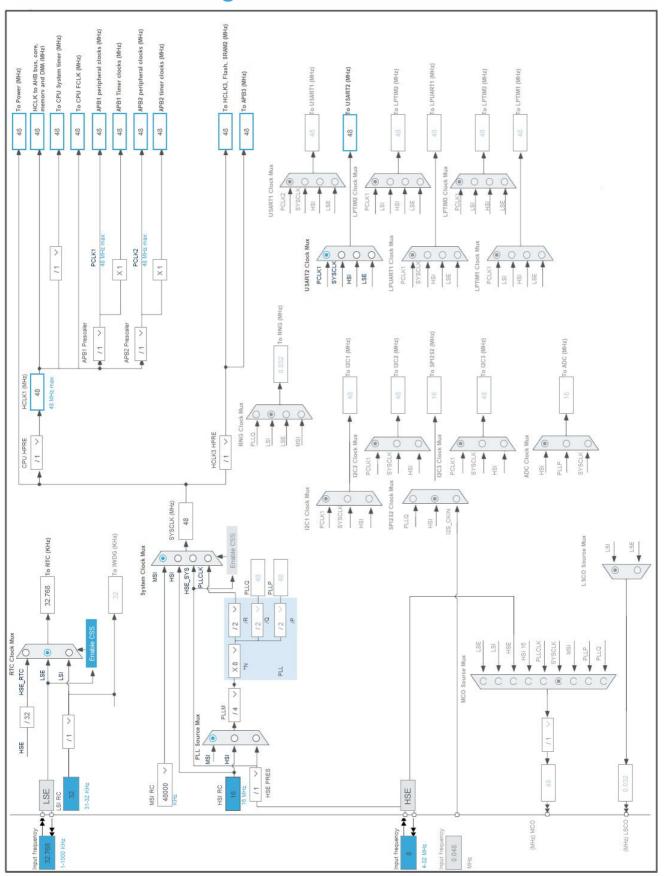


## 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after	Pin Type	Alternate Function(s)	Label
	reset)			
9	PA2	I/O	USART2_TX	
10	PA3	I/O	USART2_RX	
11	VDD	Power		
16	PA8 *	I/O	GPIO_Output	LED_BLUE
18	NRST	Reset		
20	RFI_P	MonolO		
21	RFI_N	MonolO		
22	RFO_LP	MonolO		
23	RFO_HP	MonolO		
24	VR_PA	Power		
25	VDDPA	Power		
26	OSC_IN	MonolO	RCC_OSC_IN	
27	OSC_OUT	MonolO	RCC_OSC_OUT	
28	VDDRF	Power		
29	VDDRF1V55	Power		
32	PB12 *	I/O	GPIO_Input	FREQ_HIGH
36	PA13	I/O	DEBUG_JTMS-SWDIO	
37	VBAT	Power		
39	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
40	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
41	VDDA	Power		
42	PA14	I/O	DEBUG_JTCK-SWCLK	
43	PA15	I/O	ADC_IN11	
44	VDD	Power		
45	VFBSMPS	Power		
46	VDDSMPS	Power		
47	VLXSMPS	Power		
48	VSSSMPS	Power		

<sup>\*</sup> The pin is affected with an I/O function

## 4. Clock Tree Configuration



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## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	Feather_RAK3172_LoRaWAN_End_Node
Project Folder	D:\Projects\Teapot Feather RAK3172
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_WL V1.3.0
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_RTC_Init	RTC
5	MX_SUBGHZ_Init	SUBGHZ
6	MX_USART2_UART_Init	USART2
7	MX_ADC_Init	ADC
8	MX_LoRaWAN_Init	LORAWAN

## 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32WL
Line	STM32WLEx
MCU	STM32WLE5CCUx
Datasheet	DS13105_Rev7

### 1.2. Parameter Selection

Temperature	25
Vdd	3.0

### 1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

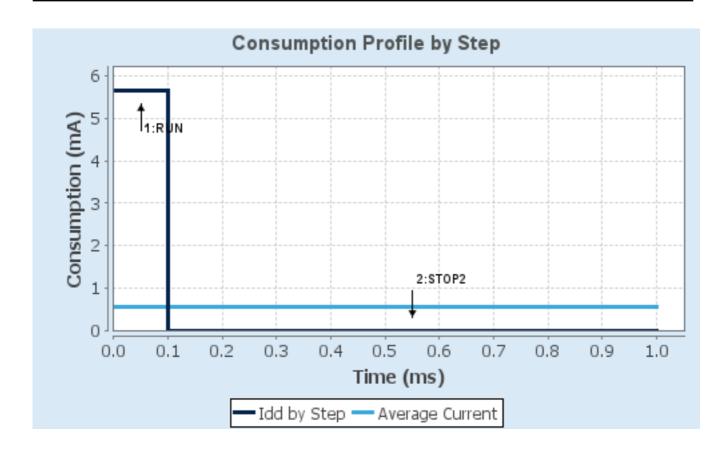
## 1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP2
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-Medium/SMPS-OFF	NoRange
Fetch Type	SRAM1	NA
CPU Frequency	48 MHz	0 Hz
Clock Configuration	MSI	ALL CLOCKS OFF
Clock Source Frequency	48 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	5.65 mA	885 nA
Duration	0.1 ms	0.9 ms
DMIPS	60.0	0.0
Ta Max	124.53	125
Category	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	565.8 μA
Battery Life	1 month, 21 days,	Average DMIPS	60.0 DMIPS
	1 hour		

### 1.6. Chart



## 2. Peripherals and Middlewares Configuration

2.1. ADC mode: IN11

mode: Temperature Sensor Channel

mode: Vrefint Channel2.1.1. Parameter Settings:

#### ADC\_Settings:

Clock Prescaler Synchronous clock mode divided by 4 \*

Resolution ADC 12-bit resolution

Calibration Disable

Data Alignment Right alignment
Scan Conversion Mode Disabled

End Of Conversion Selection End of single conversion

Low Power Auto Wait

Auto Off

Enabled \*

Continuous Conversion Mode

Disabled

Discontinuous Conversion Mode Disabled

Discontinuous Conversion Mode Disabled

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None

DMA Continuous Requests Disabled

Overrun behaviour

Sequencer

Sequencer set to fully configurable

SamplingTime Common 1 160.5 Cycles \*
SamplingTime Common 2 160.5 Cycles \*

Oversampling Mode Disabled

Trigger Frequency Low frequency \*

ADC\_Regular\_ConversionMode:

Enable Regular Conversions Disable

2.2. ADV\_TRACE

mode: Enabled

**2.3. DEBUG** 

JTAG and Trace: Serial Wire

#### 2.4. MISC

mode: Enabled

#### 2.5. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

#### 2.5.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Disabled
Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 64

MSI Calibration Value 0

MSI Auto Calibration Enabled

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

LSE Drive Capability

LSE oscillator low drive capability

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 2.6. RTC

mode: Activate Clock Source

mode: Activate Calendar
Alarm A: Internal Alarm A
2.6.1. Parameter Settings:

#### General:

Asynchronous Predivider value RTC\_PREDIV\_A \*

Bin Mode Free running Binary mode \*

SSRU Underflow Interrupt Enabled

Alarm A:

Free running 32 bit value 0

Binary AutoControl RTC\_ALARMSUBSECONDBIN\_AUTOCLR\_NO \*

Free running 32 bit mask

SS[31:0] are compared and must match to activate alarm.

#### 2.7. SEQUENCER

mode: Enabled

**2.8. SUBGHZ** 

mode: Activated

2.8.1. Parameter Settings:

Baudrate Prescaler Value 4 \*

2.9. SYS

**Timebase Source: None** 

2.10. TIMER

mode: Enabled

2.11. TINY\_LPM

mode: Enabled

2.12. USART2

**Mode: Asynchronous** 

2.12.1. Parameter Settings:

**Basic Parameters:** 

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable
ClockPrescaler 1

Fifo Mode Enable \*

Txfifo Threshold 1 eighth full configuration
Rxfifo Threshold 1 eighth full configuration

**Advanced Features:** 

Auto Baudrate Disable TX Pin Active Level Inversion Disable Disable **RX Pin Active Level Inversion** Data Inversion Disable TX and RX Pins Swapping Disable Overrun Enable DMA on RX Error Enable MSB First Disable

#### 2.13. LORAWAN

mode: Enabled

#### 2.13.1. LoRaWAN application:

#### Application selection:

Application End Node skeleton \*

Application configuration recommandations !! Please read carefully Information panel

below!!

board settings:

Send Tx on Timer or Button Evt TX\_ON\_TIMER

Probes Lines in Platform Settings false

lora\_app:

Active region LORAMAC\_REGION\_AS923

Transmition duty cycle 10000
Application user port 2
Switch class port 3

Default class CLASS\_A

Default handler message state Confirmed message \*

Handler Adaptive Data Rate

Default activation type

OTAA

Force rejoin at each reboot

true

Default data rate

DR\_0

Default Tx output power TX\_POWER\_0

Default Unicast ping slots periodicity 4

Default reponse timeout for class b and class c confirmed downlink frames in milli seconds. 8000

sys\_conf:

Trace verbose level VLEVEL\_H \*

Enable Application Logging true
Disable Low Power Mode false
Enable Sensor false

#### 2.13.2. LoRaWAN commissioning:

Commissioning:

Public network true
Current network ID 0

se-identity:

 LoRaWAN device EUI
 00,00,00,00,00,00,00,00

 App/Join EUI
 01,01,01,01,01,01,01

Application key 2B,7E,15,16,28,AE,D2,A6,AB,F7,15,88,0

9,CF,4F,3C

Network key 2B,7E,15,16,28,AE,D2,A6,AB,F7,15,88,0

9,CF,4F,3C

Device Address 00,00,00,00

Network session key 2B,7E,15,16,28,AE,D2,A6,AB,F7,15,88,0

9,CF,4F,3C

Application session key 2B,7E,15,16,28,AE,D2,A6,AB,F7,15,88,0

9,CF,4F,3C

lorawan\_conf:

Enable Key read access true

#### 2.13.3. LoRaWAN middleware:

#### lorawan\_conf:

Region(s) selection please select the desired region(s) in the

list below

Region Asia freq: 923 true \*

Region Australia freq: 915

Region China freq: 470

false
Region China freq: 779

false
Region Europe freq: 433

false
Region Europe freq: 868

false

false

Region Korea freq: 920 false
Region India freq: 865 false

Region USA freq: 915 false \*

Region Russia freq: 864 false
Enable LoRaMAC ClassB false
Enable the context management storage false

### Feather\_RAK3172\_LoRaWAN\_End\_Node Project Configuration Report

Select the LoRaWAN Link Layer specification version v1.0.4 \*

Enable the additional LoRaWAN packages false

Select the Default channel plan for region AS923 AS923-4 \*

radio\_conf:

Radio maximum wakeup time (in ms) 1

radio\_board\_if:

Select radio Driver

Bsp via extSettings \*

mw\_log\_conf:

Enable Middleware log true

### 2.13.4. Platform Settings:

RTC RTC
ADC ADC
USART USART2

<sup>\*</sup> User modified value

## 3. System Configuration

## 3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA15	ADC_IN11	Analog mode	No pull-up and no pull-down	n/a	
DEBUG	PA13	DEBUG_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	DEBUG_JTCK- SWCLK	n/a	n/a	n/a	
RCC	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	High *	LED_BLUE
	PB12	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	FREQ_HIGH

## 3.2. DMA configuration

DMA request	Stream	Direction	Priority
USART2 TX	DMA1 Channel1	Memory To Peripheral	Low

### USART2\_TX: DMA1\_Channel1 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable \*

Peripheral Data Width: Byte
Memory Data Width: Byte

## 3.3. NVIC configuration

## 3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	0	0	
RTC Tamper, RTC TimeStamp, LSECSS and RTC SSRU Interrupts	true	0	0	
DMA1 Channel 1 Interrupt	true	2	0	
USART2 Interrupt	true	2	0	
RTC Alarms (A and B) Interrupt	true	0	0	
SUBGHZ Radio Interrupt	true	0	0	
PVD and PVM detector	unused			
FLASH (CFI) global Interrupt	unused			
RCC Interrupt	unused			
ADC Interrupt	unused			

## 3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	false
RTC Tamper, RTC TimeStamp, LSECSS and RTC SSRU Interrupts	false	true	true
DMA1 Channel 1 Interrupt	false	true	true
USART2 Interrupt	false	true	true
RTC Alarms (A and B) Interrupt	false	true	true

# Feather\_RAK3172\_LoRaWAN\_End\_Node Project Configuration Report

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
SUBGHZ Radio Interrupt	false	true	true

<sup>\*</sup> User modified value

## 4. System Views

4.1. Category view

4.1.1. Current

### 5. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32wl\_bsdl.zip

HW Models https://www.st.com/resource/en/hw\_model/stm32wl\_reference\_board\_mb

1720-xo\_ios\_shield.7z

IBIS models https://www.st.com/resource/en/ibis model/stm32wl ibis.zip

System View https://www.st.com/resource/en/svd/stm32wl\_svd.zip

Description

Bill of Materials https://www.st.com/resource/en/bill\_of\_materials/mb1720\_bom.zip

Bill of Materials https://www.st.com/resource/en/bill\_of\_materials/mb1789\_hp\_bom.zip

Bill of Materials https://www.st.com/resource/en/bill\_of\_materials/mb1789\_lp\_bom.zip

Bill of Materials https://www.st.com/resource/en/bill of materials/mb1791 bom.zip

Bill of Materials https://www.st.com/resource/en/bill\_of\_materials/mb1792\_bom.zip

Bill of Materials https://www.st.com/resource/en/bill\_of\_materials/mb1842\_bom.zip

Board https://www.st.com/resource/en/board manufacturing specification/mb17

Manufacturing 20\_bdp.zip

**Specifications** 

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb17

Manufacturing 20\_manufacturing.zip

Specifications

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb17

Manufacturing 89\_hp\_bdp.zip

**Specifications** 

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb17

Manufacturing 89\_hp\_manufacturing.zip

**Specifications** 

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb17

Manufacturing 89\_lp\_bdp.zip

**Specifications** 

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Manufacturing 89\_lp\_manufacturing.zip

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**Specifications** 

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb18

Manufacturing 42\_bdp.zip

**Specifications** 

Board https://www.st.com/resource/en/board\_manufacturing\_specification/mb18

Manufacturing 42\_manufacturing.zip

**Specifications** 

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1720\_schematics.zip

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1789\_hp\_schematic.z

ip

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1789\_lp\_schematic.zi

р

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1791\_schematic.zip

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1792\_schematic.zip

Schematic Pack https://www.st.com/resource/en/schematic\_pack/mb1842\_schematic.zip

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_stm8\_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_software\_development\_tools.pdf

Presentations https://www.st.com/resource/en/product\_presentation/microcontrollers\_st

m32wl\_series\_product\_overview.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32cubemonitor-

wireless-longrange\_rftest.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32wba-series-

product-overview.pdf

Flyers https://www.st.com/resource/en/flyer/flnucleolrwan.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32wl.pdf

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Application Notes https://www.st.com/resource/en/application\_note/an1181-electrostatic-

discharge-sensitivity-measurement-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2606-stm32-

microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2639-soldering-

recommendations-and-package-information-for-leadfree-ecopack-mcus-

and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2834-how-to-get-the-

best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2867-oscillator-

design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an3126-audio-and-

waveform-generation-using-the-dac-in-stm32-products-

stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an3155-usart-protocol-

used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an4013-stm32-

crossseries-timer-overview-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application\_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4759-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4894-eepromemulation-techniques-and-software-for-stm32-microcontrollersstmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an5036-thermal-management-guidelines-for-stm32-applications-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application\_note/an5407-optimized-rf-

board-layout-for-stm32wl-series-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5408-migrating-fromstm32l0-stm32l1-and-stm32l4-series-associated-with-sx12xxtransceivers-to-stm32wl-series-microcontrollers-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5409-stm32cubemcu-package-examples-for-stm32wl-series-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5457-rf-matchingnetwork-design-guide-for-stm32wl-series-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5480-how-to-build-asigfox-application-with-stm32cubewl-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application note/an5543-enhancedmethods-to-handle-spi-communication-on-stm32-devicesstmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5568-ultralowpowerfeatures-of-stm32wl-series-microcontrollers-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5690-vrefbufperipheral-applications-and-trimming-technique-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5664-rssi-and-snr-forlora-modulation-on-stm32wl-series-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application note/an4899-stm32microcontroller-gpio-hardware-settings-and-lowpower-consumptionstmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5612-esd-protectionof-stm32-mcus-and-mpus-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application note/an5796-stm32wl-seriesrf-bench-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5156-introduction-tostm32-microcontrollers-security-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5646-32-mhz-hseoscillator-selection-for-stm32wl-series-mcus-stmicroelectronics.pdf Application Notes https://www.st.com/resource/en/application\_note/an5042-how-tocalibrate-the-hse-clock-for-rf-applications-on-stm32-wireless-mcus-

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& Software

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