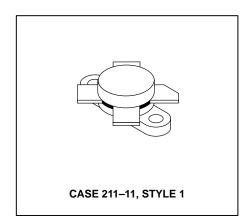
The RF Line NPN Silicon RF Power Transistor

Designed primarily for applications as a high–power linear amplifier from 2.0 to 30 MHz.

- Specified 28 Volt, 30 MHz Characteristics —
 Output Power = 150 W (PEP)
 Minimum Gain = 10 dB
 Efficiency = 40%
- Intermodulation Distortion @ 150 W (PEP) —
 IMD = -30 dB (Min)
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR

MRF422

150 W (PEP), 30 MHz RF POWER TRANSISTORS NPN SILICON



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	40	Vdc
Collector–Base Voltage	V _{СВО}	85	Vdc
Emitter–Base Voltage	V _{EBO}	3.0	Vdc
Collector Current — Continuous	IC	20	Adc
Withstanding Current — 10 s	_	30	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	290 1.66	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.6	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 200 mAdc, I _B = 0)	V(BR)CEO	35	1		Vdc
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, V _{BE} = 0)	V(BR)CES	85	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 100 mAdc, I _E = 0)	V(BR)CBO	85	_	_	Vdc
Emitter-Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V(BR)EBO	3.0	_	_	Vdc
Collector Cutoff Current (V _{CE} = 28 Vdc, V _{BE} = 0, T _C = 25°C)	ICES			20	mAdc

(continued)

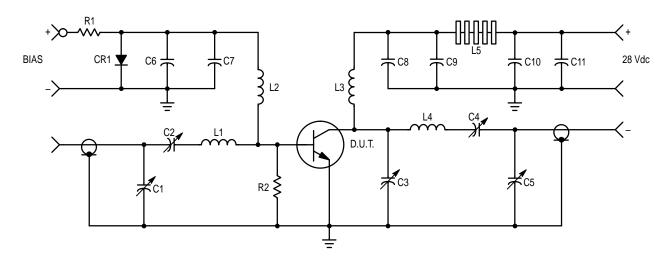


ELECTRICAL CHARACTERISTICS — **continued** ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	hFE	15	30	120	_
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	420	_	pF
FUNCTIONAL TESTS					
Common–Emitter Amplifier Power Gain ($V_{CC} = 28 \text{ Vdc}$, $P_{out} = 150 \text{ W (PEP)}$, $I_{C(max)} = 6.7 \text{ Adc}$, $I_{CQ} = 150 \text{ mAdc}$, $f = 30, 30.001 \text{ MHz}$)	GPE	10	13	_	dB
Collector Efficiency $(V_{CC} = 28 \text{ Vdc}, P_{out} = 150 \text{ W (PEP)}, I_{C(max)} = 6.7 \text{ Adc}, I_{CQ} = 150 \text{ mAdc}, f = 30, 30.001 \text{ MHz})$	η	_	45	_	%
Intermodulation Distortion (1) (VCE = 28 Vdc, P_{Out} = 150 W (PEP), I_{C} = 6.7 Adc, I_{CQ} = 150 mAdc, f = 30, 30.001 MHz)	IMD	_	-33	-30	dB
Output Power (V _{CE} = 28 Vdc, f = 30 MHz)	P _{out}	150	_	_	Watts (PEP)

NOTE:

^{1.} To Mil-Std-1311 Version A, Test Method 2204, Two Tone, Reference each Tone.



C1, C2, C3, C5 — 170–680 pF, ARCO 469

C4 — 80-480 pF, ARCO 466

C6, C8, C11 — ERIE 0.1 μ F, 100 V

C7 — MALLORY 500 μF, 15 V Electrolytic

C9 — UNDERWOOD 1000 pF, 350 V

C10 — 10 μ F, 50 V Electrolytic

R1 — 10 Ω , 25 Watt Wire Wound

 $R2 - 10 \Omega$, 1.0 Watt Carbon

CR1 - 1N4997

L1 — 3 Turns, #16 Wire, 5/16" I.D., 5/16" Long

 $L2 - 10 \, \mu H$ Molded Choke

L3 — 12 Turns, #16 Enameled Wire, Close Wound, 1/4" Dia.

L4 — 5 Turns, 1/8" Copper Tubing

L5 — 10 Ferrite Beads — FERROXCUBE #56-590-65/3B

Figure 1. 30 MHz Test Circuit Schematic

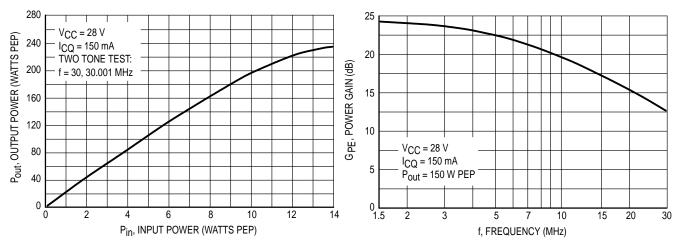


Figure 2. Output Power versus Input Power

Figure 3. Power Gain versus Frequency

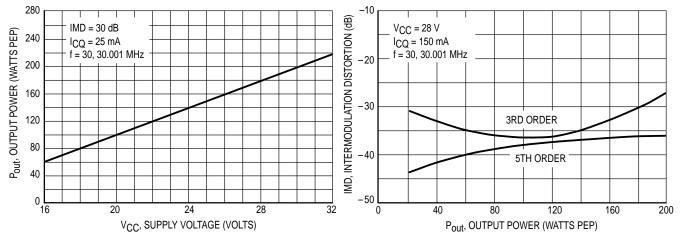


Figure 4. Linear Output Power versus Supply Voltage

Figure 5. Intermodulation Distortion versus Output Power

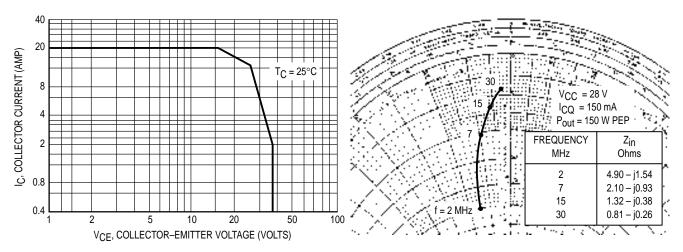


Figure 6. DC Safe Operating Area

Figure 7. Series Input Impedance

MOTOROLA RF DEVICE DATA MRF422

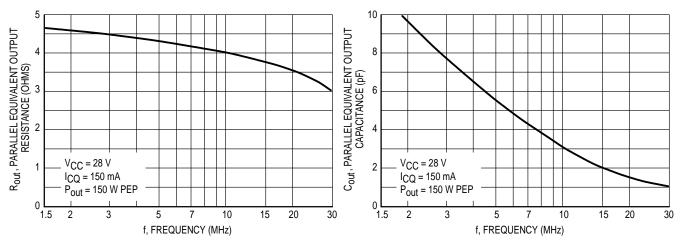
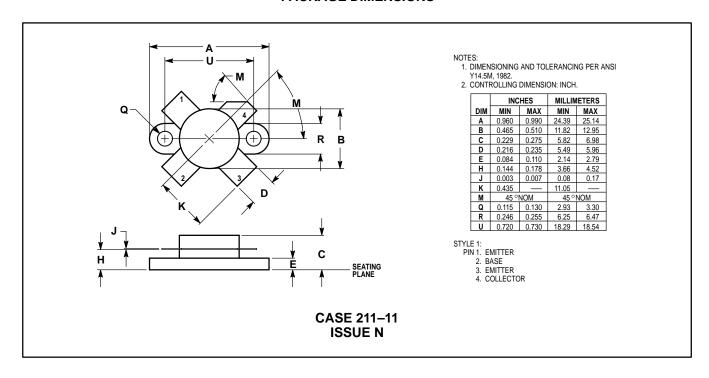


Figure 8. Output Resistance versus Frequency

Figure 9. Output Capacitance versus Frequency

PACKAGE DIMENSIONS



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