MICRO-BUS

Compiled by DJD.

Appearing ever two months, Micro-Bulls presents ideas, applications, and programs for the most popular phenogeness, and programs for the most popular phenogeness, and programs for the most popular of the most popular phenogeness, and person of the most popular phenogeness of the most popular phenogeness and person person of the most popular phenogeness and person person of the most popular phenogeness and person per

T HIS month's Micro-Bus examines the Acorn Teletext VDU, an unusual memory-mapped VDU with the ability to display Prestel and Teletext pictures. The VDU card can be used with the low-cost System One microcomputer, and several programs are presented to demonstrate the use of the VDU with the System One for graphics.

BLOCK DIAGRAM

A block diagram of the Acorn Teletext VDU is shown in Fig. 2. It consists of three main parts: the VDU memory, the controller chip, and the teletext character generator. The VDU displays the contents of the 1K of memory, which is switched between the VDU and the processor for half of each memory cycle; in other words, access to the VDU is transparent so that the processor can read from or write to the screen memory without affecting the display. The VDU is normally configured for a format of 25 lines of 40 characters, thus using 1000 bytes of the 1024 RAM locations. This format includes an extra line over the teletext format, which is 24 x 40. The memory is normally addressed as locations #0400 to #07FF.



Fig. 1. The Acorn Teletext VDU cerd

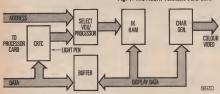


Fig. 2. Block diegrem of the teletext VDU card; it interfeces to the computer's address end dete lines

SCREEN FORMAT

The format of characters on the screen is controlled by the 6845 CRT controller chip, and the format can, to a certain extent, be reprogrammed simply by altering constants stored in the 6845's registers. The 6845 is addressed as two memory locations; an address register and a data register. In fact the 6845

contains a total of 18 registers; the number loaded into the address registers determines which of the other registers is selected as the data register. A drawback of using the CRTC is that it must be programmed before any dishap will be obtained. The routine of Fig. 31 lustrates how to do this using the 6020 micro. The contract of the contract of

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	80 00 08	LOOPS		CREA	SELECT REG
0985	B9 91 09		LDA	CRTTAB,Y	GET VALUE
0488	8D 01 03			CREB	
09.88	88		DEY		
	10 P4		BPL.	LOOPS	
OPEE	4C G4 FF		JMD	RESTRY	BACK,
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0991	3P		70.147870	\$37,\$28,\$3	205
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Fig. 3. Progrem for the 6502 micro sets up the VDU cerd for a 25 × 40 displey, and a fleshing-underline cursor

Registers \$00 to \$09 determine the screen format. For example, to obtain a format of 16 rows of 32 characters set register \$401 (number of characters per line) to \$20, and register \$406 (number of lines) to \$10. The display can be turned off completely, without affecting the contents of the display memory, by setting register \$406 to \$90.

Registers #0C and #0D contain the high and low bytes respectively of the screen start address. The start address can be changed to alter the mapping of memory to the screen; for example, incrementing it by 40, the number of locations per line, will scroll the display.

The CRTC also provides a cursor, which can be flashing or static, and whose shape can be programmed. The type of cursor is determined by registers #0A and #0B. For the normal flashing underline cursor register #0A has the value #68; or cursors can be obtained by altering this register as follows:

Function:	Value:
Cursor off	#1F
Static block	#00
Slow flashing block	#60
Fast flashing block	#40

The position of the cursor on the screen is determined by registers #0E and #0F, which contain the high and low bytes of the cursor address.

The CRTC also provides a light-pen strobe input; a logic-level transition on this input will cause the address of the cell currently being displayed to be stored in the light-pen address registers, #10 and #11.

TELETEXT CONTROLLER

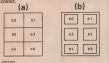
The character generation and display is performed by the SAA5050 Teletext Character Generator, which in addition to being able to display the usual and lower-case character set, has many attractive features such as: doubleheight characters; the ability to flash characters; display of characters in six colours or white against a background of any colour, black, or white; and display of two different types of graphics. Furthermore, most of these options can be used in combination.

Most memory-mapped VDUs use spare bits in the display memory to select special options for each character; for example, the top bit of the character could be used to determine whether the character is displayed static or flashing. In the teletext VDU there are too many different options to enable them to be specified in this way for each character, so a rather cunning method is used instead. Certain control codes, when present in a line, change the state of subsequent characters on that VDU line. Thus a line will normally be steady, but if a 'flash' code is put on the VDU, all the remaining characters on that line will flash. It is even possible to make one word in a line flash by preceding the word with a 'flash' code, and following it by a 'steady' code.

The Teletext method of selecting special functions has one drawback: the control code will appear on the VDU as a blank cell, corresponding to the background colour; so, for example, it is not possible to display a line of contiguous characters all in different colours: there has to be a blank between each character.

GRAPHICS

To provide the facility for plotting graphs and histograms the character cell is divided into six picture elements, or "pixels", each of which can be either set to a colour, or cleared to the background colour. The state of each cell is determined by one bit of the code, as shown in Fig. 4. For the graphics symbols, bit 5 is always set. To display codes as graphics, rather than characters, they are preceded by a code specifying graphics and the required colour.



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Fig. 4. Diagrems showing how the teletext grephics symbols ere constructed for: (e) contiguous graphics, end (b) sepereted graphics

The usual graphics mode is called 'contiguous graphics', but there is also an option called 'separated graphics' in which the pixels are separated by a thin border; see Fig. 4 (b). If the whole VDU screen is to be used for

graphics the first character of each line should be a 'graphics colour' code; the overall resolution is thus 78 × 75. A routine to clear the display and set it up for white graphics is shown in Fig. 5.

CLEAR SCREEN FOR GRAPHICS

Fig. 5. Routine to cleer the display, and initialise the display memory for graphics

FXAMPLES

Some examples of displays generated by the Teletext VDU are shown in Fig. 6. The top line demonstrates the use of colour. The default colour at the start of each line is white; to display the word CYAN in the colour cyan it is preceded by an "alpha cyan" code, and it is followed by an "alpha white" code to return to white characters.

The second line illustrates the use of a different background colour. The background colour can be set to the current colour with the "new background" code; since the default colour at the start of a line is white, a "new background" code at the start of a line will give a white background. It is followed by an 'alpha blue" code to cause the subsequent characters to appear in blue, which is dark against the white background. The background is returned to black after the text with a "black background" code.

WHITE CYAN WHITE CYAN

GRAPH PLOTTING The following two programs illustrate how the Acorn Teletext VDU can be used for simple graph-plotting. The routines were devised



Fig. 7. Routine for the 6502 micro to plot a point et specified coordinates on the teletext VDU

DOUBLE HEIGHT CONTIGUOUS GRAPHICS: arting the contract of SEPARATED GRAPHICS

Fig. 6. Exemples of the types of displey possible with the teletext VDII

The double-height characters are obtained by putting the same characters on two successive lines, preceded in each case by "double height"codes. The VDU automatically displays the top halves of the letters on the first line and the lower halves on the second line.

The first graphics example shows two lines of contiguous graphics, preceded by "graphics white" and "graphics cyan" codes respectively. In the second graphics example the lines are the same, but are also preceded by "separated graphics" codes.

by Peter Mayne of London, who uses the VDU with an Acorn System One microcomputer

The PLOT routine, in Fig. 7, plots a point, or pixel, on the display at the coordinates specified in the locations XC and YC. The coordinates (0,0) correspond to the bottom left-hand corner of the display, and (77,74) to the top right. Note that XC and YC are modified by the PLOT routine.

The program works by finding, in ADDS, the address of the location that contains the required pixel, and determines a number to add to that location to set the required pixel. Since there are three pixels per cell in the Y direction, the routine must divide the value of YC by three to find which line contains the required location.

The GRAPH program, Fig. 8, demonstrates how the PLOT routine can be used to plot a graph of an equation. As it stands, the program plots Y = ½X + 16 for values of X from 0 to 63. The program also draws axes, and finally labels the graph with the equation; see Fig. 9. A delay is included to

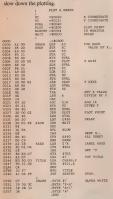


Fig. 8. Program for the 6502 micro to plot a graph on the teletext VDU

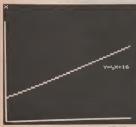


Fig. 9. Greph produced by the progrem of Fig. 8

Before running the program, the routines of Fig. 3 and Fig. 5 should be loaded and executed at #0980 and #0E80 respectively to initialise the CRTC and clear the screen for graphics.

NINE PROBLEMS REVISITED

One of the problems poxed in the April Micro-Bus saked for a way to reverse the bits in a byte, on the 6800 micro, in under 10 yeglest. A solution in hardware was proposed, but John Diamond of Coventry has solved the but John Diamond of Coventry has solved the reverse of that byte's position in the table. The reverse of that byte's position in the table. The Fig. 10, then takes only 9 cycles. It uses self-modifying ode to avoid having to use the index register, thus saving several cycles. A memory.

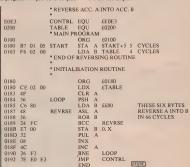
The listing of Fig. 10 also includes an initialisation routine to generate the 256-byte look-up table. Other functions could be implemented simply by providing a different

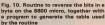
Another of the problems was to write a programme to find the highest prime factor of a number using a rudimentary machine-code called MINIL. The solution was given without explanation, and readers were asked to provide one. J. Rennie of Somerset wrote:

"Your challenge to provide an explanation of the MINIL program . . . proved to be quite irresistable, and I found the task most ineteresting. The program is based on a fairly simple algorithm (see flowchart of Fig. 11). Let N denote the number whose highest prime factor is to be found. Thus $N=\{P_a, x_i\}_{i=1}^n$ factor is to be found. Thus $N=\{P_a, x_i\}_{i=1}^n$ factor of N. The algorithm finds the highest factor of N, working down from N-1, which is $(P_a, Y_i)_i \times \dots \times P_{n-1}_{n-1}$. This is repeated until only P_i , is left. No more given the med factor of $(P_a, x_i)_i \times \dots \times P_{n-1}_{n-1}$. This is repeated until only P_i , is left. No more factor of the original number, can be output.

"In the original MINIL program registers D and A hold the current values of N and X respectively, while steps 4-10 find the factors of N. The two loops NOT and NEW are identical to loops 1 and 2 of the flowchart."

A similar explanation of the program was received from Doug Letts of London. Readers interested in the MINIL language may like to try writing programs which find the greatest common divisor of two numbers, the integer part of a number's square root, and the factorial of a number. But be warned: one of these problems is impossible, and the other two are not simple!





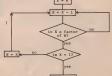


Fig. 11. Flowchart of the MINIL progrem to find the highest prime factor of a number