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## PhD Student, Formal Methods for Pseudo-Electric Circuit Verification

	Education
2022 — present	PhD Student - Formal Methods, Université Claude Bernard Lyon 1, Lyon - France. Doctoral School: InfoMaths
2021 – 2022	Master of Science - Master of Science in Informatics at Grenoble (MoSIG), Grenoble INP - ENSIMAG, Grenoble - France. Option: Software and Hardware Components Engineering
2020 – 2022	Engineer's Degree - Embedded Systems and Connected Devices, Grenoble INP - ENSIMAG, Grenoble - France.
2018 – 2022	Engineer's Degree - Embedded Systems and Digital Services, Institut National des Postes et Télécommunications, Rabat - Morocco.
2016 – 2018	Associate's Degree - Mathematics, Informatics and Physics, Faculty of Science et Technology - Hassan the First University, Settat - Morocco.
2016	Baccalauréat, Lycée Al-Qods, Tantan - Morocco.
	Experience
	As a PhD Student
	Research and Development,  Aniah, Grenoble - France.
	Internships
•	Research Internship - Formal Methods, Verimag Laboratory, Grenoble - France, Applying formal verification techniques to circuit electric verification.
=	<b>Internship - Connected Systems</b> , <i>Schneider Electric</i> , Grenoble - France, Implementation and test of Zigbee Direct; a feature that enables Bluetooth Low Energy capable devices to communicate with Zigbee networks.
	<b>Internship - Emebdded Systems</b> , <i>Arkeocean</i> , Rabat - Morocco, Simulation of scuba diving decompression algorithms & Implementation of a dive computer on a STM32F4 microcontroller platform.
July 2019	Internship - Data Analysis,  Soft Centre, Agence Nationale de Réglementation des Télécommunications, Rabat - Morocco,  Data analysis for a multimodal mobility management solution.
	Associations
June 2019 –	President, IEEE INPT Student Branch, IEEE, Morocco Section.

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