Exercise 3: Electrical and Logical Characteristics of Gates

Oliver Vinceas Section: 3 **Points** Point Comments Prelab Earned Value 0 Part 1 10

10

10

Prelab

Part 2

| Demo | | Point Value | Points Earned | Date | |
|------|-------------------------|----------------|------------------|-------|-----|
| Demo | Voltage measurements | 20 | 70 | 01/26 | SM. |
| | Ring oscillator | 20 | 20 | 01/26 | SM. |

To receive any grading credit students must earn points for both the demonstration and the report.

Exercise 3: Electrical and Logical Characteristics of Gates

| Report | | Point Value | Points Earned | Comments |
|------------------------------------|--|----------------|------------------|----------|
| | | 3 | | |
| Design Methodology | Voltage measurement circuit schematic | 4 | | . I |
| - | Ring oscillator circuit schematic | 4 | | |
| | Voltage tables | 4 | 2 | |
| Results and Analysis | oscilloscope capture(s) | 2 | £ | |
| e garage | Delay table | 3 | | |
| , | Delay graph | 4 | | 9 8 |
| Conclusion | | 3 | | |
| | Q1 | 3 | | |
| Questions | Q2 | 3 | | |
| | Q3 | 4 | -5 | |
| Writing Composition | | 3 | | |
| Total for prelab, demo, and report | | 100 | | |

