

Exercise 4: Combinational Logic Circuit Design Using Boolean Algebra Simplification

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Section: 3

Prelab		Point Value	Points Earned	Comments
Part 1	Table completed	3	3	2/2 HB
	Expressions correct	2	2	
Part 2	Correct simplified equations	1	1	2/2 HB
	Steps shown	2	0	
	Boolean properties shown	2	0	
Part 3	Correct schematic	3	3	2/2 HB
	Pin and chip numbers	1	1	2/2 HB
	Correct simulation	4	4	2/2 HB
	Parts placement diagram	2	2	2/2 HB

Demo		Point Value	Points Earned	Date
Demo	Two fully functional output pins	20	20	2/2 JKL
	Additional two fully functional output pins	20	20	

To receive any grading credit students must earn points for both the demonstration and the report.

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Report		Point Value	Points Earned	Comments
Abstract		5		
Design Methodology	Truth table	4		
	Boolean expressions	4		
	Correct simplified equations	1		
	Steps shown	2		
	Boolean properties shown	2		
	Schematic	4		
Results and Analysis	Discussion	5		
Conclusion		3		
Questions	EGC	3		
	Chip count	3		
Writing Composition		4		
Total for prelab, demo, and report		100		