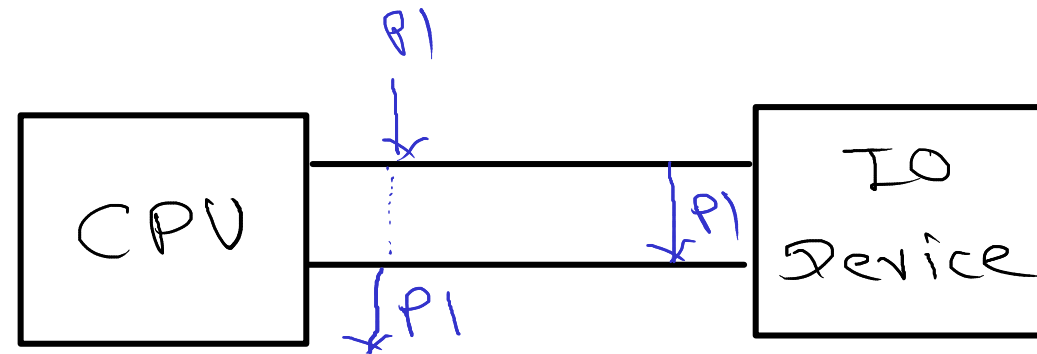


IO Techniques

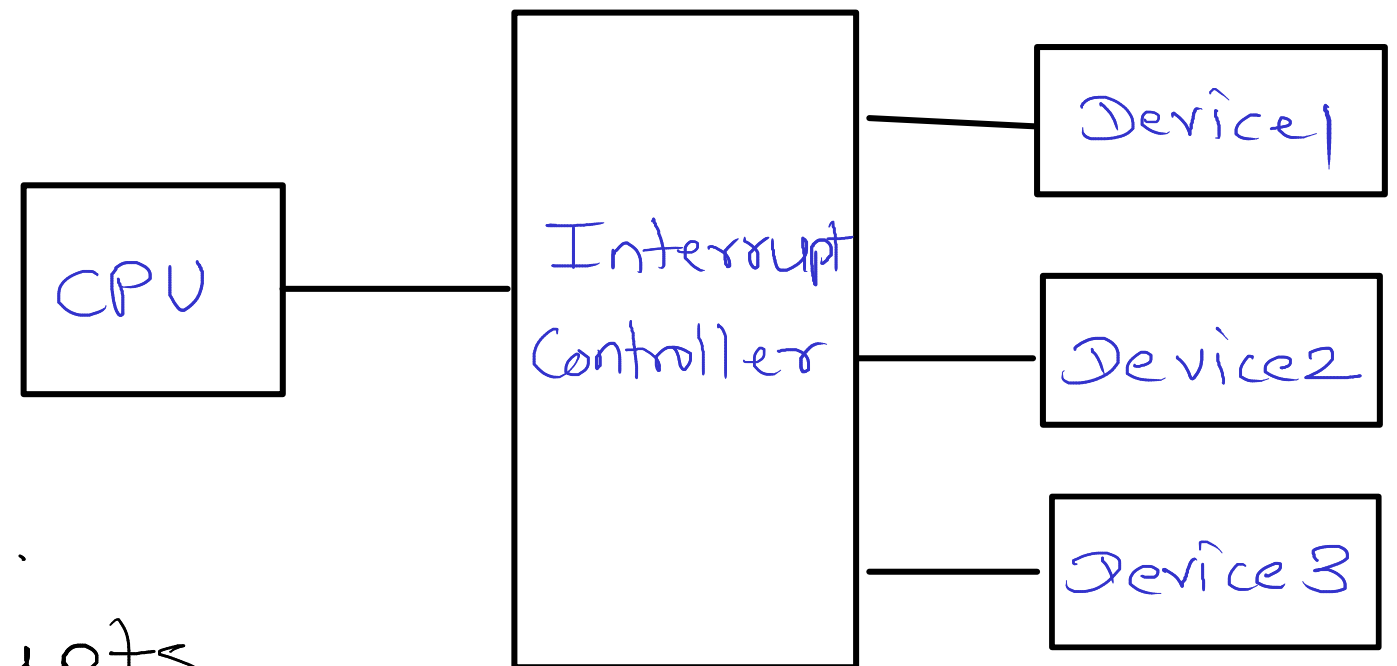
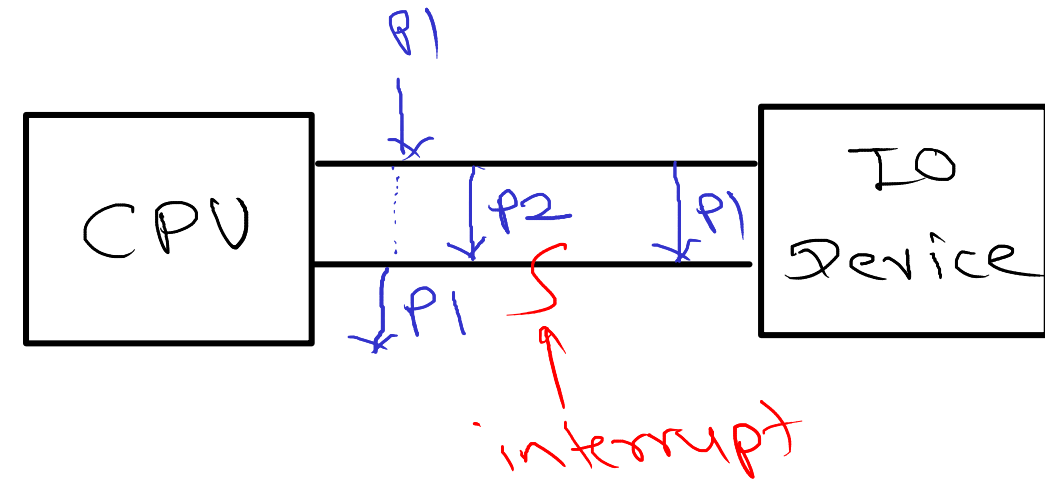
Synchronous IO

Hardware Technique : Polling



Asynchronous IO

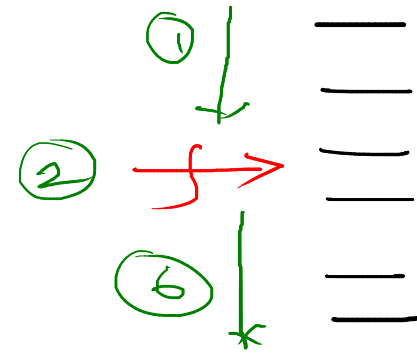
Hardware Technique : Interrupt



- inform interrupts to CPU.
- decide priority of interrupts

Interrupt Handling

Program:



interrupt_handler()

{

1) save execution context of current process (PCB)

2) find address of ISR from IVT.

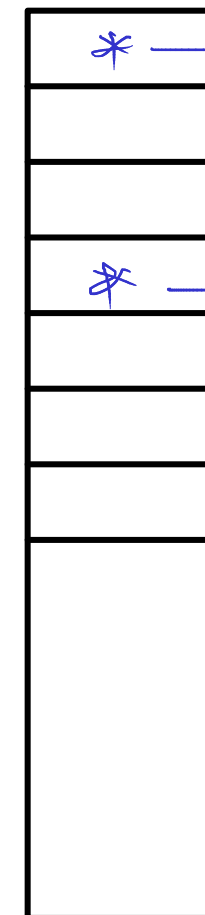
3) call ISR

4) Restore execution context of paused process.

}



IVT



ISR1()

ISR2()

→ few instructions & CPU registers are not accessible

Program:

① ↓
==
==
==
syscall_api(),
⑥ ↓
==
==
==

mode {
1 User mode / unprivileged
0 Kernel mode / system mode / privileged

mode 1

syscall_api() {

② ↓ - necessary setting
↓ - s/w interrupt

Dual Mode Operations

interrupt_handler()

{

1) save execution context of current process

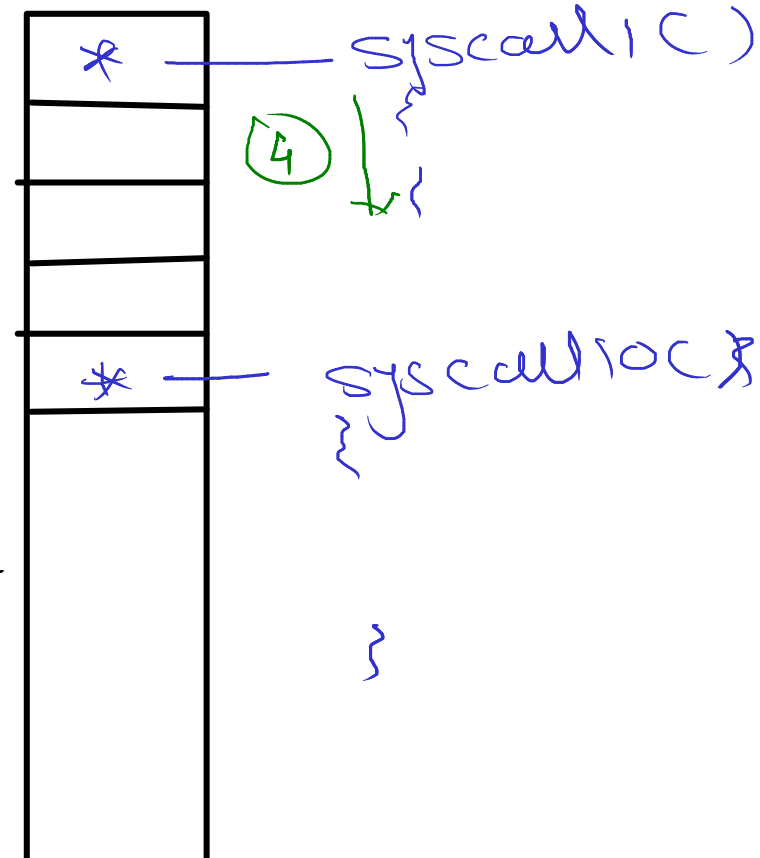
③ { 2) Find addr of actual system call implementation from system call table

3) call system call implⁿ

⑤ ↓ 4) Restore execution context of paused process.

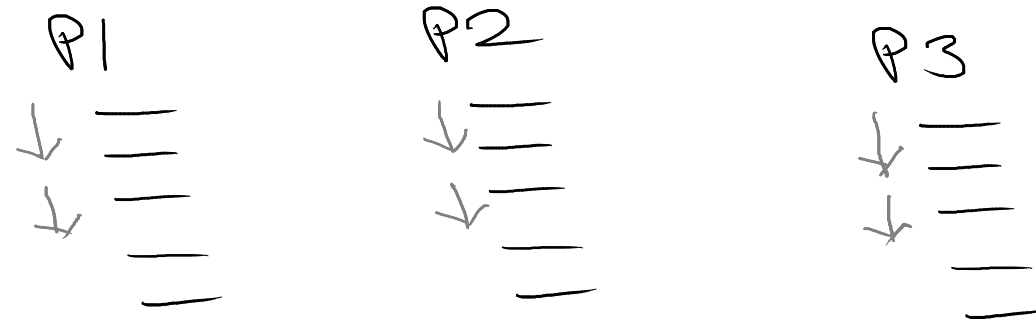
}

System
call
Table



mode 0

CPU Scheduler



```
interrupt_handler() {
```

```
{
```

```
1) save execution context
```

```
2) find ISR from IVT
```

```
3) call ISR ——— ISR()
```

```
4) pid = scheduler()
```

```
5) cpu_dispatcher(pid)
```

```
}
```

```
scheduler() {
```

```
{ if(remaining_time > 0)
```

```
  select same process
```

```
else
```

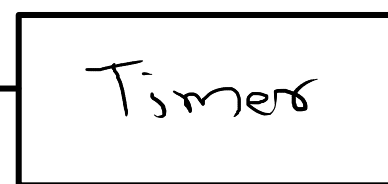
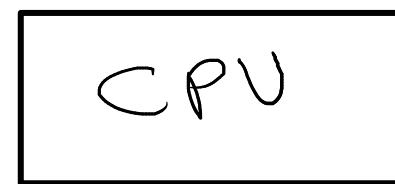
```
  select another process
```

```
  return pid;
```

```
} dispatcher(pid)
```

```
{ restore execution context of pid process
```

```
}
```



— generates interrupts periodically