# **Concurrent Programming**

# Exercise Booklet 9: Promela and Spin<sup>1</sup>

Solutions to selected exercises ( $\Diamond$ ) are provided at the end of this document. Important: You should first try solving them before looking at the solutions. You will otherwise learn **nothing**.

### 1 Basic Promela

Exercise 1. Do all interleavings in the following program ensure that x is always between 0 and 200? Encode it in Promela and use Spin to determine the answer.

Exercise 2. ( $\Diamond$ ) Implement the following entry/exit protocol (Attempt I) seen in class, in Promela. Use the Promela code in the slides as an aid in understanding Promela syntax.

```
int turn = 1;
Thread.start { // Q
  while (true) {
    await (turn==1);
    turn = 2;
  }
}
Thread.start { // Q
  while (true) {
    await (turn==2);
    turn = 1;
  }
}
```

Exercise 3. Implement the following entry/exit protocol (Attempt III) seen in class, in Promela. Use the Promela code in the slides as an aid in understanding Promela syntax.

```
boolean wantP = false;
   boolean wantQ = false;
   Thread.start { //P
                                       Thread.start { // Q
     while (true) {
                                         while (true) {
      // non-critical section
                                          // non-critical section
      wantP = true;
                                          wantQ = true;
      await (!wantQ);
                                          await (!wantP);
      // CRITICAL SECTION
                                          // CRITICAL SECTION
      wantP = false;
                                          wantQ = false;
      // non-critical section
                                          // non-critical section
10
11
   }
```

#### Exercise 4.

Draw the transition system of the following two programs separately and then compare them:

<sup>&</sup>lt;sup>1</sup>Sources include: http://www.cs.toronto.edu/~chechik/courses01/csc2108/lectures/spin.2up.pdf

```
% Program 1
                                % Program 2
     byte state = 1;
                                   byte state = 1;
     active proctype A(){
                                   active proctype A(){
3
                                       (state == 1) ->
       atomic {
          (state == 1) ->
                                         state = state+1
            state = state+1 6
                                   }
6
                                   active proctype B() {
7
                                      (state==1) ->
8
     active proctype B() { 9
                                         state = state-1
9
       atomic {
10
         (state == 1) ->
11
            state = state-1
13
       }
14
```

Exercise 5. Check whether the following algorithm guarantees mutual exclusion by adding an auxiliary variable critical and appropriate statements. Do you recognize this algorithm?

```
bool flag[2]
   bool turn
2
3
   active [2] proctype user()
5
6
       flag[_pid] = true
       turn = _pid
       (flag[1-_pid] == false || turn == 1-_pid)
                   // critical section
   crit:
           skip
10
11
       flag[_pid] = false
12
13
```

Exercise 6. What happens in the previous algorithm if you exchange the line flag[\_pid] = true with the line turn = \_pid?

Exercise 7. Consider the following simplified presentation of the Bakery Algorithm for two processes:

```
int np,nq =0;
   Thread.start { // P
                                  Thread.start { // Q
     while (true) {
                                    while (true) {
                                3
      // non-critical section 4
                                     // non-critical section
      np = nq + 1;
                                     nq = np + 1;
                                5
      await nq==0 or np<=nq;
                                     await np==0 or nq<np;
      // CRITICAL SECTION
                                     // CRITICAL SECTION
      np = 0;
                                     nq = 0;
      // non-critical section
                                9
                                     // non-critical section
10
                               10
                               11 }
11
```

- 1. Encode it in Promela.
- 2. We mentioned in the lectures that if assignment is not atomic, then mutual exclusion is not guaranteed. Verify this in spin. Since assignment is atomic in spin, you first must split line 5 (in P and Q) into two assignment operations.

- 3. Add the command np = 1 in thread P just before line 4 and add nq=1 in thread Q just before line 4. Show, using Spin, that the resulting program does enjoy mutual exclusion.
- 4. If you verify the code above (for mutex), where assignment is assumed atomic, Spin seems to report an assertion violation. Why? Hint: run the following code and bear in mind that the byte type has a range of 256 different values:

```
byte n=0;

active proctype P() {
   int i=0;
   for (i: 1..1000) {
      n++;
   }
}
```

Exercise 8. The following extension to 3 processes of Dekker's algorithm is known to deadlock.

```
int turn=0
   flags = [false, false, false]
3
   3.times {
4
       int id = it
5
6
       Thread.start {
          int left = (id+2)%3;
          int right = (id+1)%3;
          while (true) {
            flags[id] = true;
10
            while (flags[left] || flags[right])
11
              if (turn == left) {
12
                flags[id] = false;
13
                await (turn==id);
14
                flags[id] = true;
15
16
            }
17
            // Critical Section
18
            turn = right;
19
            flags[id]=false;
20
         }
21
   }
22
```

Encode it in Promela and verify where it deadlocks (there are multiple traces that lead to deadlock, exhibiting one suffices). Here is some code that you can start from.

```
byte turn;
   bool flags[3];
2
3
4
   proctype P() {
     byte myId = _pid-1;
6
     /* complete here */
7
8
9
   init {
10
     turn=0;
11
     byte i;
12
13
     for(i:0..2) { flags[i] = false; }
14
       for (i:0..2) { run P(); }
15
```

```
16 }
17 }
```

## Exercise 9. $(\lozenge)$

- 1. Define a general semaphore with operations acquire(sem) and release(sem), where sem is a numeric shared global variable. Since there are no functions in Promela, you can declare macros using inline. Hint: use atomic and blocking expressions.
- 2. Check that your solution is correct by providing an example of its use.

Exercise 10. Implement the Bar exercise from eb5, in Promela, using the semaphore encodings from Exercise. 9. Use Spin to show that the required invariant is upheld, i.e. that only one Jets fan goes in for every two Patriot fans, by inserting an appropriate assertion. In your example use 20 Jets fans and 20 Patriot fans. Here is a stub:

```
byte ticket = 0;
   byte mutex = 1;
   /* additional declarations here */
5
6
   inline acquire(sem) {
     atomic {
9
        sem > 0 -> sem --
10
11
   }
12
13
   inline release(sem) {
14
     sem++
15
   }
16
17
   active [20] proctype Jets() {
18
     /* complete */
19
   }
20
21
22
   active [20] proctype Patriots() {
23
     /* complete */
24
   }
```

Exercise 11. Implement the Bar exercise from eb6 (monitors!), in Promela, using the semaphore encodings from Exercise. 9. Use Spin to show that the required invariant is upheld, i.e. that only one Jets fan goes in for every two Patriot fans, by inserting an appropriate assertion. Here is a stub:

```
int N=4;

inline jets() {
    // complete
}

inline patriots() {
    // complete
}
```

```
11
   proctype Jets() {
12
13
      jets();
14
15
16
   proctype Patriots() {
17
18
      patriots();
19
20
21
   init {
22
     int i;
23
     for (i: 1 .. N) {
24
        atomic {
25
          run Jets();
          run Patriots()
27
        }
28
     }
29
   }
30
```

Exercise 12. Implement the Car Wash exercise from eb5, in Promela, using the semaphore encodings from Exercise. 9. Use Spin to show that there is at most one car in any of the three stations. Here is a stub:

```
#define N 3 /* Number of Washing Machines */
   #define C 10 /* Number of Washing Machines */
2
3
   byte permToProcess[N]
   byte doneProcessing[N]
5
   byte station0 = 1
   byte station1 = 1
   byte station2 = 1
10
   inline acquire(s) {
11
    atomic {
^{12}
     s>0 -> s--
13
    }
14
15
16
   inline release(s) {
17
   s++
18
   }
19
20
   proctype Car() {
21
     /* complete */
22
23
24
25
   proctype Machine(int i) {
26
     /* complete *
27
28
29
   }
30
31
   init {
32
   byte i;
33
```

```
for (i:0..(N-1)) {
35
         permToProcess[i] = 0;
36
         doneProcessing[i]=0;
37
38
39
    atomic {
40
      for (i:1..(C)) {
41
42
        run Car();
43
44
      for (i:0..(N-1)) {
45
         run Machine(i);
46
    }
47
   }
48
```

## 2 Channels

Exercise 13.  $(\lozenge)$  What does the following program print?

```
proctype A(chan q1) {
     chan q2;
     q1?q2;
3
     q2!123
4
   }
5
   proctype B(chan qforb) {
6
     int x;
     qforb?x;
8
     printf("x=%d\n",x)
9
   }
10
   init {
11
     chan qname = [1] of { chan };
12
     chan qforb = [1] of { int };
13
     run A(qname);
14
     run B(qforb);
15
     qname!qforb
16
17
```

Exercise 14. ( $\Diamond$ ) Implement a binary semaphore in Promela using message passing and synchronous communication. You should have two proctypes, semaphore and user. Here is the code for the user:

```
#define acquire 0
2
   #define release 1
   chan sema = [0] of { bit }; /* synchronous channel */
3
  proctype semaphore() {
     /* complete this */
5
  }
6
  proctype user() {
8
     :: sema?acquire;
9
        /* crit. sect */
10
        sema!release;
11
        /* non-crit. sect. */
12
13
     o d
  }
14
  init {
```

```
run semaphore();
run user();
run user();
}
```

Exercise 15. Implement the turnstile example from class using channels. Here is some code to get you started:

```
mtype { bump , read };
   chan counter = [0] of { mtype, chan }
   chan reply[2] = [0] of { byte }
   proctype Counter() {
5
      /* complete */
6
7
   proctype Turnstile() {
9
    /* complete */
10
11
12
   init {
13
     byte x;
14
     atomic {
15
16
      run Counter();
       run Turnstile();
17
       run Turnstile();
18
     }
19
     _nr_pr==2;
20
     counter!read(reply[0]);
21
22
     reply[0]?x;
     printf("%d\n",x) /* should print 20 */
23
   }
24
```

### 3 Solutions to Selected Exercises

#### Answer to exercise 2

```
byte turn=1;
2
   active proctype P() {
4
5
        :: do
6
              :: turn==1 -> break;
              :: else
7
            od;
            printf("P went in \n");
9
            turn = 2
10
     od
11
   }
12
13
   active proctype Q() {
14
     dο
15
       :: do
16
              :: turn==2 -> break;
17
18
              :: else
19
          printf("Q went in \n");
20
```

The following variation is acceptable but no quite right since the "await" does busy-waiting, it does not block:

```
byte turn=1;
2
   active proctype P() {
4
       :: turn==1;
5
          printf("P went in \n");
6
          turn = 2
7
     o d
8
   }
9
10
   active proctype Q() {
11
12
       :: turn==2;
13
          printf("Q went in \n");
14
          turn = 1
16
          od
   }
17
```

#### Answer to exercise 9

```
inline acquire(sem) {
   atomic {
    sem>0;
    sem --
   }
}

inline release(sem) {
   sem++
   }
}
```

### Answer to exercise 13

- Init sends qforb to A on quame
- A sends 123 to qforb
- B receives 123 on qforb and prints it

#### Answer to exercise 14

```
#define acquire 0
  #define release 1
  chan sema = [0] of { bit };
  proctype semaphore() {
4
    byte count = 1;
5
     dο
6
     :: (count == 1) -> sema!acquire; count = 0
     :: (count == 0) -> sema?release; count = 1
9
     o d
  }
10
11
  proctype user() {
12
   do
```

```
:: sema?acquire;
14
    /* crit. sect */
15
       sema!release;
16
       /* non-crit. sect. */
17
18
19 }
20 init {
   run semaphore();
21
   run user();
run user();
22
23
24 }
```