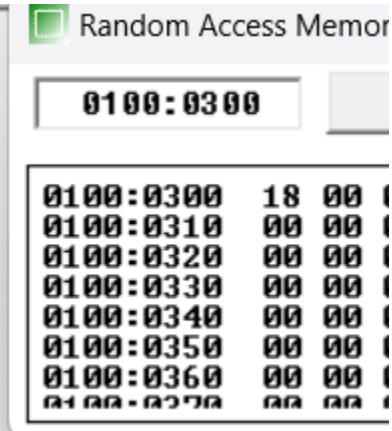
Q1

```
MOV AX,04H
MOV BX,AX

DECC:
  DEC BX
  CMP BX,00H
  JNE FACTORIAL
  JE MEMORY

FACTORIAL:
     MUL BX
     JMP DECC

MEMORY:
     MOV [0300H],AX
```

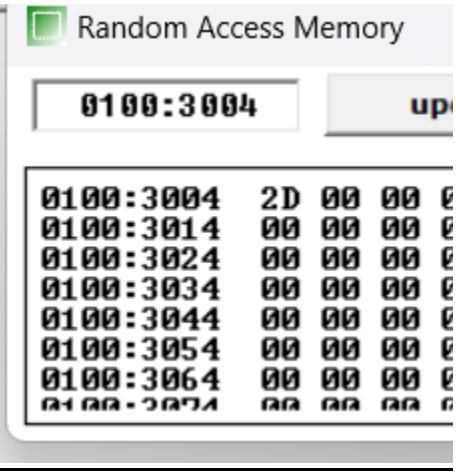


Q2(a)

```
MOV AX,9H
MOV BX,9H

START:
CMP BX,0H
JNE ADDITION
JE END

ADDITION:

DEC BX
ADD AX,BX
JMP START

END:
MOV [3002H],AX
```

Q2(b)

```
MOV AX,9H
MOV BX,9H
MOV CX,AX

START:
DEC BX
ADD AX,BX
LOOP START

END:
MOV [3004H],AX
```



Q3

```
MOV AX,09H
MOV BX,05H
MOV CX,07H

CMP AX,BX
JG GREATER
JL LESSER

GREATER:
CMP AX,CX
JG GR
JL GRRR

LESSER:
CMP BX ,CX
JG GRR
JL GRRR

GR:
MOV [3006H],AX
HLT

GRR:
MOV [3006H],BX
HLT

GRRR:
MOV [3006H],CX
HLT
```