- ADDLW
- ANDLW
- DAW
- SUBLW
- XORLW
- MULLW
- ADDWF   fileReg, d
- ADDWFC   fileReg, d
- ANDWF   fileReg, d
- IORWF   fileReg, d
- SUBFWB   fileReg, d
- SUBWF   fileReg, d
- SUBWFB   fileReg, d
- XORWF   fileReg, d
- COMF   fileReg, d
- DECF   fileReg, d
- DECFSZ   fileReg, d
- DECFSNZ fileReg, d
- INCF   fileReg, d
- INCFSZ   fileReg, d
- INCSNZ   fileReg, d
- MOVF   fileReg, d
- NEGF   fileReg, d
- RLCF   fileReg, d
- RLNCF   fileReg, d
- RRCF   fileReg, d
- RRNCF   fileReg, d
- SWAPF   fileReg, d
- BTG   fileReg, d
- IORLW
- MOVFF
- BC
- BNC
- BZ
- BNZ
- BN
- BNC
- BOV
- BNOV
- BRA
- GOTO
- CALL
- RCALL
- RETURN
- NOP
- CLRF
- SETF
- BCF   fileReg, bit
- BSF   fileReg,bit

- BTFSS   fileReg,bit
- BTFSC   fileReg,bit
- BTG   fileReg,bit
- STATUS
- C - Carry flag
- DC - Digital Carry flag
- Z - Zero flag
- OV - Overflow flag
- N - Negative flag
- CPFSGT
- CPFSEQ
- CPFSLT
- EQU
- ORG

- TMR0H/TMR1H
- TMR0L/TMR1L
- TMR0IF/TMR1IF
- INTCON
- PIR1

T0CON:-

- TMR0ON   D7   Timer0 ON and OFF control bit
- T08BIT   D6   Timer0 8-bit/16-bit selector bit
- T0CS   D5   Timer0 clock source select bit
- T0SE   D4   Timer0 source edge select bit
- PSA   D3   Timer0 prescaler assignment bit
- T0PS2:T0PS0   D2D1D0 Timer0 prescaler selector

T1CON:-

- RD16   D7   16-bit read/write enable bit
- TICKPS2:T1CKPS0 D5D4 Timer1 prescaler selector
- T1OSCEN   D3   Timer1 oscillator enable bit
- TISYNC   D2   Timer1 synchronization
- TMRICS   D1   Timer1 clock source select bit
- TMRION   DO   Timer1 ON and OFF control bit