Microprocessor Interfacing and Programming (EE3002)
Date: September 23rd 2024
Course Instructor(s)
Mr. Maaz Rizvi

Sessional-I Exam
Total Time (Hrs): 1
Total Marks: **15**
Total Questions: **3**

Roll No _____   Section _____   Student Signature _____

Do not write below this line
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Attempt all questions
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Question No. 1 (CLO No. 1)   Marks: 5

Give the code using 8086 Instruction Set to swap even and odd bits of the number **56H** and store it in a new register

Question No. 2 (CLO No. 1)   Marks: 5

Give Memory mapping and draw the chip select mechanism for interfacing **256kB** of RAM and **128kB** of ROM with the 8086 processor. Also, specify the following for both RAM and ROM.   18   17

1) No. of Address Lines 18/17   8   8
2) No. of Data Lines   2   1
3) Control Lines

Question No. 3 (CLO No. 1)   Marks: 5

Explain the pipelining effect in the 8086 processor.