Figure 1. Test for stalling where the PC is delayed at 0x010 until the operation using register 9 has gone through the WB phase
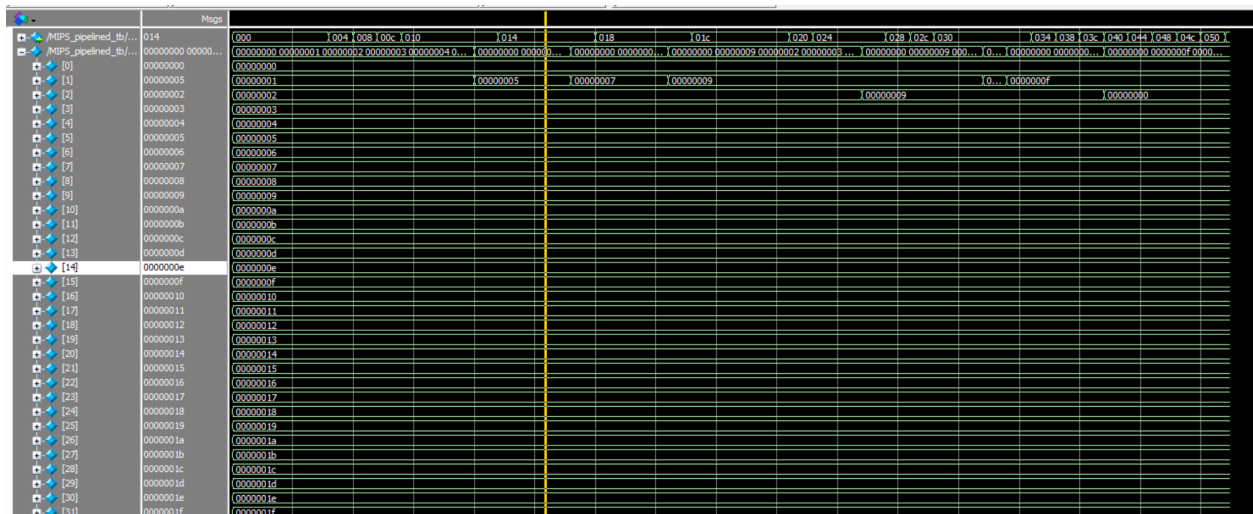

Figure 2. Test for forwarding where multiple calculated values are forwarded to the next instruction where needed
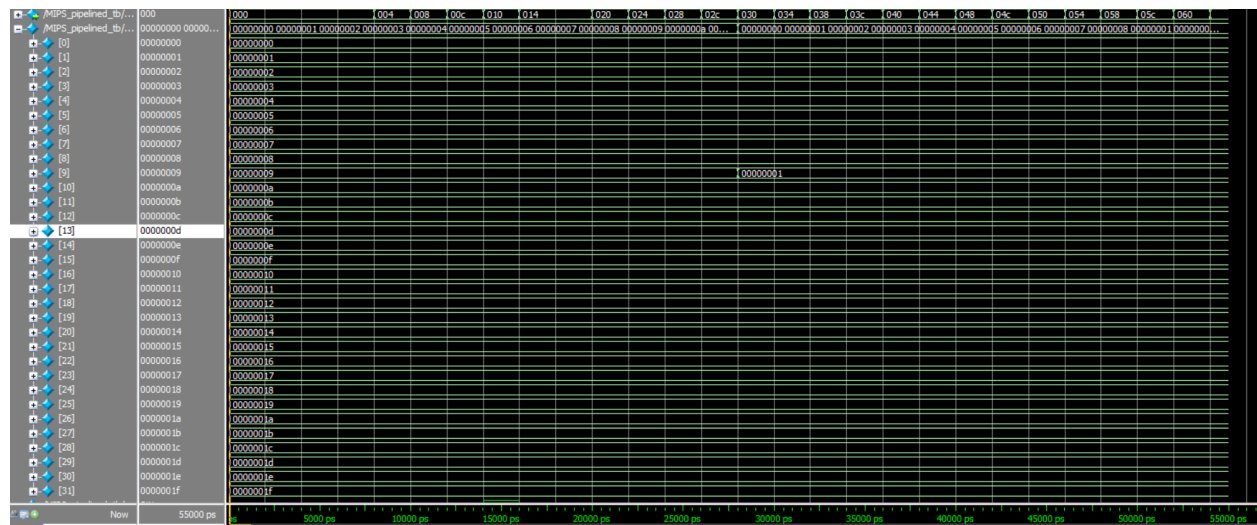
Figure 3. Stall test where 3 instructions are flushed when the beq operation is true. See commented //Stall test in fetch to see exactly which instructions are skipped.