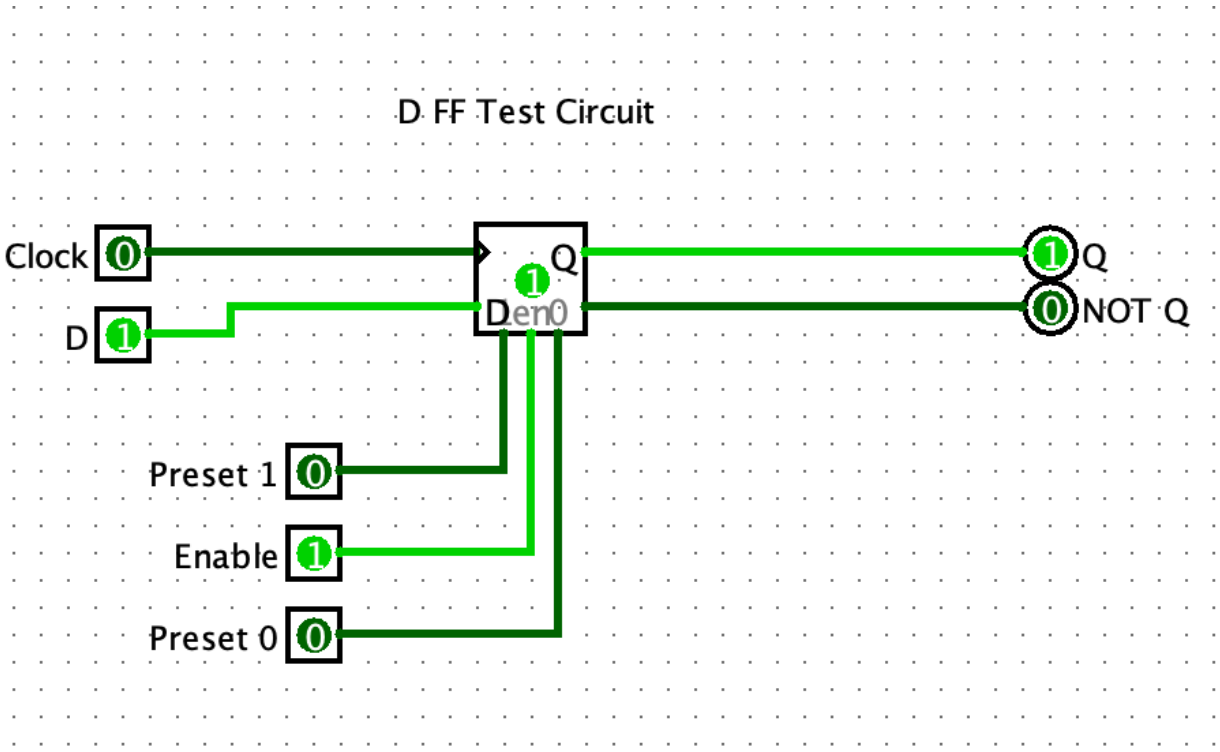
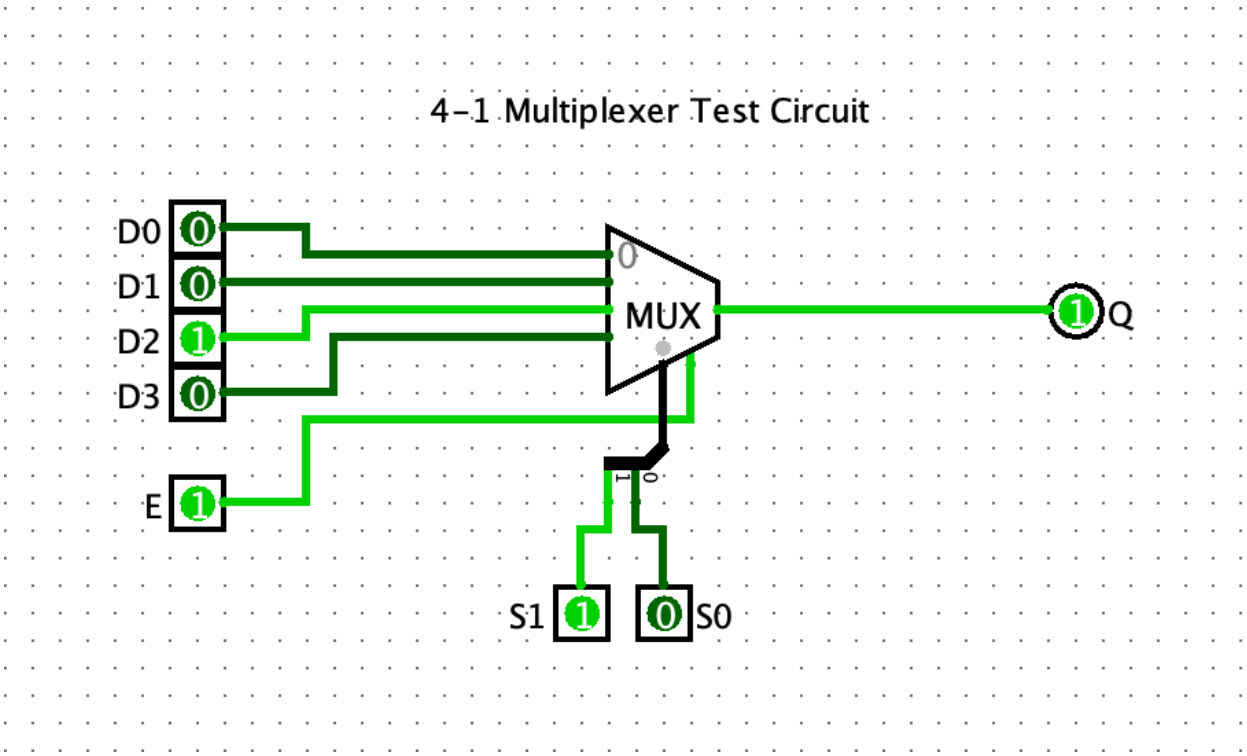
Lab 1
Owen Monus
CS 301
200482797
Jan 13, 2024

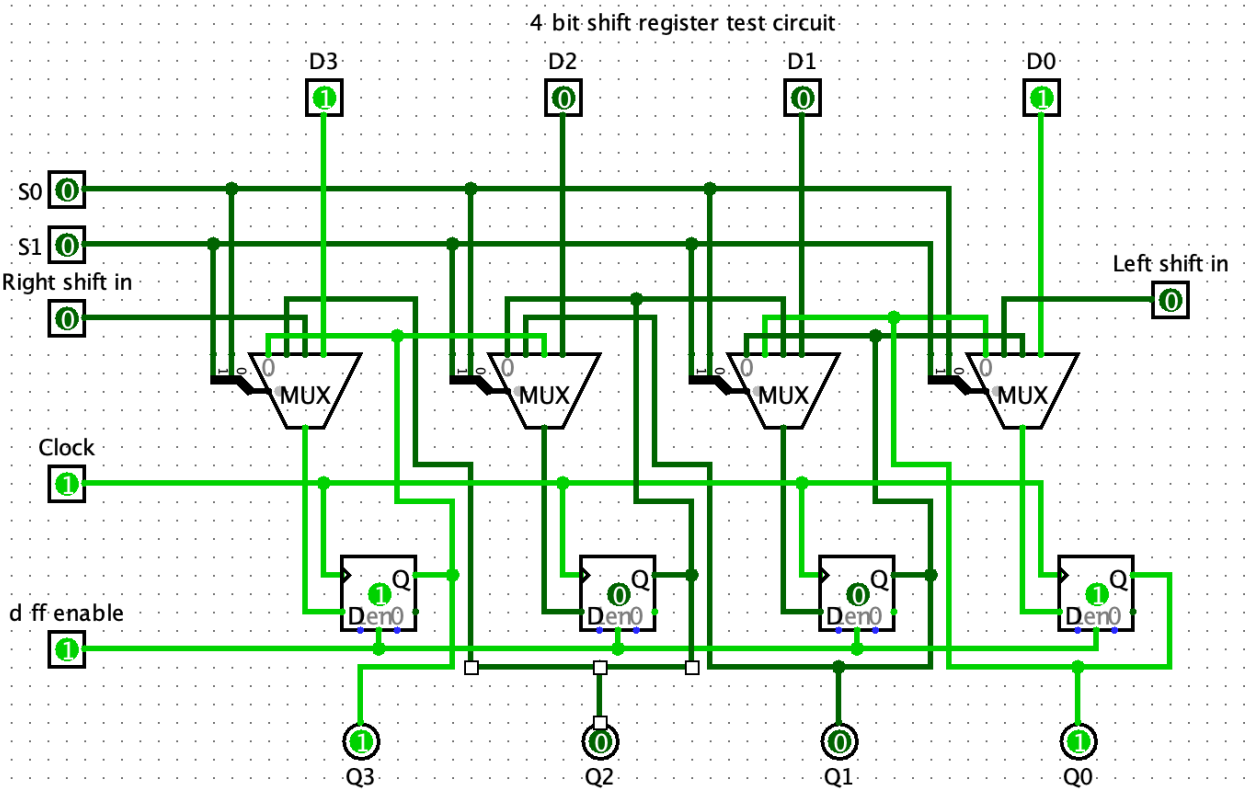
testing circuit of the one bit D-flip-flop



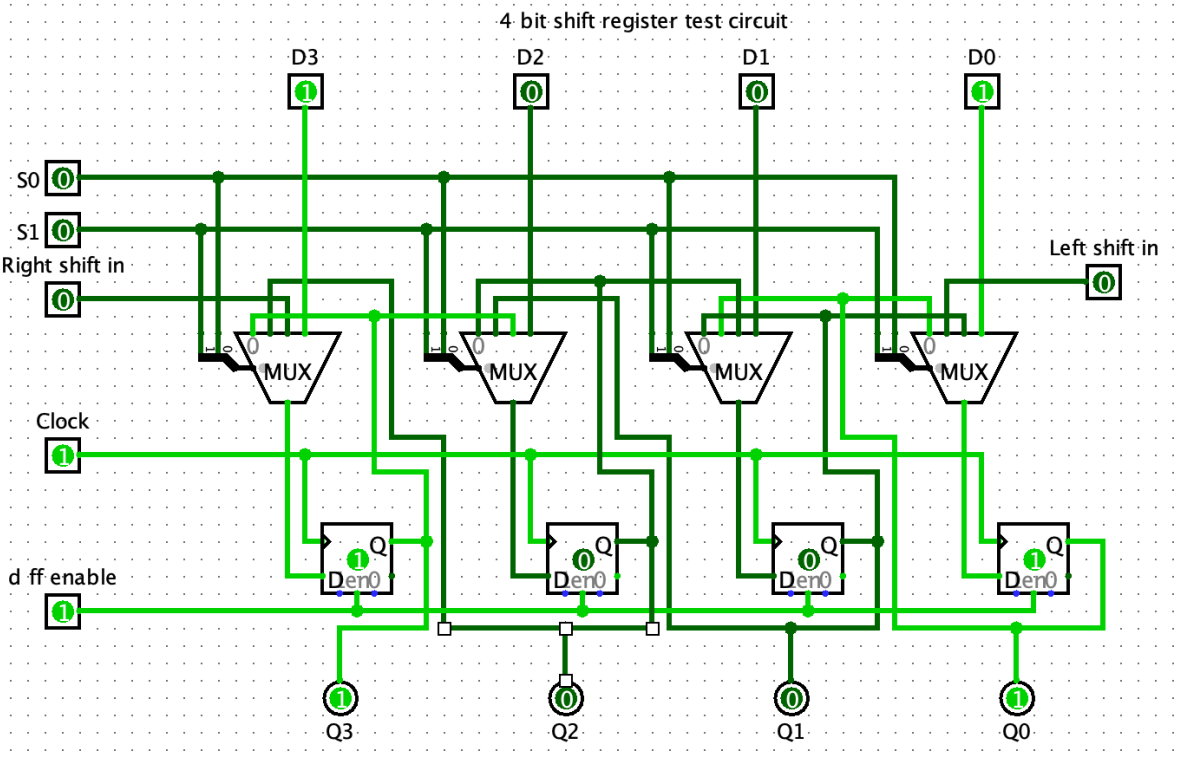
testing circuit of the one bit 4-1 Multiplexer



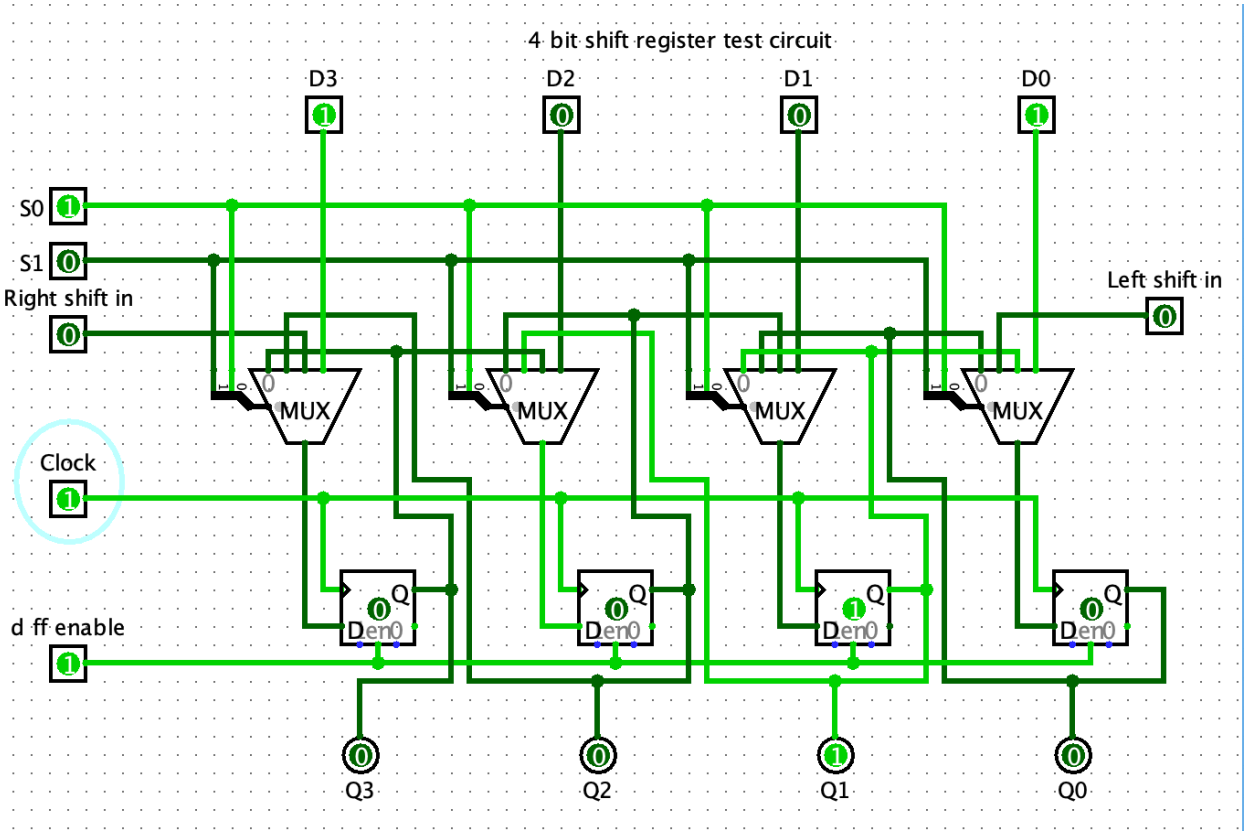
circuit implementing the 4-bit shift register design



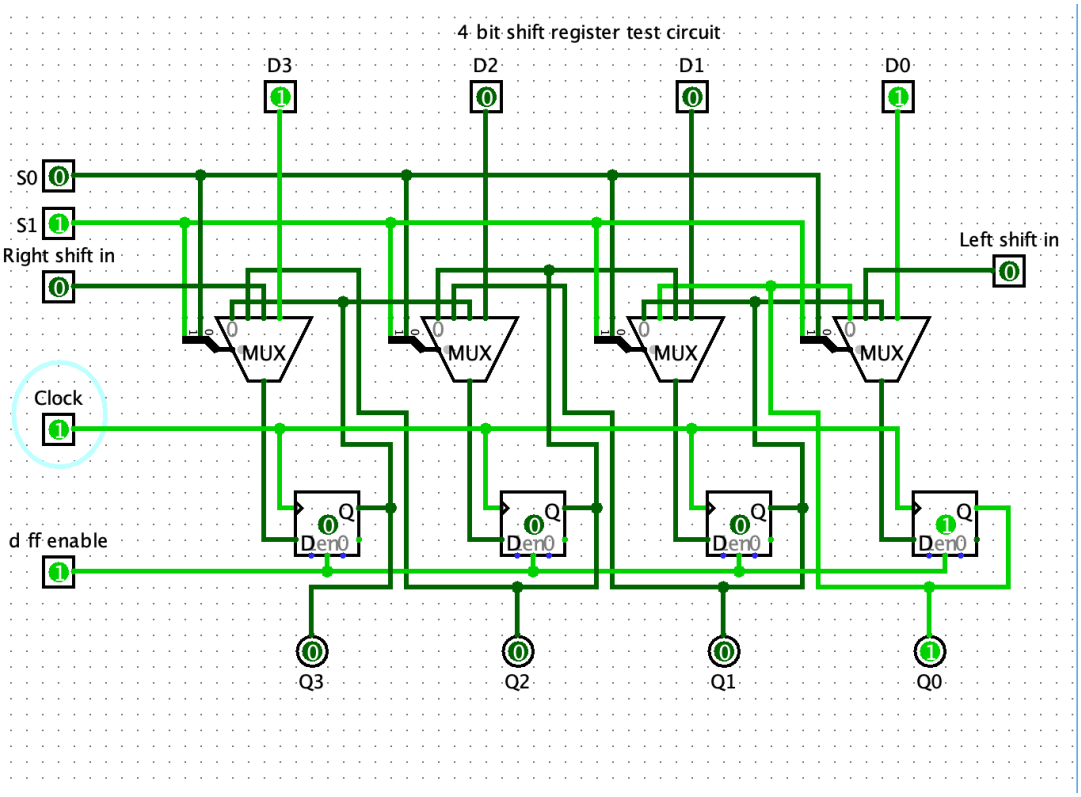
Store 0,0



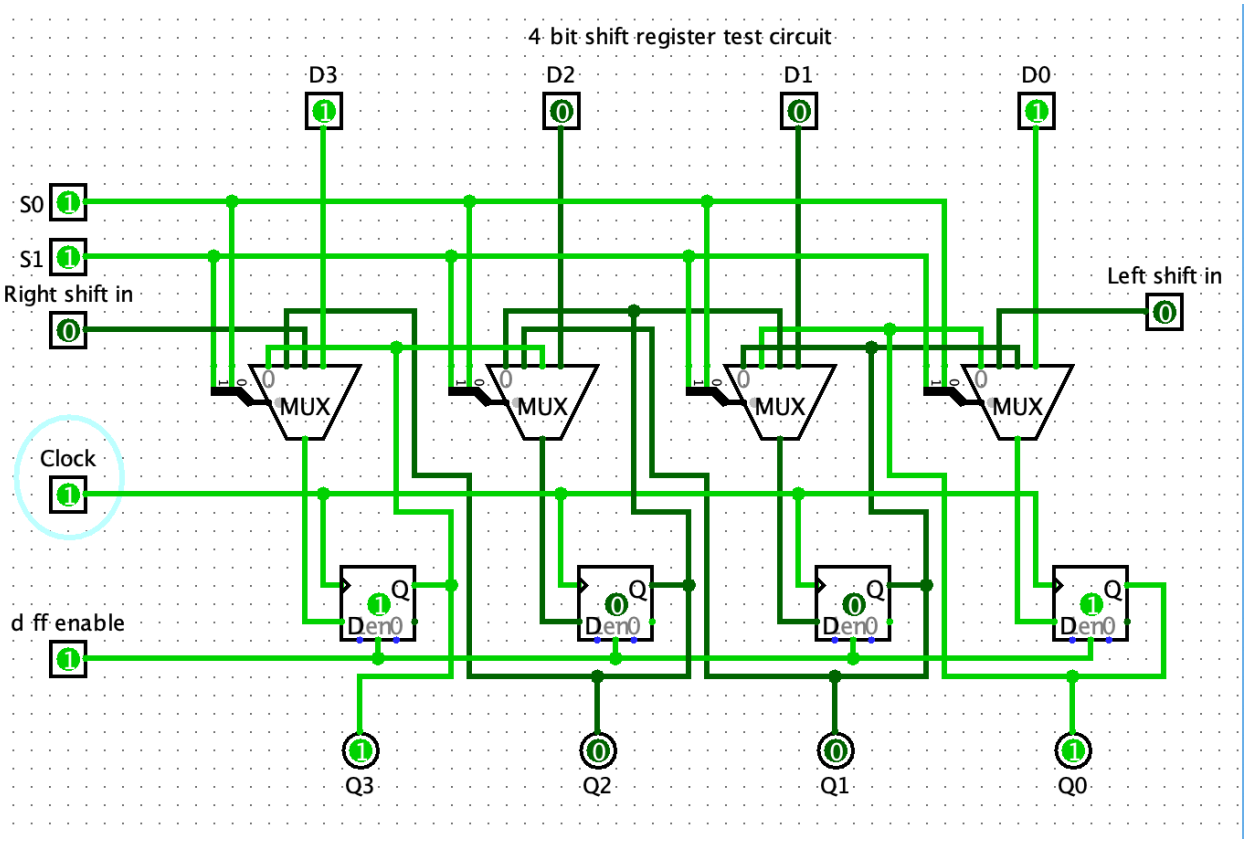
Shift left 0,1



Shift right, 1,0



Load 1,1


4 bit shift register test circuit
D3
D2
D1
D0
S0
S1
Right shift in
Left shift in
MUX
MUX
MUX
MUX
Clock
d ff enable
Q
Den0
Q3
Q2
Q1
Q0