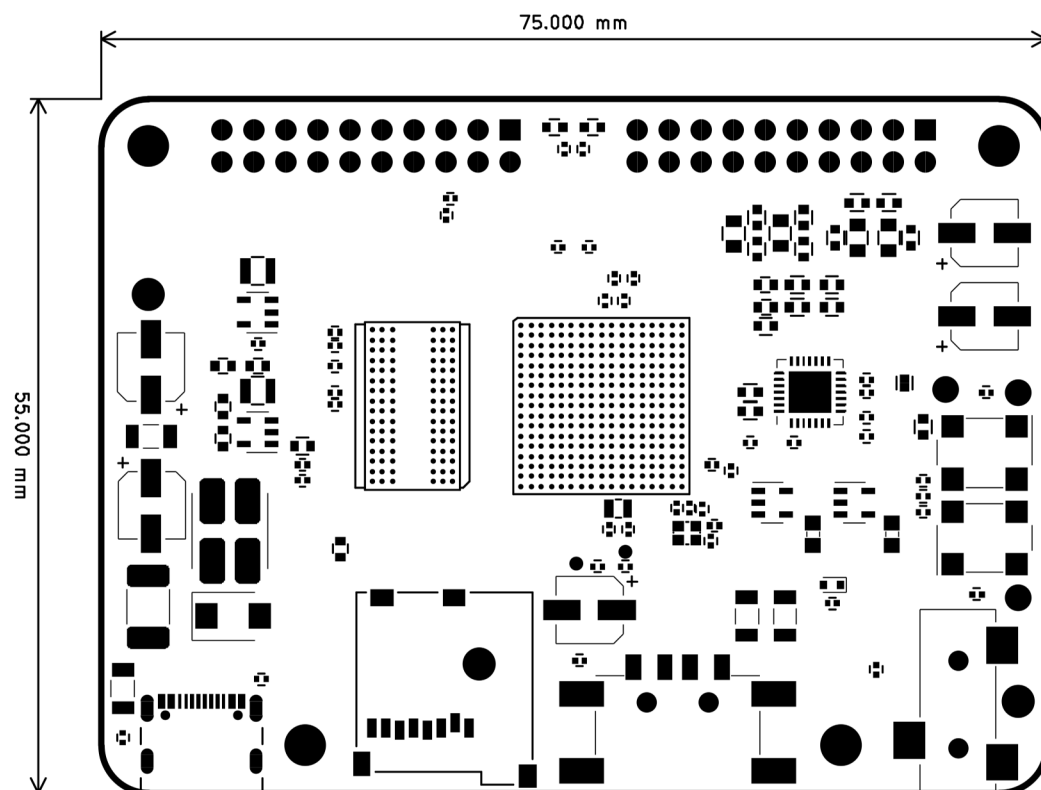


# Organelle 2 Processor Board



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3	PWR MANAGEMENT
4	CPU POWER
5	DRAM
6	CPU PERI 2
7	STORAGE
8	CPU PERI 1
9	AUDIO
10	CONNECTORS

Sheet: Block Diagram

File: EX-PCB-10108-001\_blockdiagram.sch

Sheet: Revision History

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EX-PCB-10108-001 - Schematics

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Sheet: /

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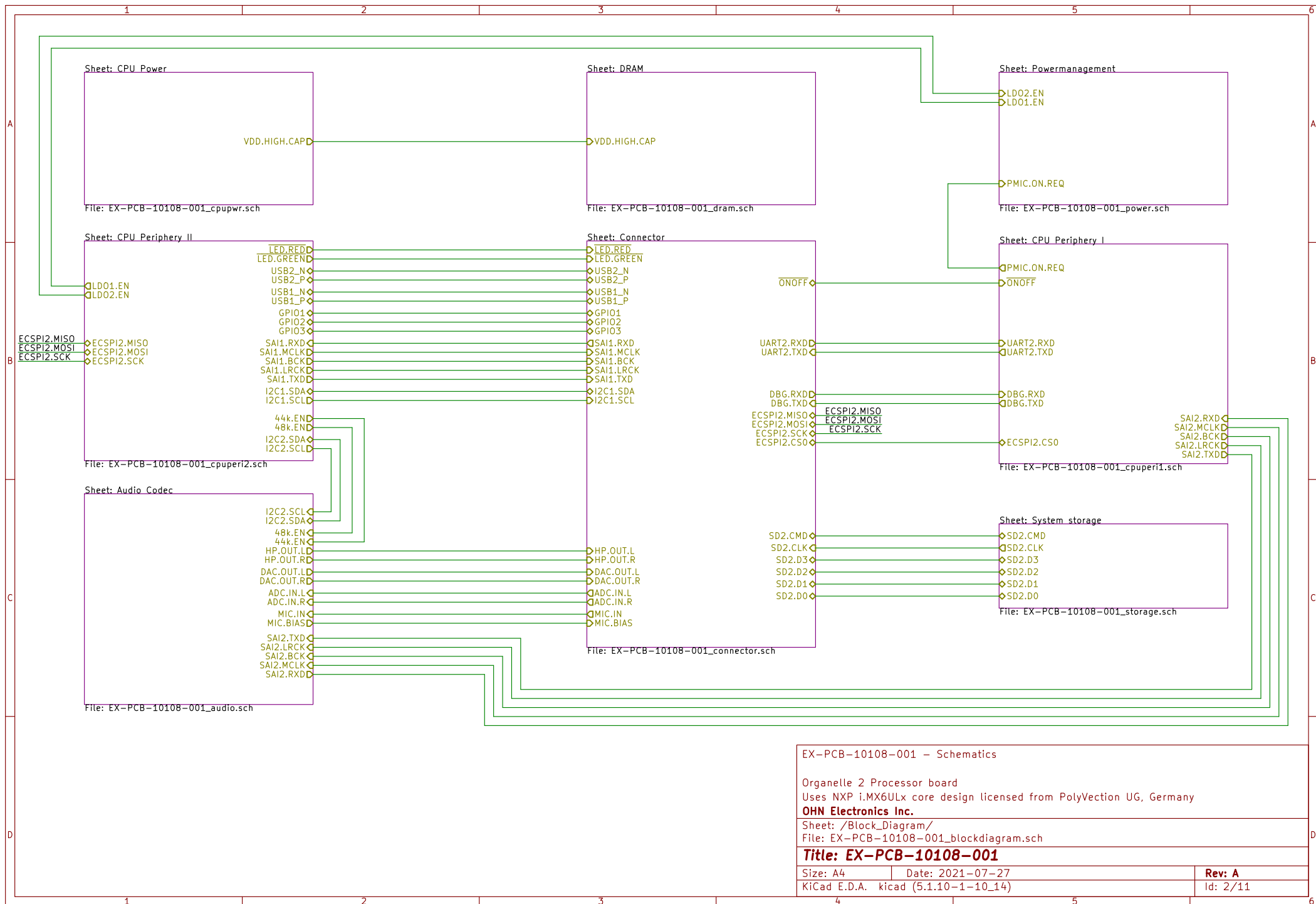
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Size: A4 Date: 2021-07-27

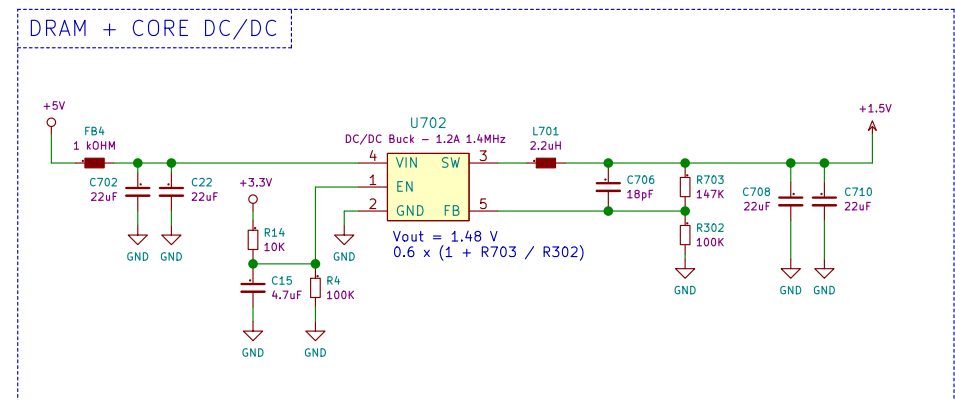
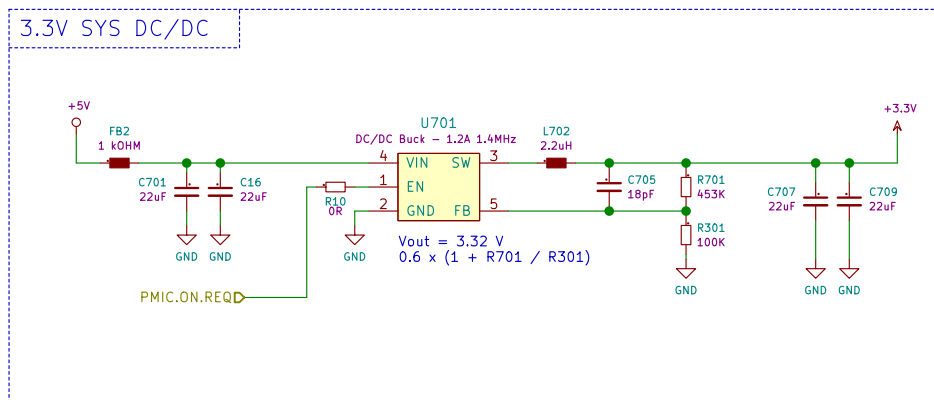
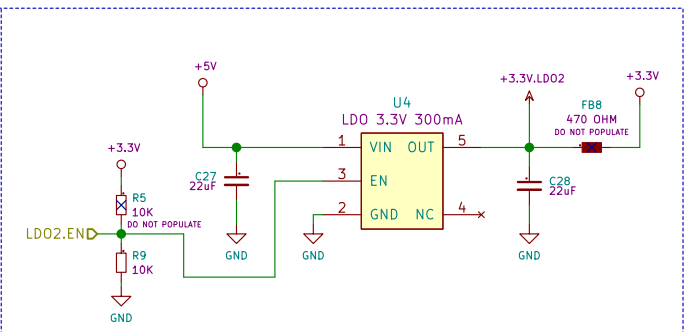
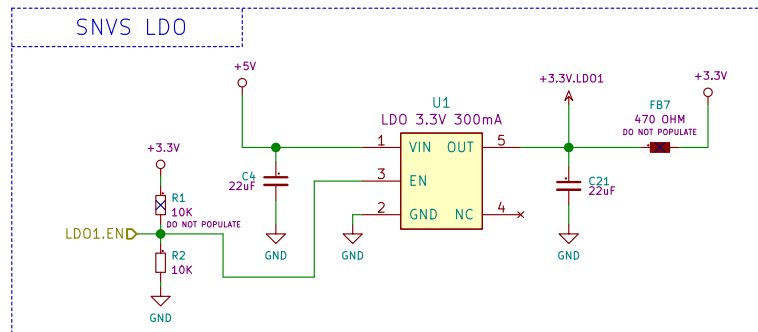
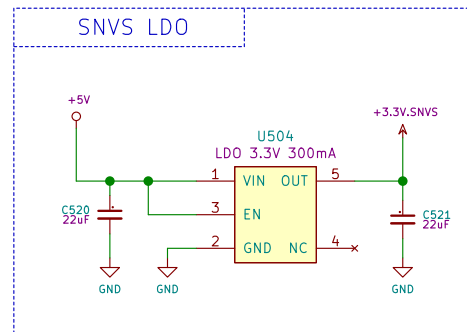
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**Rev: A**

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Size: A4	Date: 2021-07-27	Rev: A
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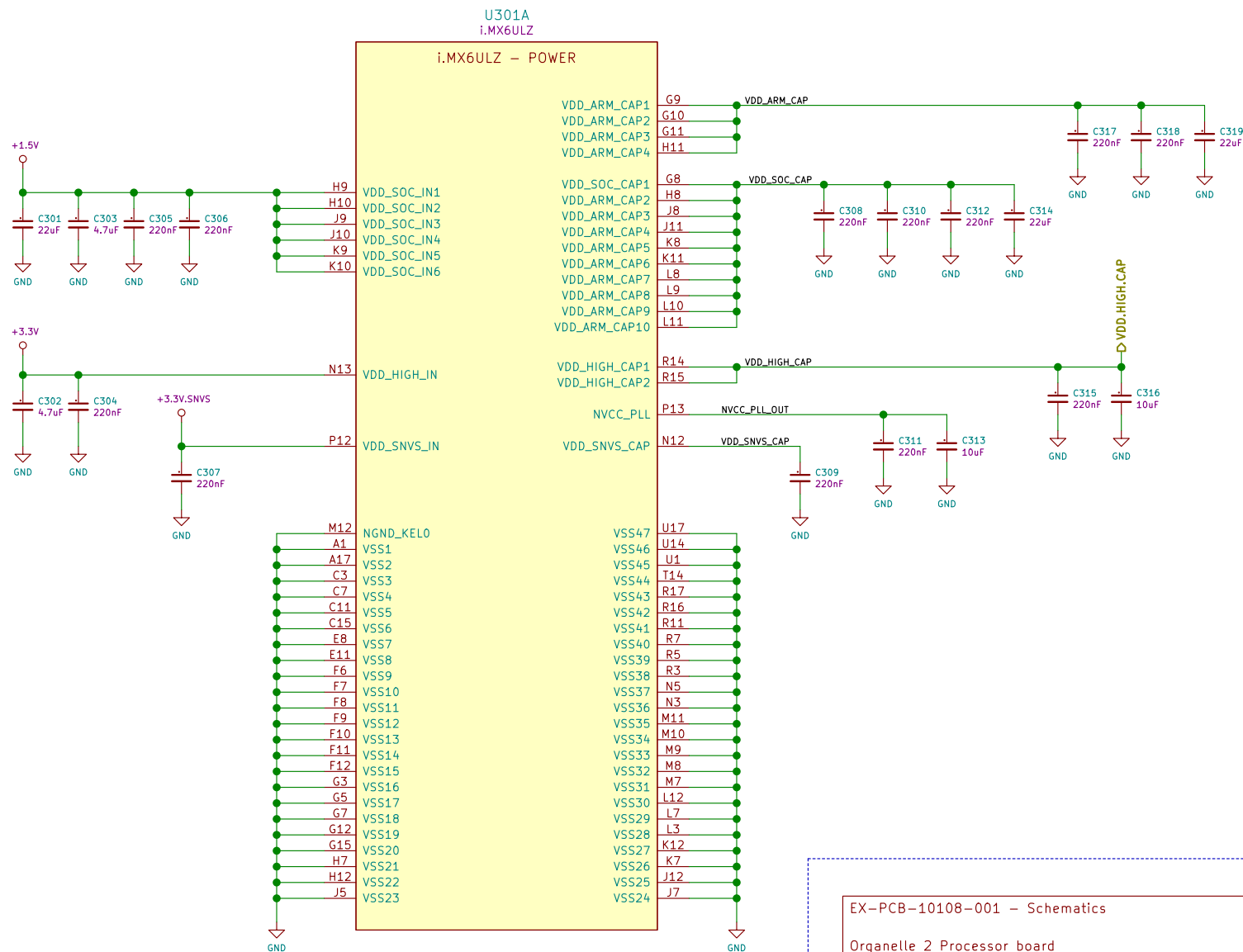
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**Rev: A**  
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# i.MX6ULZ – CPU PWR



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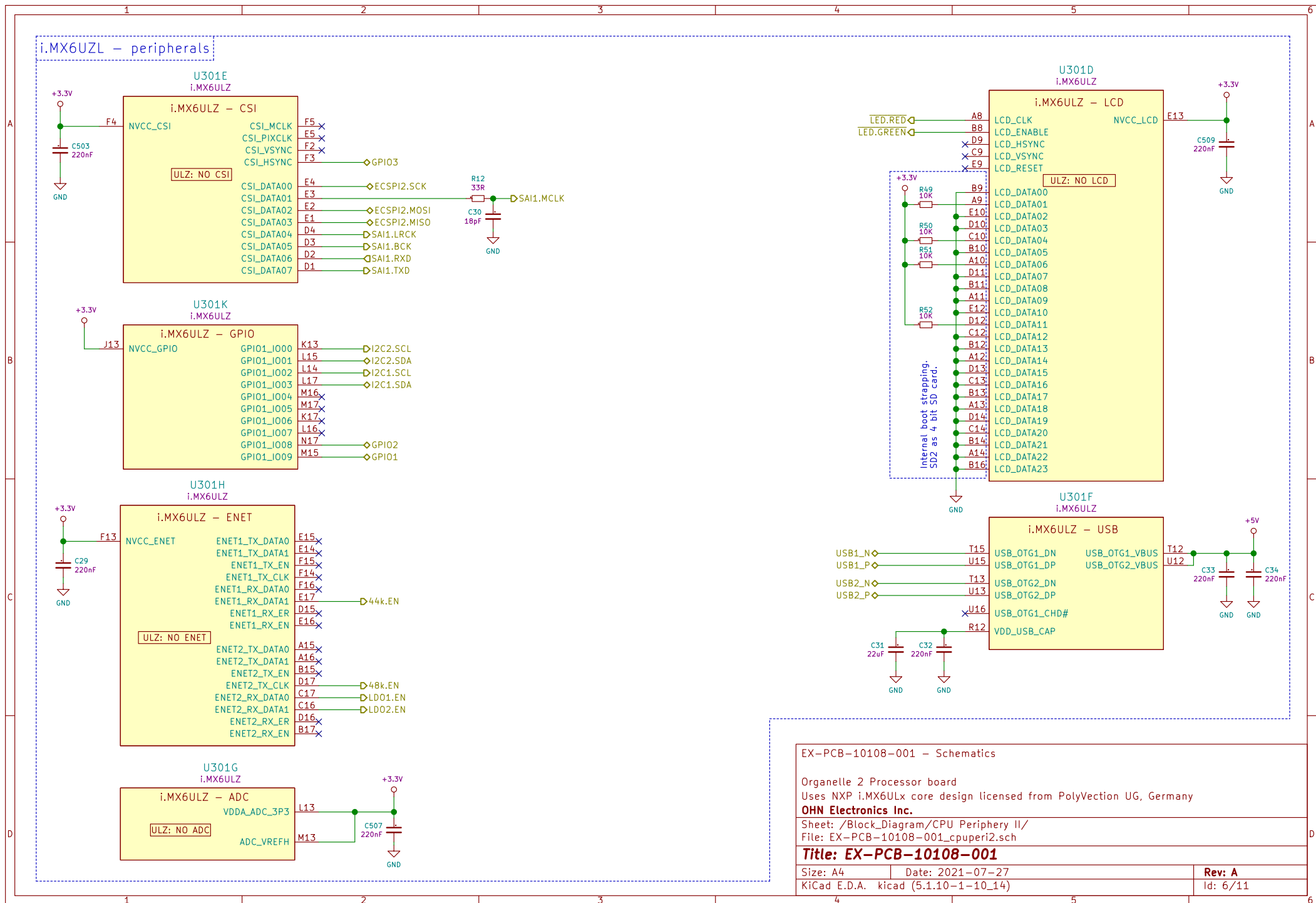
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KiCad E.D.A. kicad (5.1.10-1-10\_14)

Rev: A  
Id: 4/11





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Sheet: /Block\_Diagram/CPU Periphery II/

File: EX-PCB-10108-001\_cpuperi2.sch

**Title: EX-PCB-10108-001**

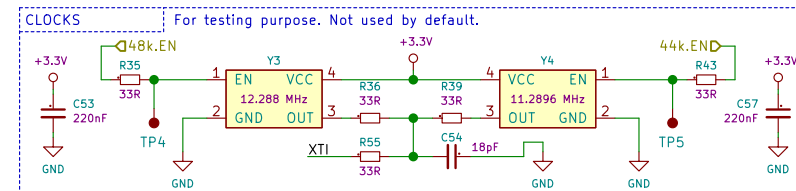
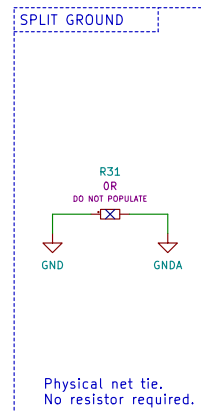
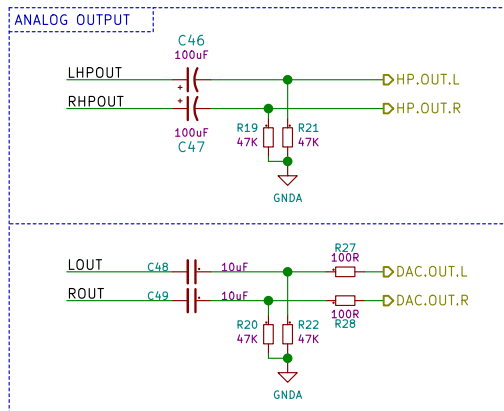
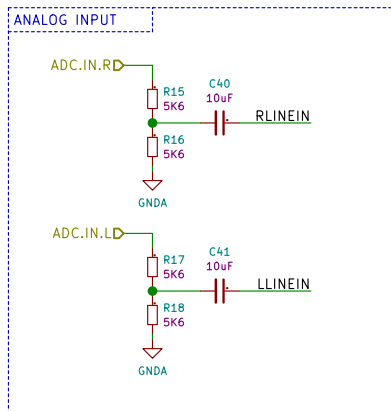
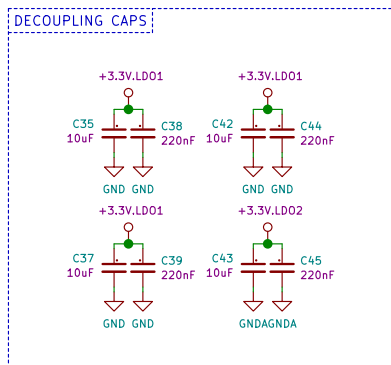
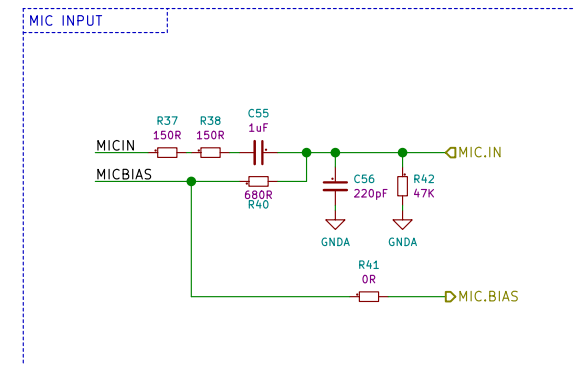
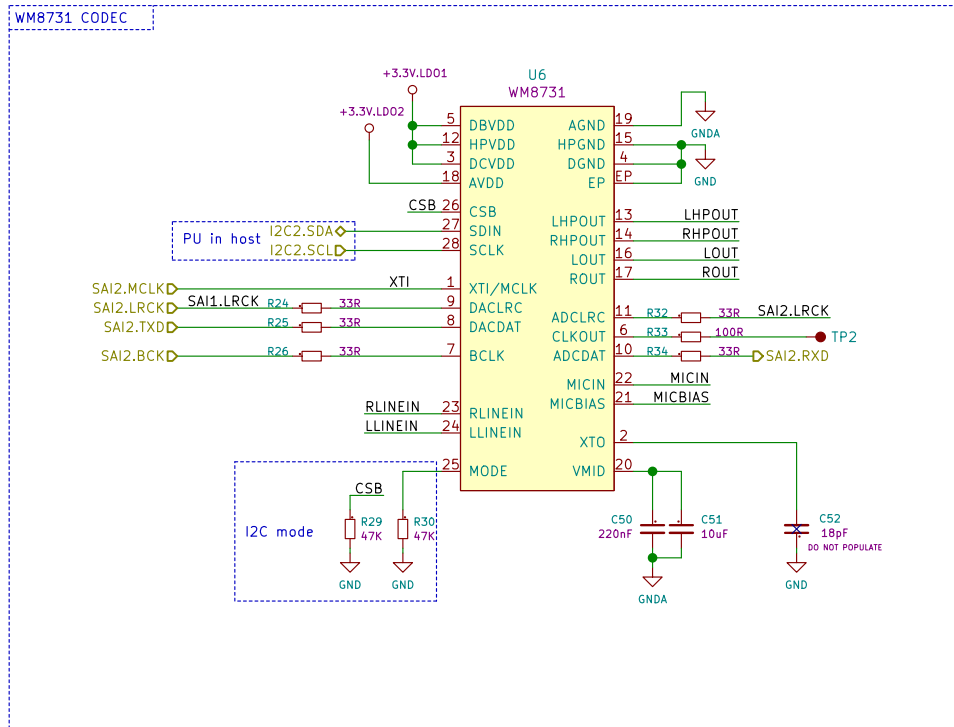
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**Rev: A**

Id: 6/11





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Sheet: /Block\_Diagram/Audio Codec/  
File: EX-PCB-10108-001\_audio.sch

**Title: EX-PCB-10108-001**

Size: A4  
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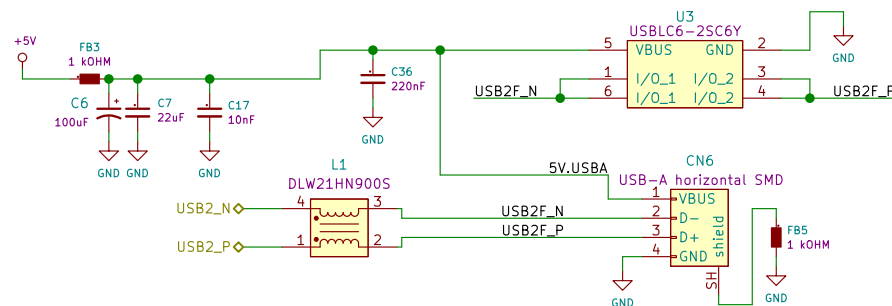
Date: 2021-07-27

Rev: A

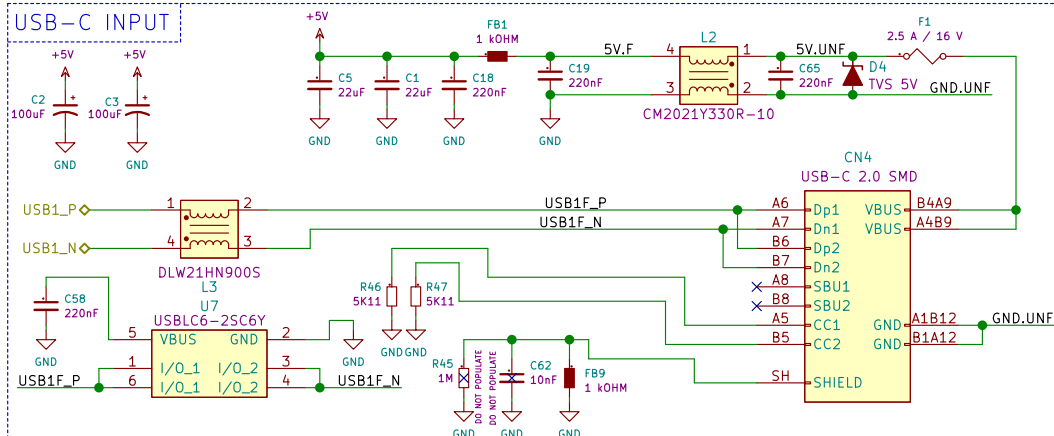
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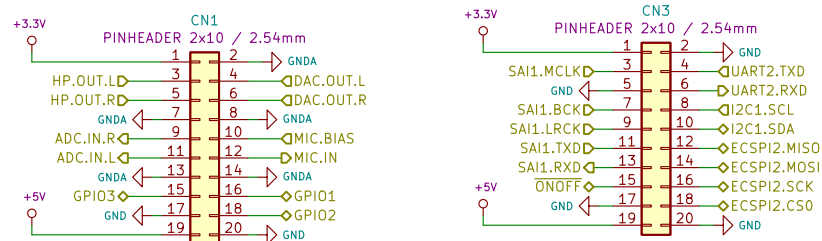
## USB CONNECTIVITY



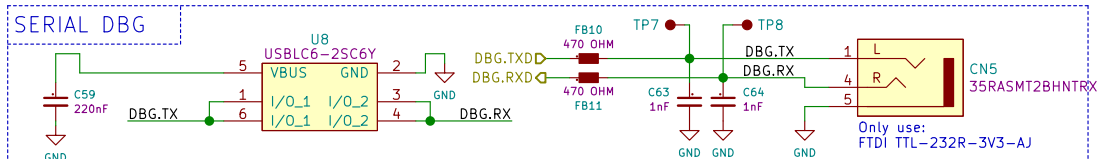
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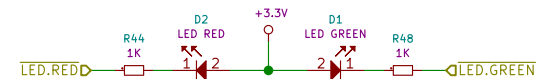
## PIN HEADER



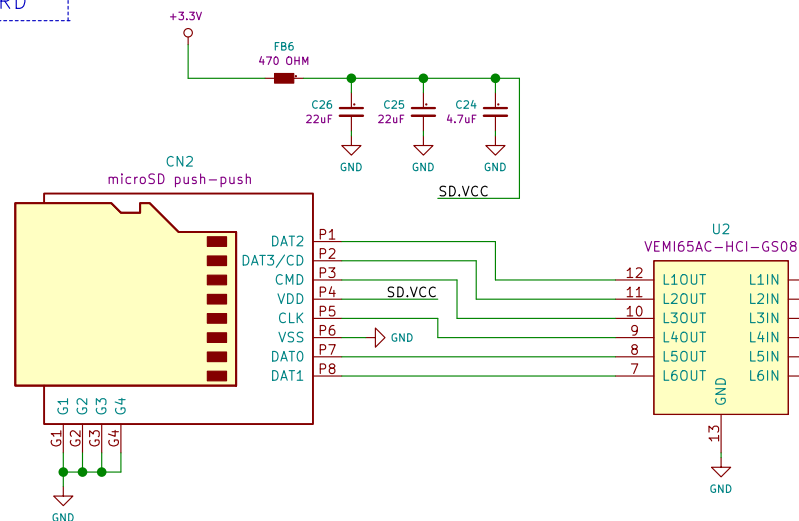
## SERIAL DBG



## STATUS LED



## MICROSD CARD



## FIDUCIALS + MOUNTING

- ✗ FID801 Fiducial 1 mm
- ✗ FID802 Fiducial 1 mm
- ✗ FID803 Fiducial 1 mm
- ✗ FID804 Fiducial 1 mm
- ✗ FID805 Fiducial 1 mm
- ✗ FID806 Fiducial 1 mm

- MH1 Mounting Hole M3
- MH2 Mounting Hole M3
- MH3 Mounting Hole M3
- MH4 Mounting Hole M3

EX-PCB-10108-001 - Schematics

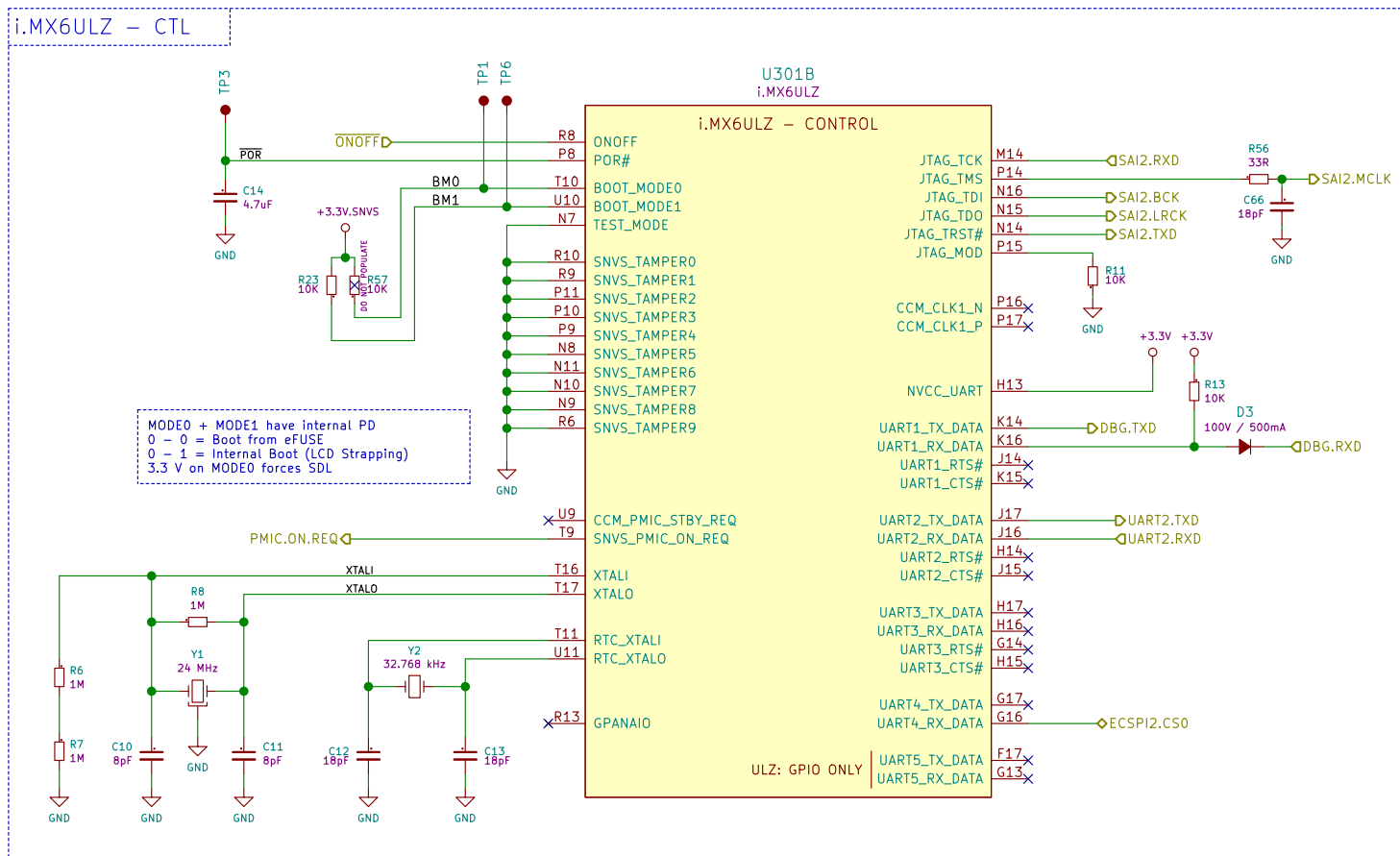
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Sheet: /Block\_Diagram/CPU Periphery I/  
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**Title: EX-PCB-10108-001**

Size: A4 Date: 2021-07-27

KiCad E.D.A. kicad (5.1.10-1-10\_14)

Rev: A

Id: 10/11

# REVISION HISTORY

REV	Description	Date	By
A	Initial desgin.	2021-07-27	PV

EX-PCB-10108-001 - Schematics			
Organelle 2 Processor board			
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Sheet: /Revision History/			
File: EX-PCB-10108-001_history.sch			
<b>Title: EX-PCB-10108-001</b>			
Size: A4		Date: 2021-07-27	<b>Rev: A</b>
KiCad E.D.A. kicad (5.1.10-1-10_14)			Id: 11/11