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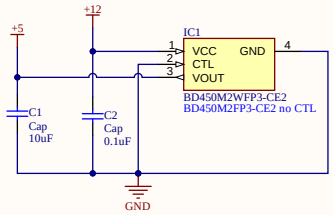
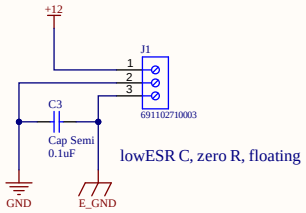
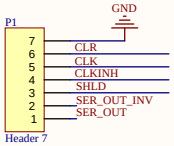
REVISION	DESCRIPTION	DATE	APPROVED

Interface pin

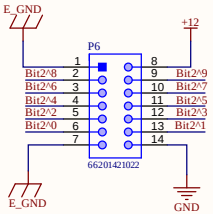


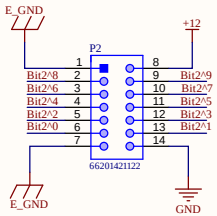


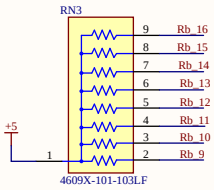
lowESR C, zero R, floating

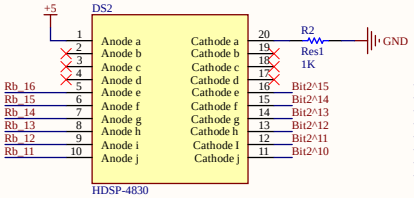


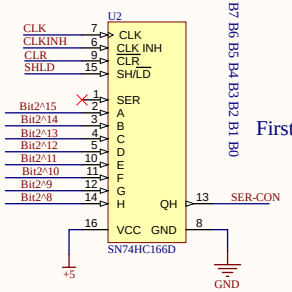
NPN Open collector input





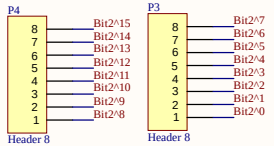


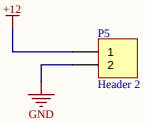




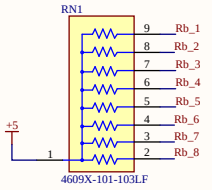
B7 B6 B5 B4 B3 B2 B1 B0

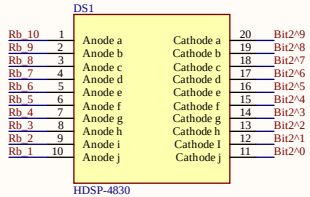
First bit

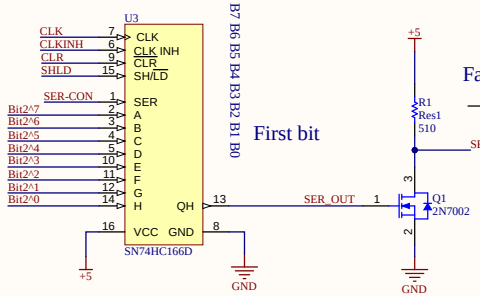




enc_pwr

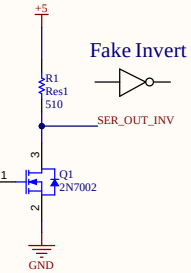






B7 B6 B5 B4 B3 B2 B1 B0

First bit



Fake Invert

APPROVALS	DATE
ENG: *	
DSN: owl_hor	
CHK: *	
REFERENCE DOCUMENTS	
BOM:	
ASSY DWG:	
FAB DWG:	
PCB DWG:	

PROJECT

Altium

PROJECT REVISION: Not in version control | DOCUMENT REVISION: Not in version control | DESIGN ITEM:

TITLE

Abs Encoder PISO Shift

SIZE	CAGE CODE	DWG NO.	REV
B			

SCALE: | FILE NAME enc-abs_piso_1.0.SchDoc | SHEET 1 OF 1