



## NOTES :

Source repository : [https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.1/KiCAD\\_files](https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.1/KiCAD_files)

Preliminary PREL, Good For Execution, GFE, As Built ASB  
Major revisions are to be described thoroughly when pushed with git push command

Date	Rev.	Creator	Reviewer	Approver	Status
	B				
	A				
2021-06-04	—	Benmouffok	Alinei	Villa	GFE

*Title: Second Intern Copper Layer*



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File: Power.kicad\_pcb

Size: A4

Scale : 1:1

Sheet:

KiCad E.D.A. pcbnew (5.1.7)-1

Id: 1/1