


NOTES :					
Source repository : <a href="https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.1/KiCAD_files">https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.1/KiCAD_files</a>					
Preliminary PREL, Good For Execution, GFE, As Built ASB Major revisions are to be described thoroughly when pushed with git push command					
Date	Rev.	Creator	Reviewer	Approver	Status
	B				
	A				
2021-06-04	—	Benmouffok	Alinei	Villa	GFE
Title: <i>Non-Plated Holes Layout</i>			<div> Open Digital Power</div> <div>Licensed under CERN-OHL-S V2.0</div>		
File: Power.kicad_pcb					
Size: A4	Scale : 1:1			Sheet:	
KiCad E.D.A. pcbnew (5.1.7)–1				Id: 1/1	