



Drill Map:

- 0.40mm / 0.016" (517 holes)
- 0.51mm / 0.020" (30 holes)
- + 1.30mm / 0.051" (16 holes)
- 2.20mm / 0.087" (3 holes)

NOTES :

Source repository : https://gitlab.laas.fr/owntech/1leg/-/tree/V1.1.1/KiCAD_files

Preliminary PREL, Good For Execution, GFE, As Built ASB
Major revisions are to be described thoroughly when pushed with git push command

Date	Rev.	Creator	Reviewer	Approver	Status
	B				
	A				
2021-06-04	-	Benmouffok	Alinei	Villa	GFE

Title: Plated Holes Layout



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File: Power.kicad_pcb

Size: A4 Scale : 1:1 Sheet:

KiCad E.D.A. pcbnew (5.1.7)-1 Id: 1/1