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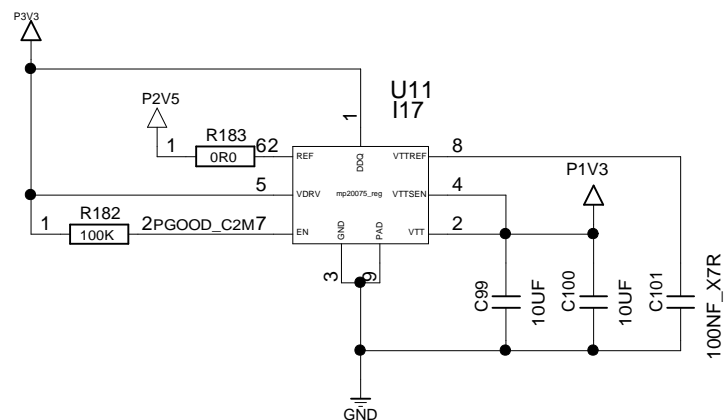
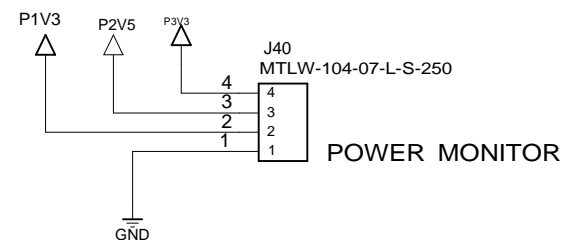
SHEET NO.	SHEET NAME	BLOCK NAME
16	page1	sfp_module
17	page1	sfp_module
18	BUFFER 1:2	diff_buff_1to2
19	BUFFER 1:2	diff_buff_1to2
20	BUFFER 1:2	diff_buff_1to2
21	BUFFER 1:2	diff_buff_1to2
22	page1	sfp_module
23	page1	sfp_module
24	page1	sfp_module
25	page1	sfp_module
26	BUFFER 1:2	diff_buff_1to2
27	I2C LEVEL TRANSLATOR	i2c_level_translate
28	I2C LEVEL TRANSLATOR	i2c_level_translate
29	I2C LEVEL TRANSLATOR	i2c_level_translate
30	I2C LEVEL TRANSLATOR	i2c_level_translate

I2C ADDRESSES  
EEPROM 0X50 (ALTERNATIVE: 0X51)  
CDR 0X60 (ALTERNATIVE: 0X40)  
CLOCK 0X68 (0X69, 0X6A, 0X6B)  
EXPANDER 0X74  
EXPANDER 0X75

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31	I2C LEVEL TRANSLATOR	i2c_level_translate
32	I2C LEVEL TRANSLATOR	i2c_level_translate
33	I2C LEVEL TRANSLATOR	i2c_level_translate
34	I2C LEVEL TRANSLATOR	i2c_level_translate
35	I2C LEVEL TRANSLATOR	i2c_level_translate
36	AB	fmc_hpc_generic
37	CDGH	fmc_hpc_generic
38	EFJK	fmc_hpc_generic

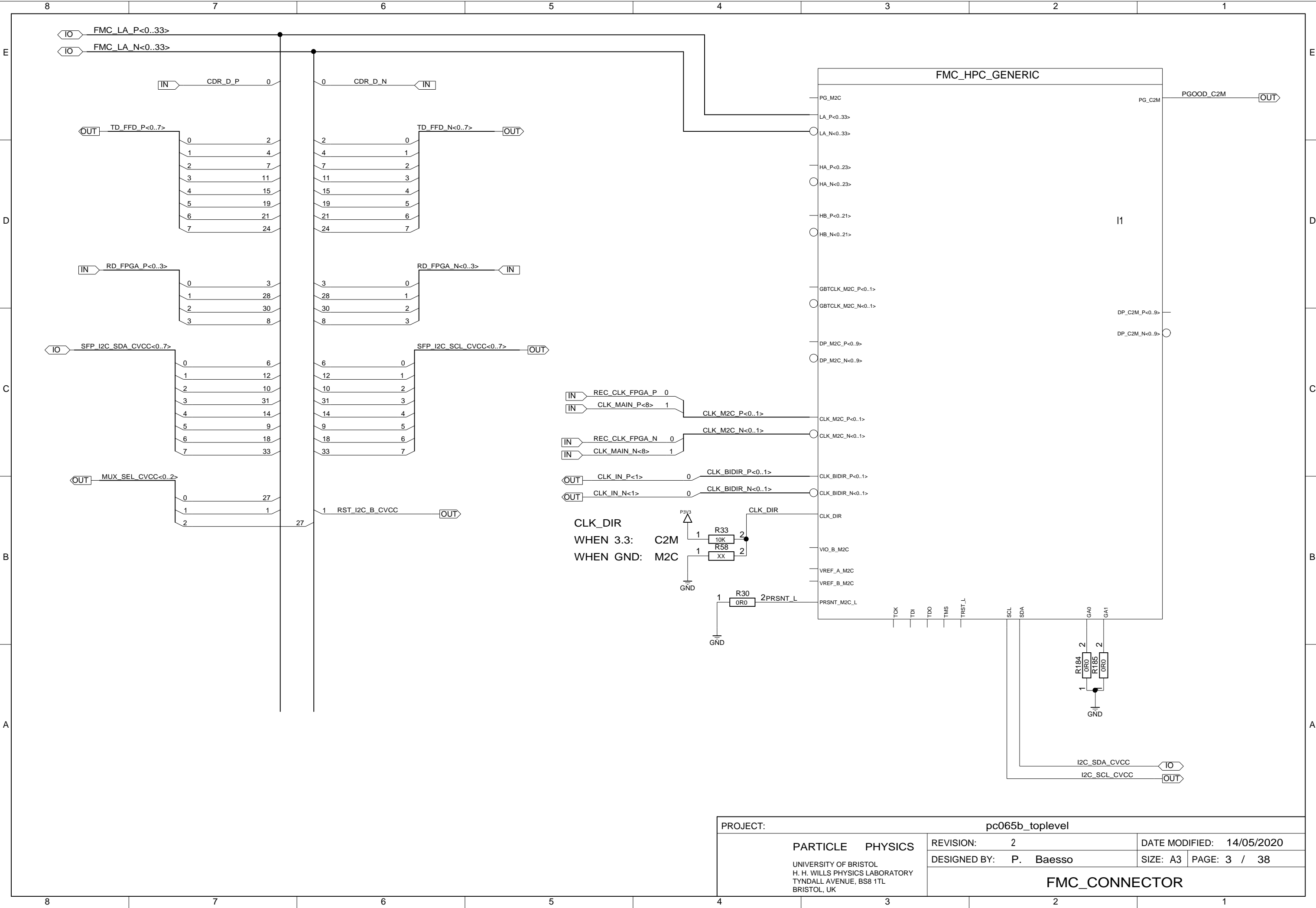
PROJECT:		pc065b_toplevel	
PARTICLE PHYSICS  UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORATORY TYNDALL AVENUE, BS8 1TL BRISTOL, UK	REVISION:	2	DATE MODIFIED: 14/05/2020
	DESIGNED BY:	P. Baesso	SIZE: A3 PAGE: 1 / 38
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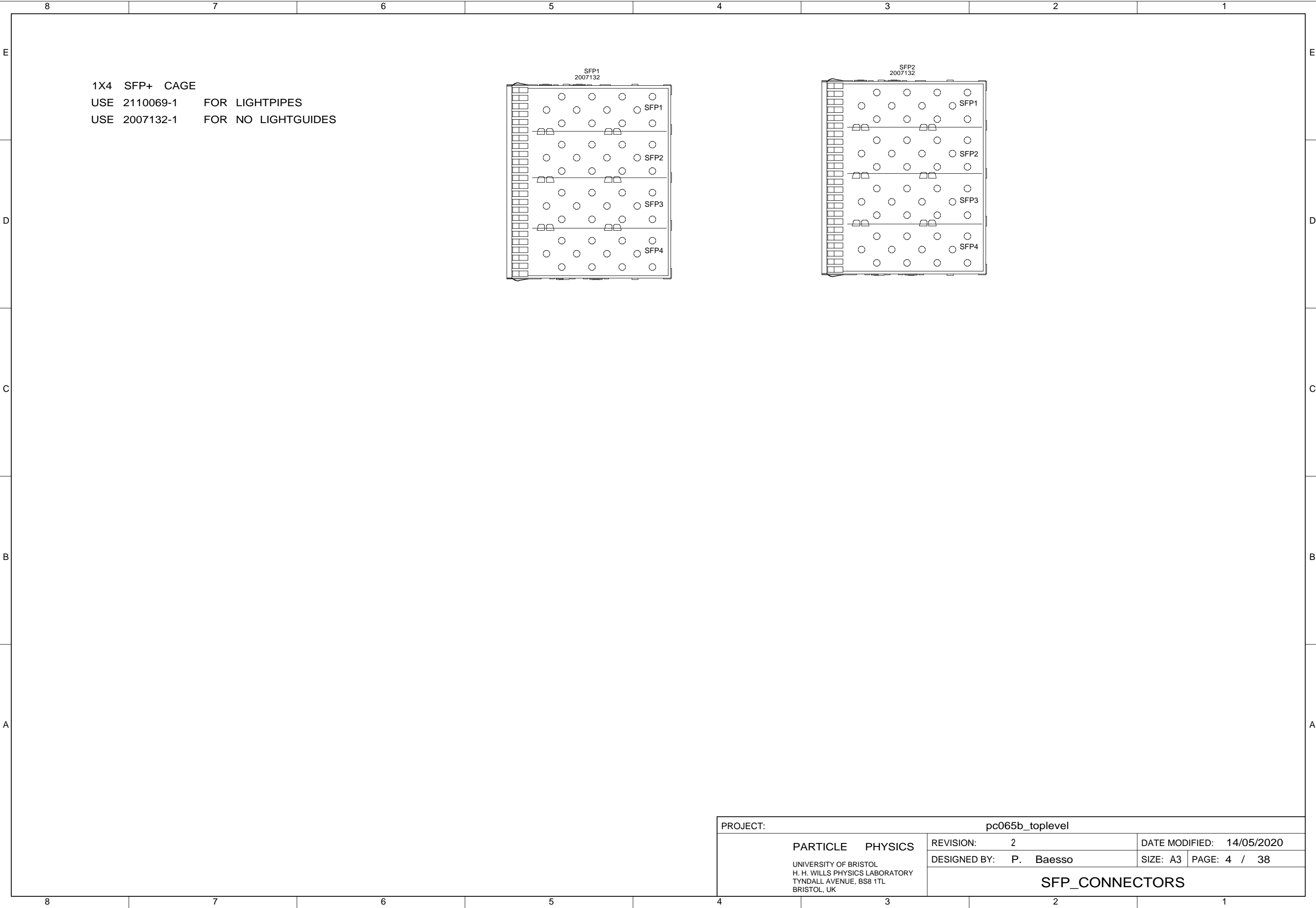
FOR NOW PLACE A JUMPER+TP TO ALLOW  
CONNECTING VADJ TO P2V5 OR USING  
EXTERNAL WIRE

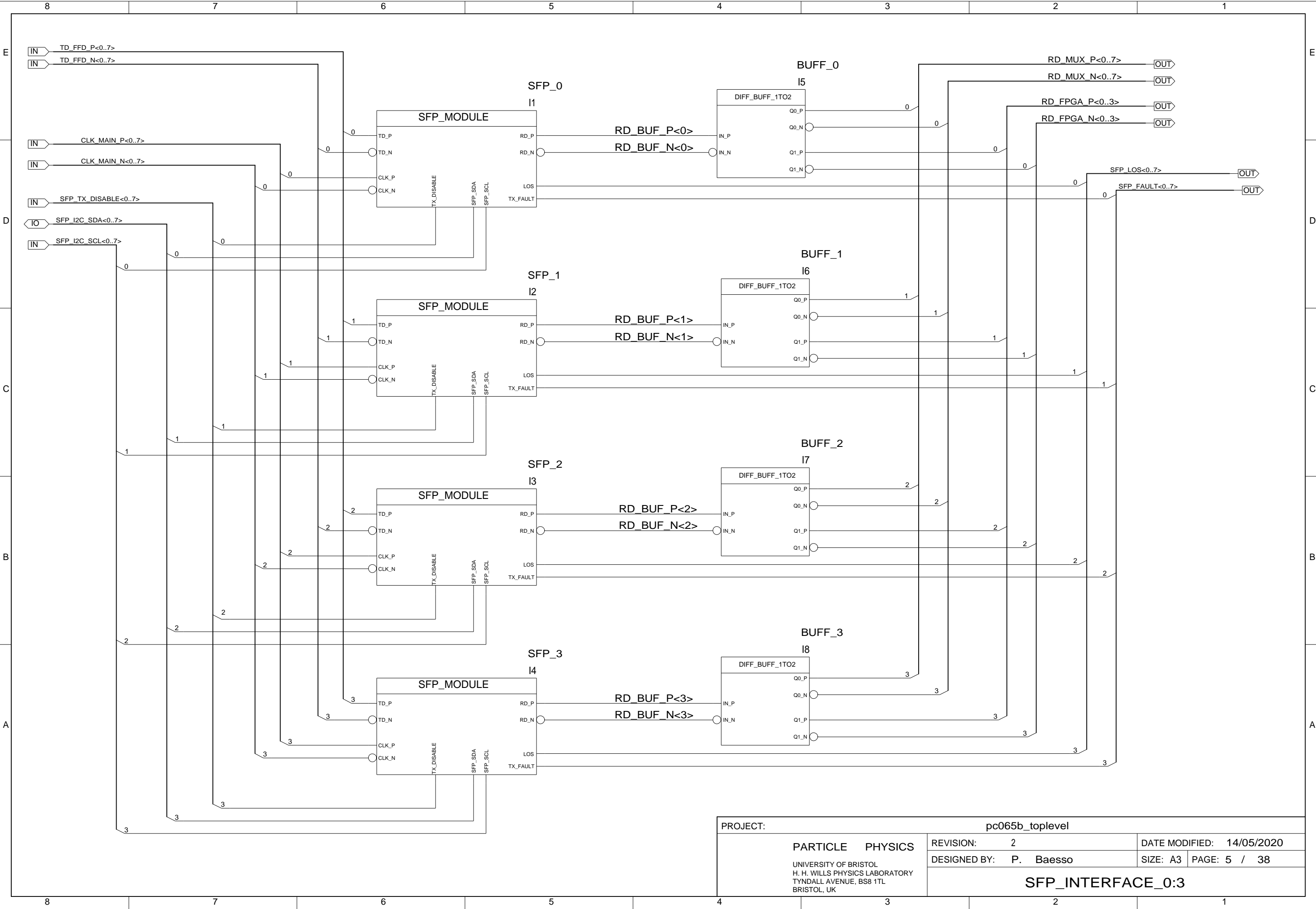
P3V3: 3 A  
P12V: 1A  
VADJ: 4 A

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	DESIGNED BY:	P. Baesso	SIZE: A3	PAGE: 2 / 38	
	POWER				

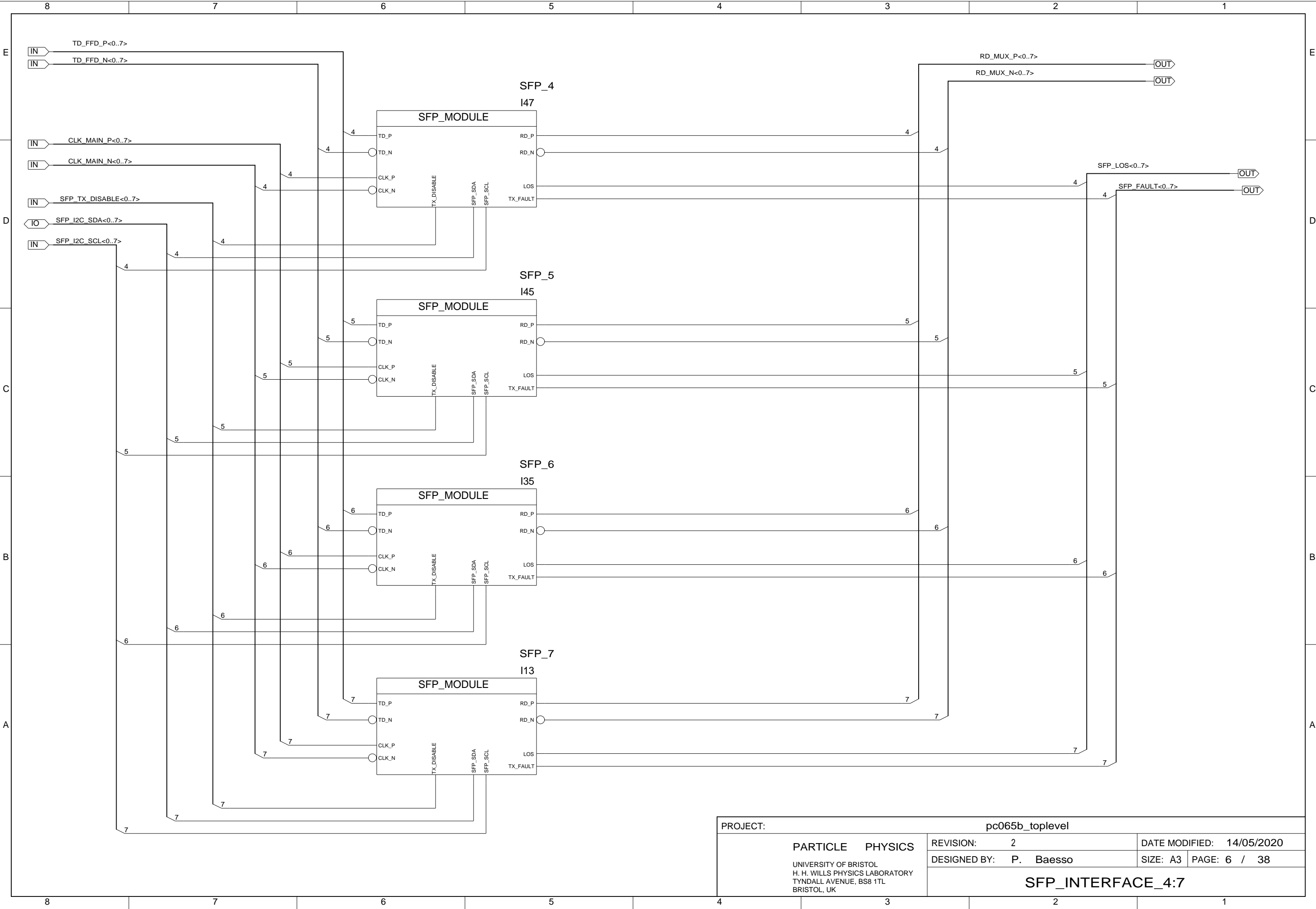


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	FMC_CONNECTOR			

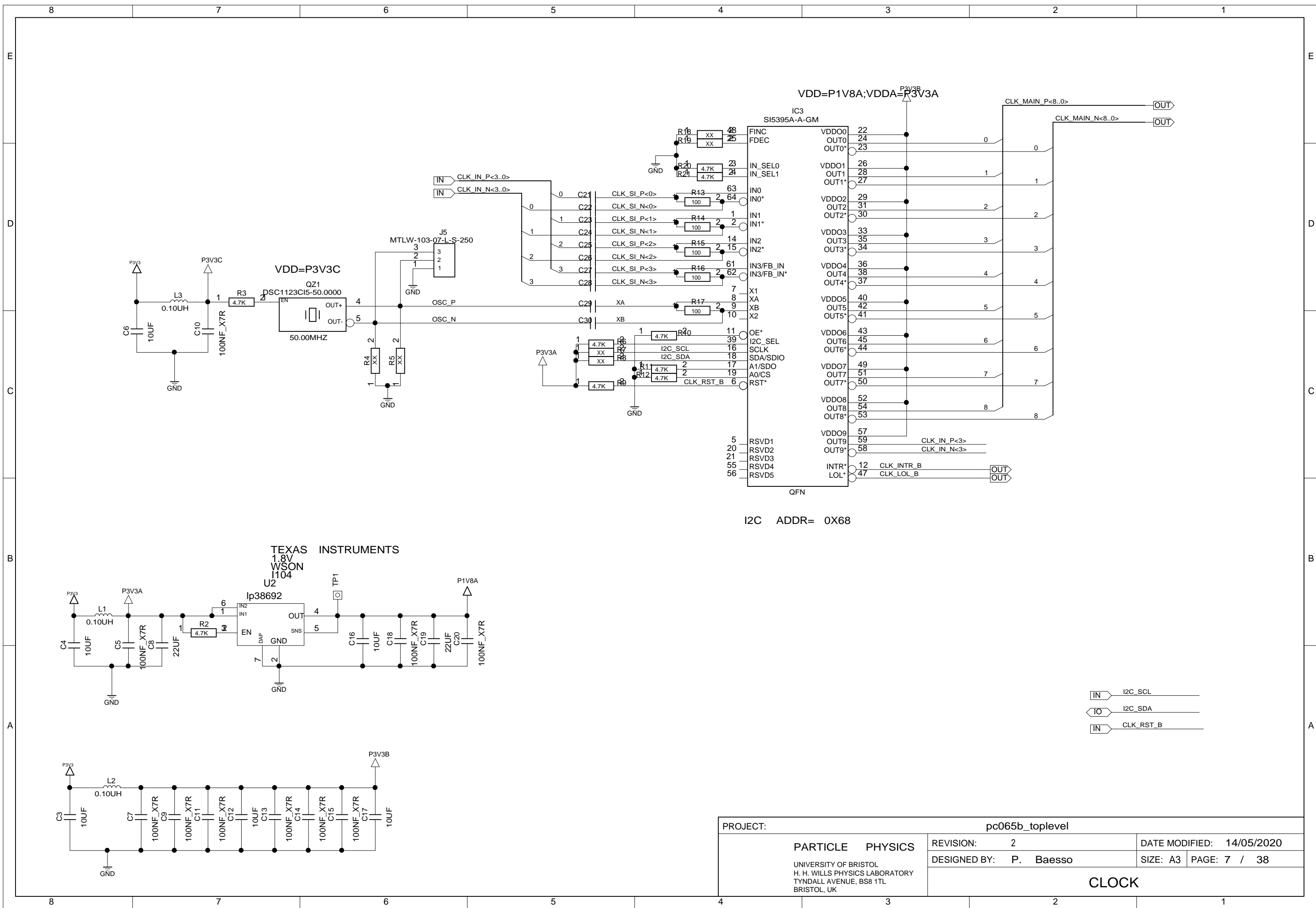


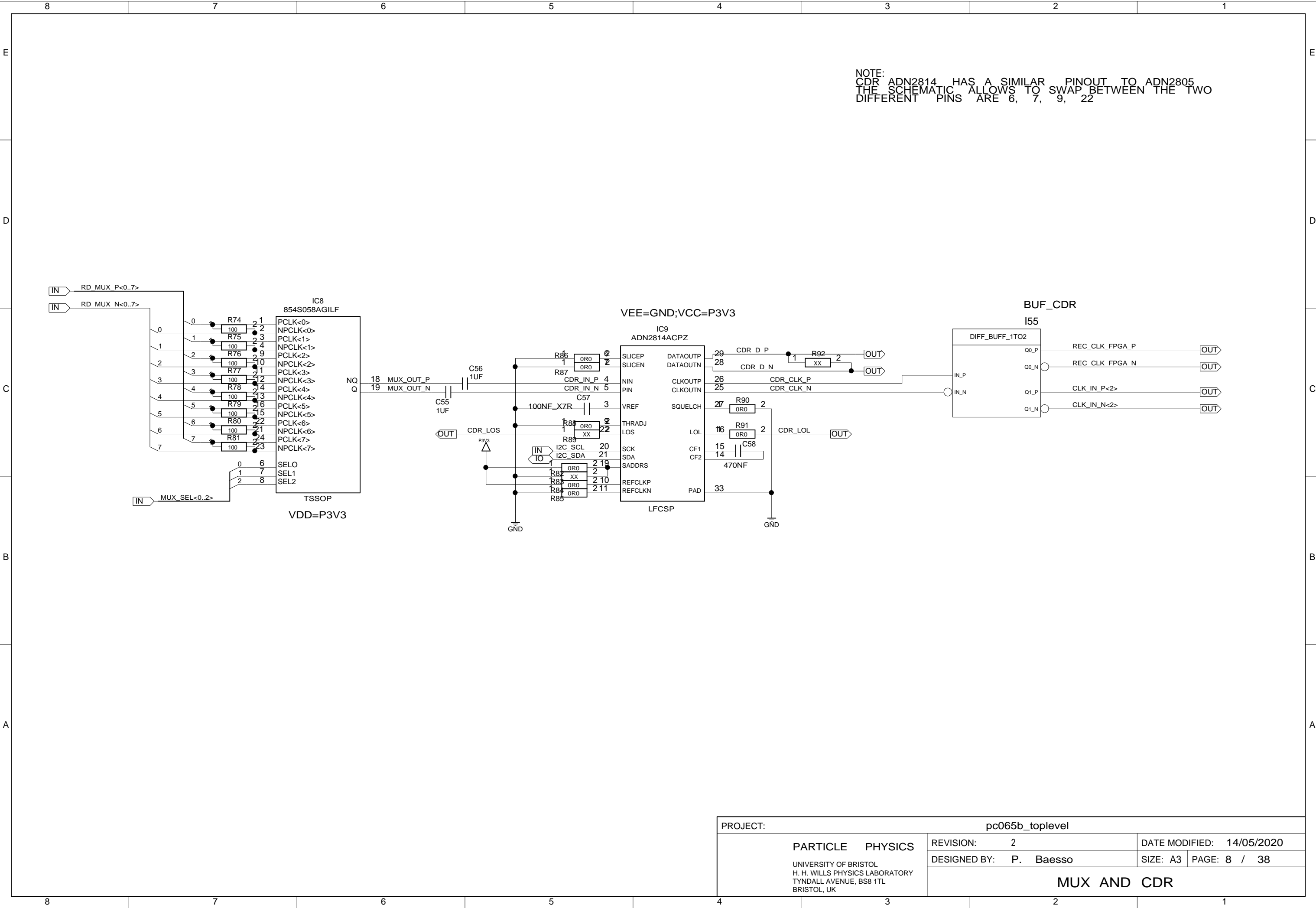


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	SFP_INTERFACE_0:3			



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	SFP_INTERFACE_4:7			

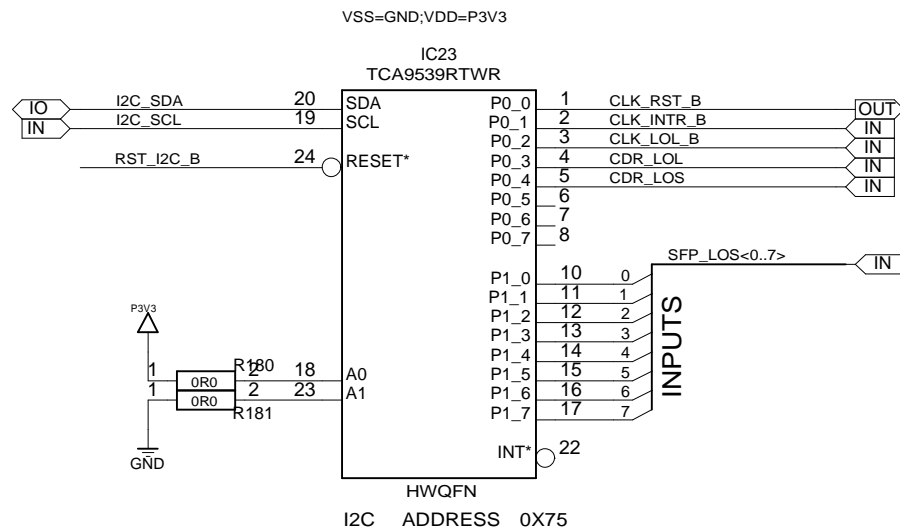
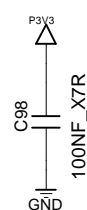
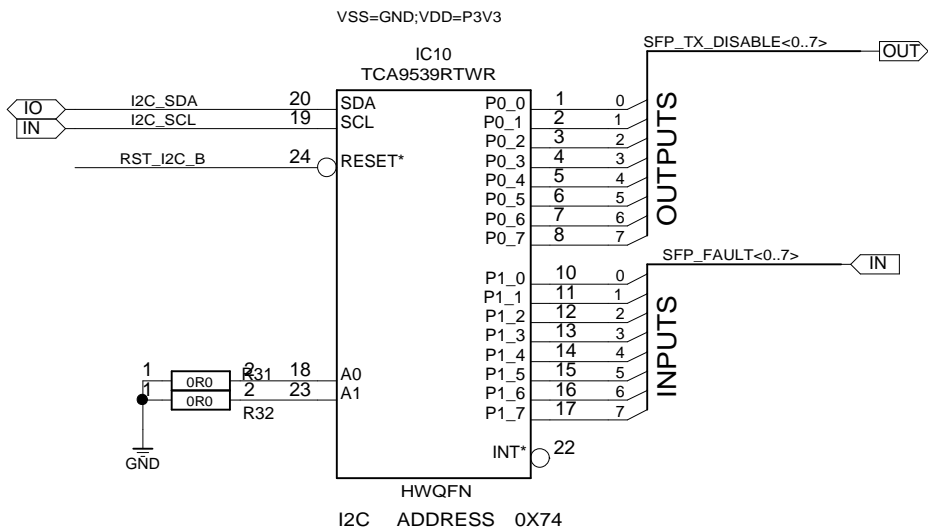




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	MUX AND CDR			



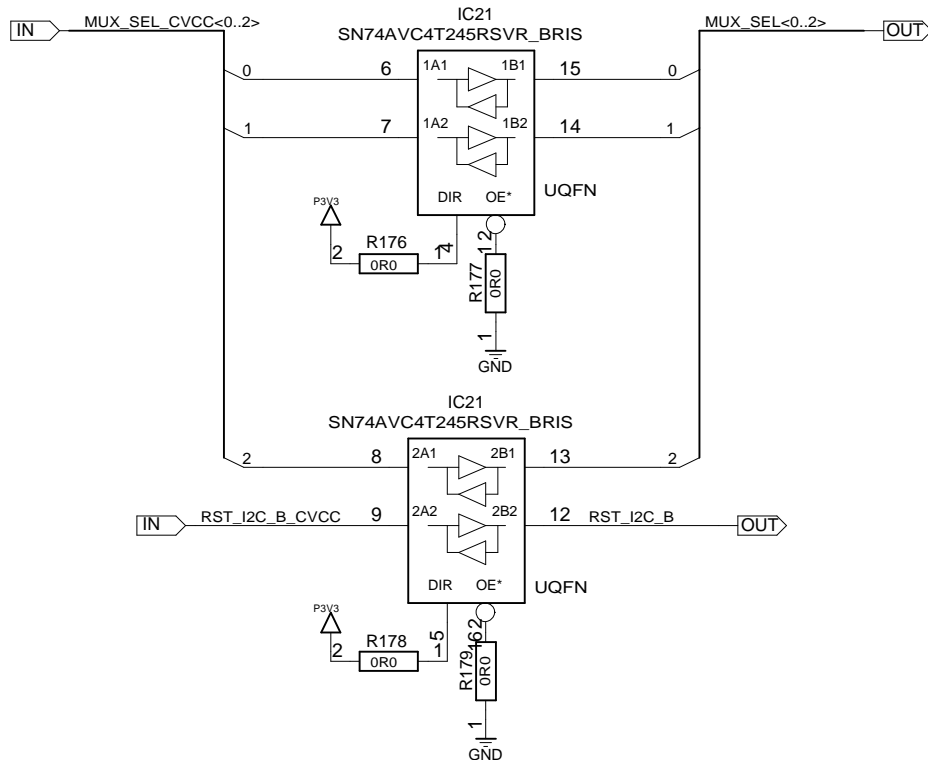




CHECK PINOUT AND FOOTPRINT!

DIR LOW: B >>> A  
DIR HIGH: A >> B

VCCA=P2V5;VCCB=P3V3



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	LEVEL TRANSLATORS			



E

D



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8

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7

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6

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5

4

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3

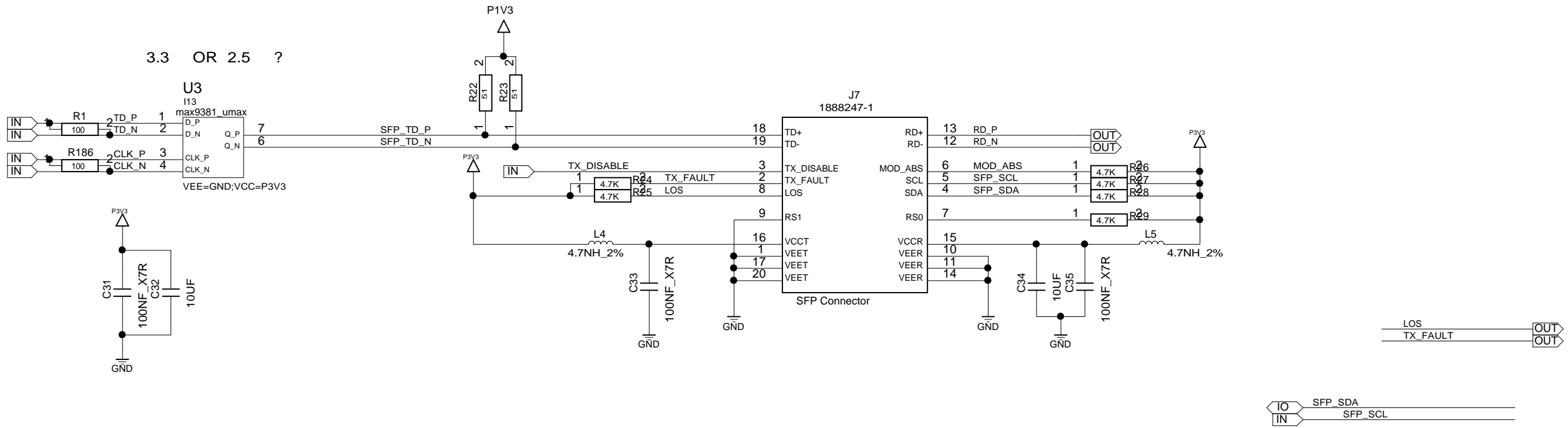
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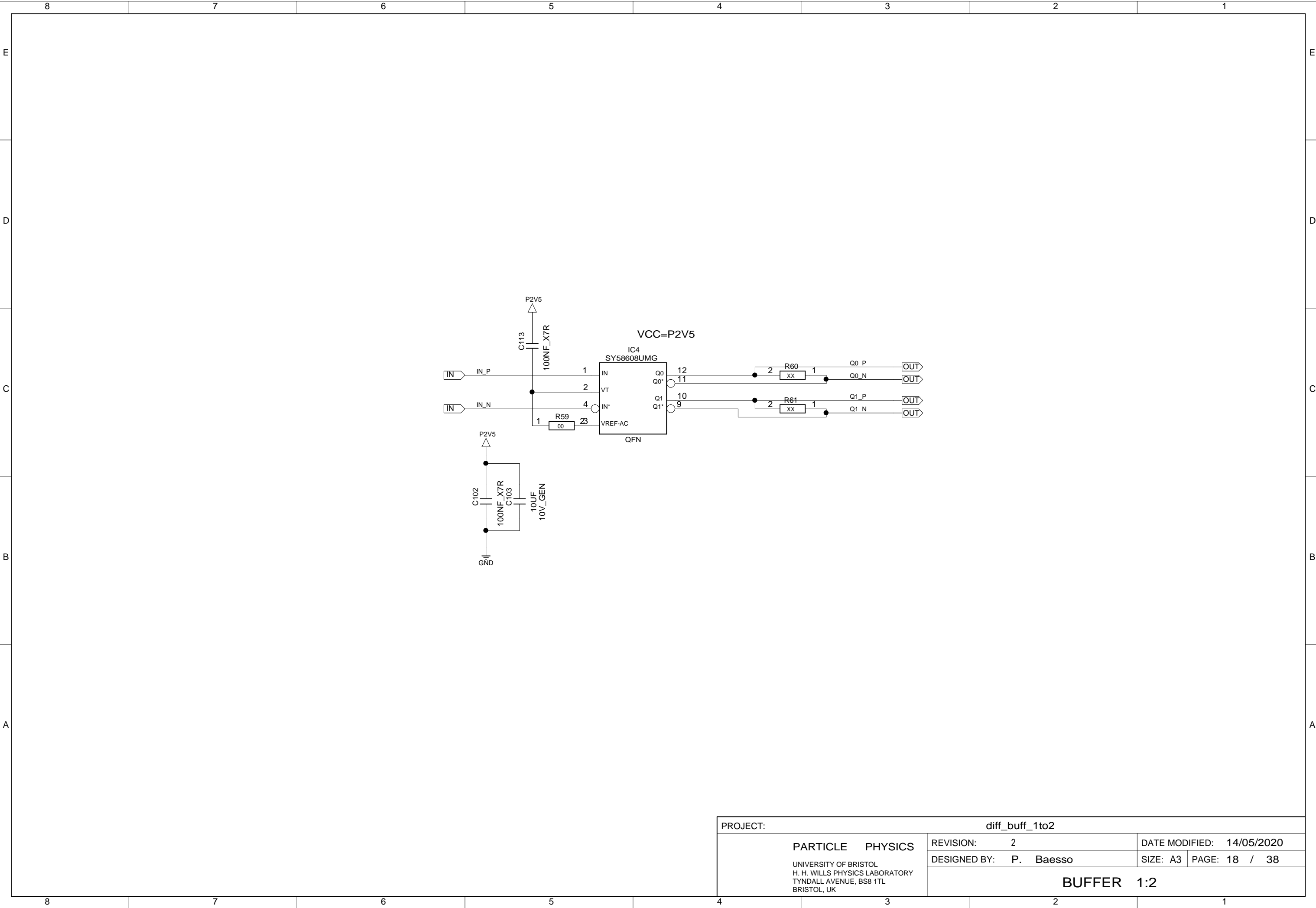
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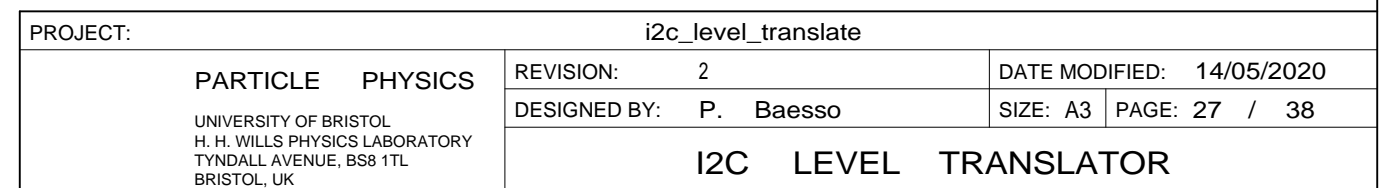
1

8		7		6		5		4		3		2		1	
E														E	
D														D	
C														C	
B														B	
A														A	
8		7		6		5		4		3		2		1	

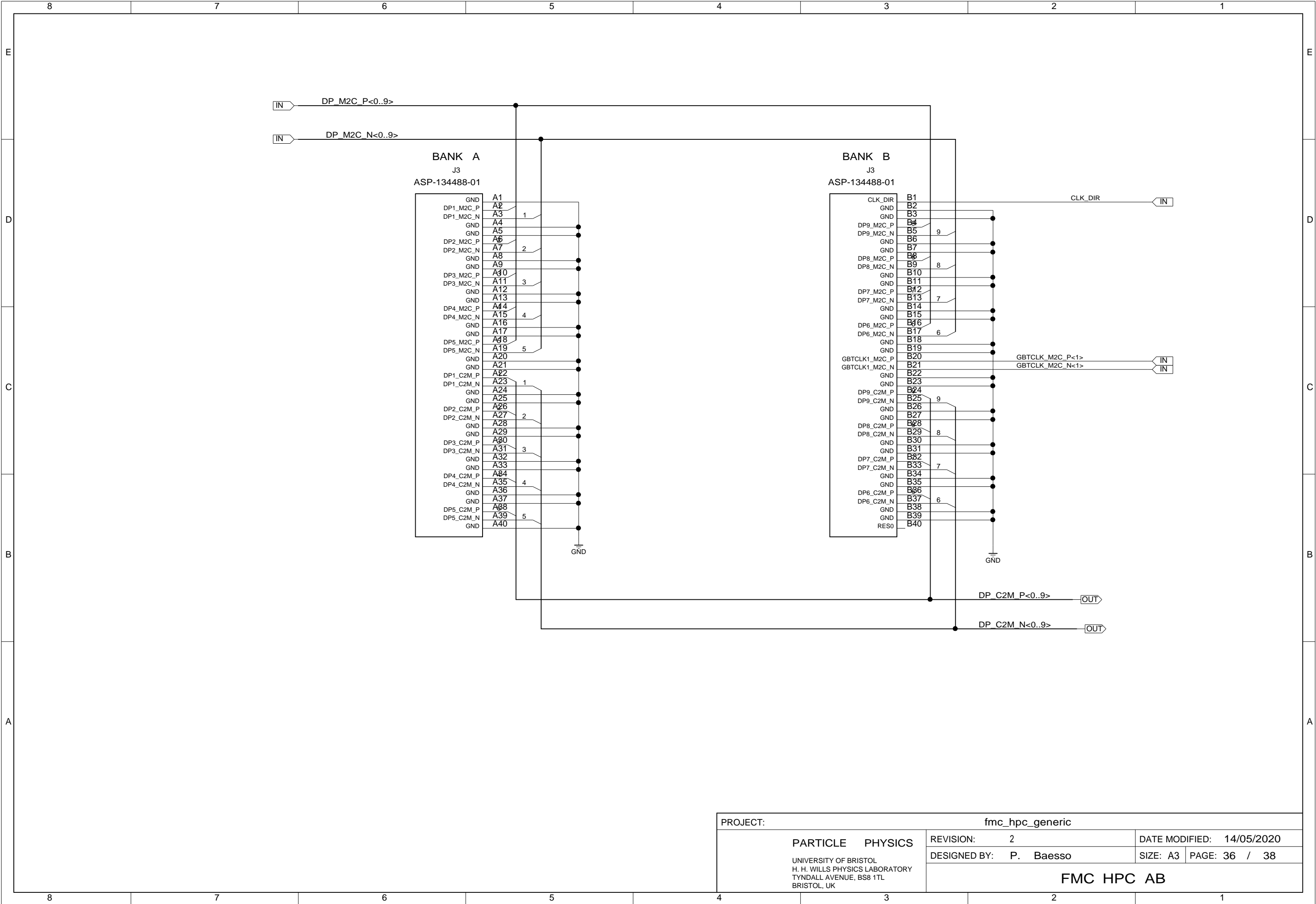


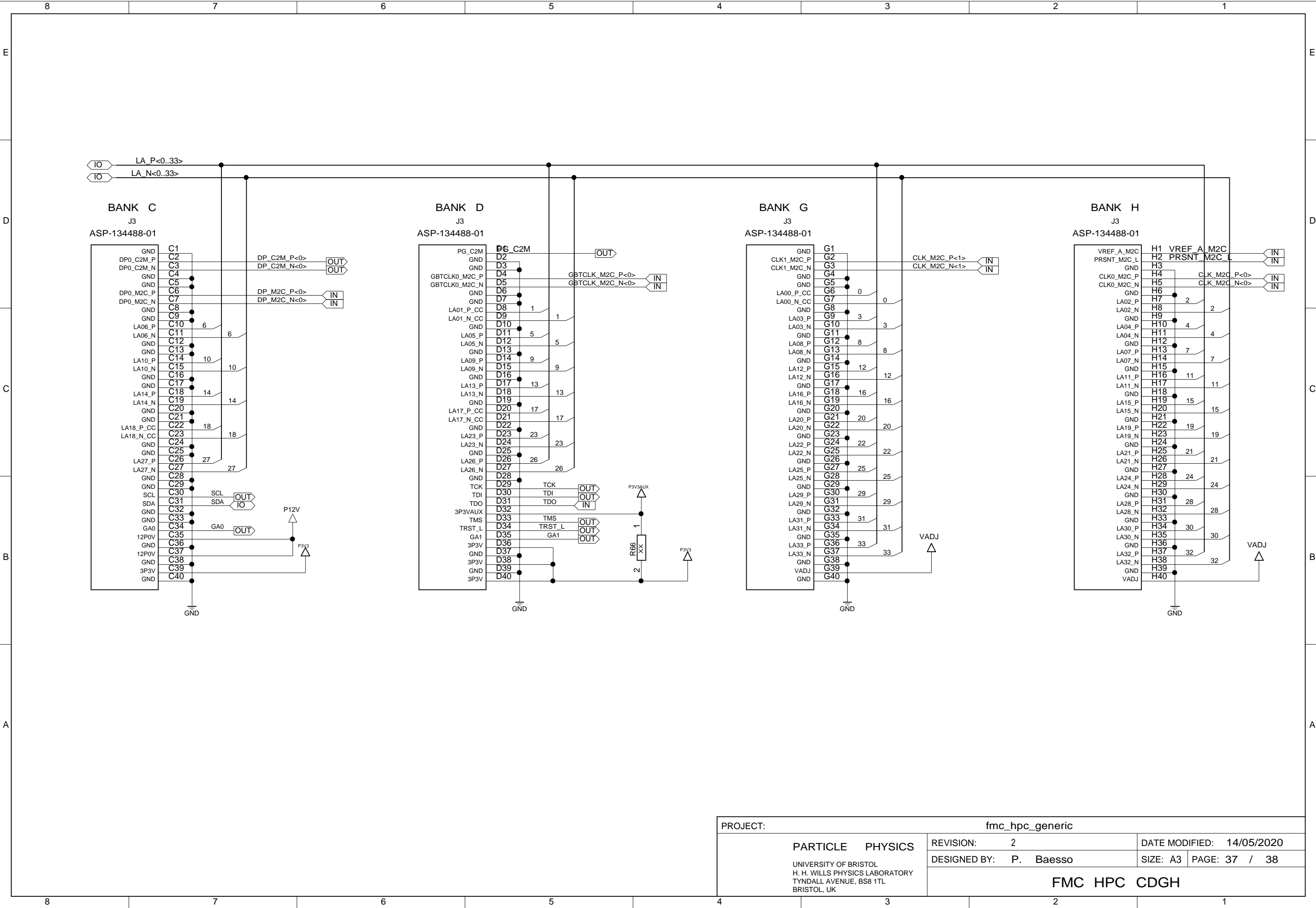
PROJECT:		sfp_module		
PARTICLE PHYSICS UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORATORY TYNDALL AVENUE, BS8 1TL BRISTOL, UK	REVISION:	2	DATE MODIFIED: 14/05/2020	
	DESIGNED BY:	P. Baesso	SIZE: A3	PAGE: 14 / 38
	SFP_MODULE			



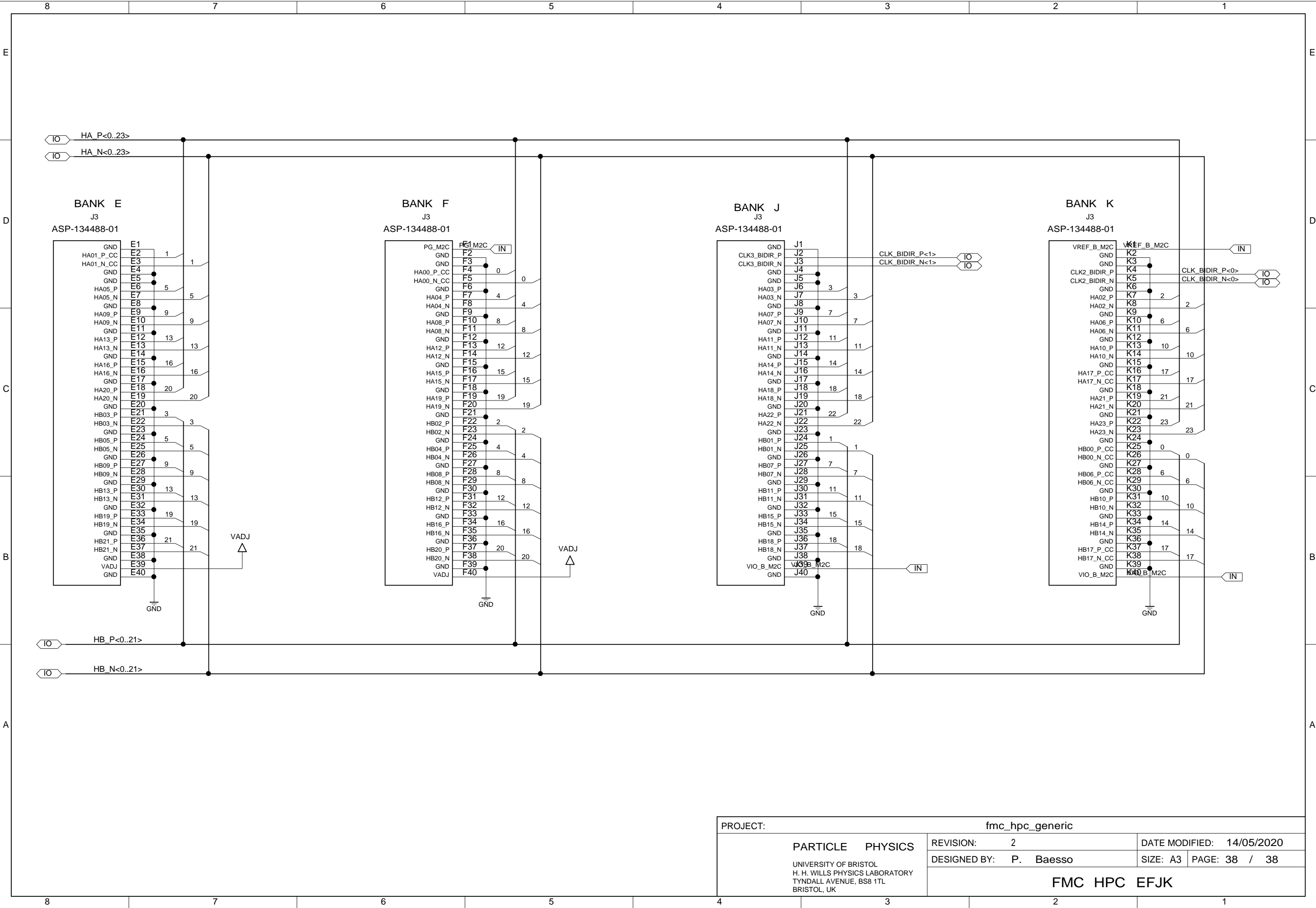








PROJECT:		fmc_hpc_generic		
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	FMC HPC CDGH			



PROJECT:		fmc_hpc_generic		
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	FMC HPC EFJK			