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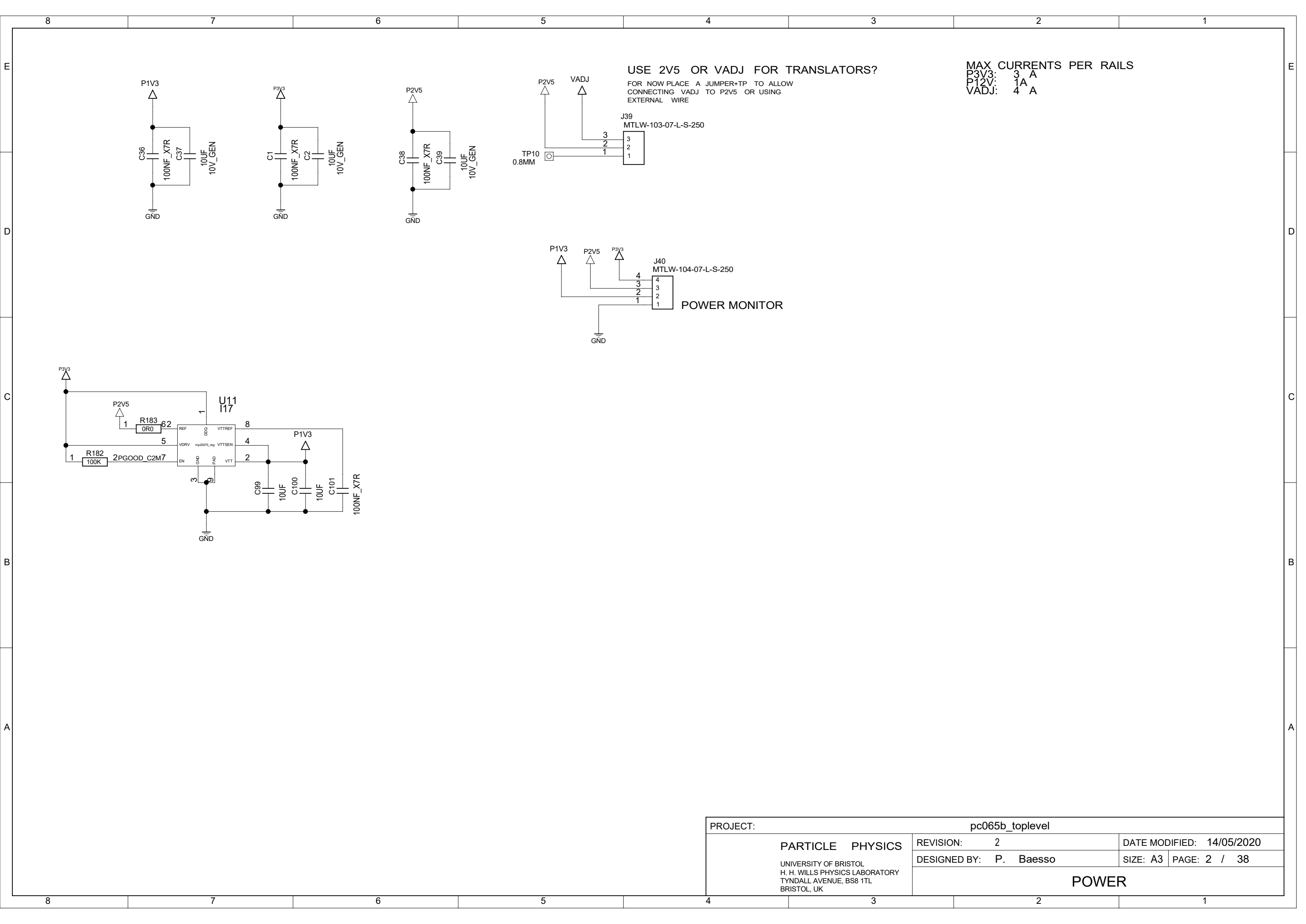
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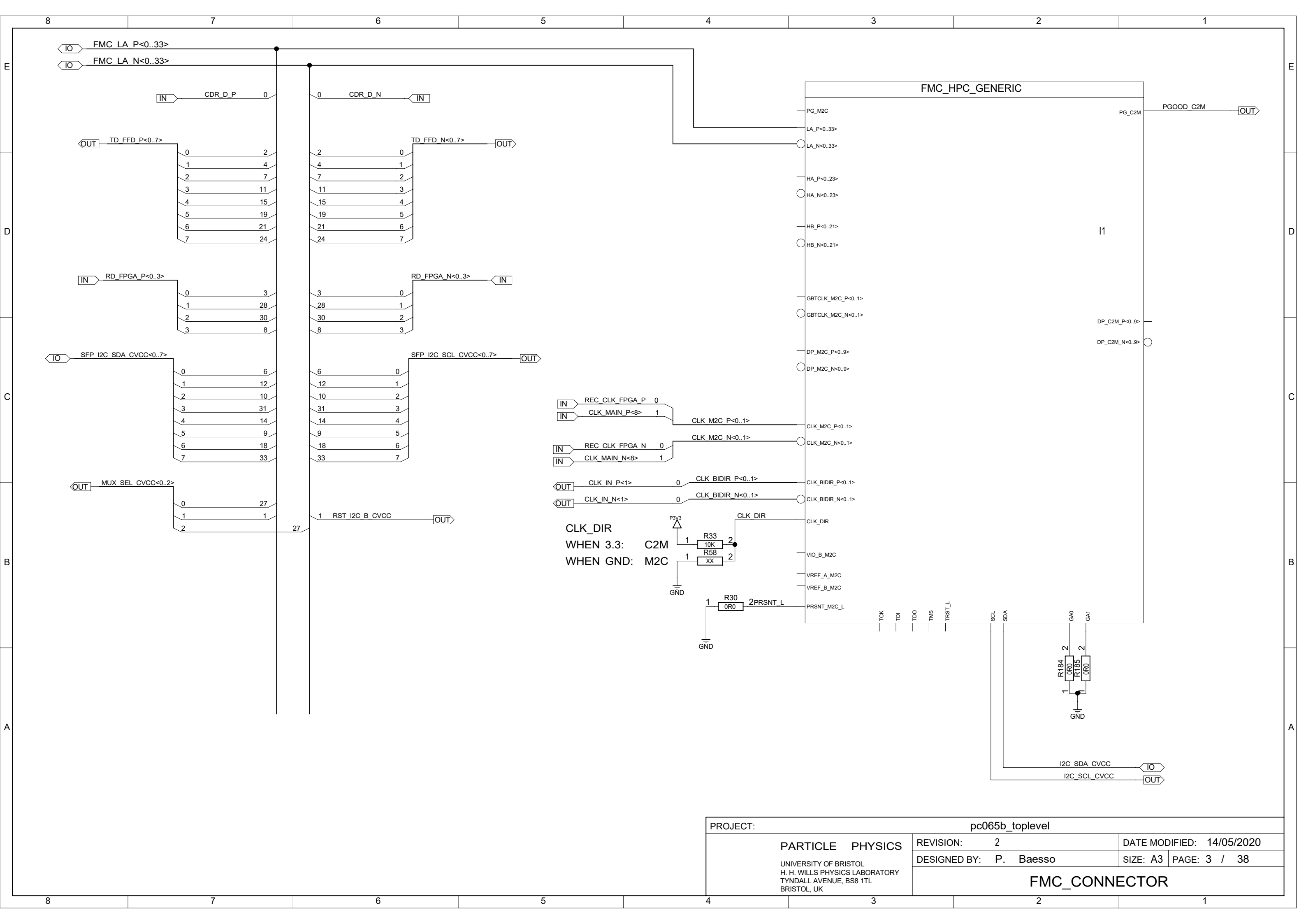
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CDR 0X60 (ALTERNATIVE: 0X40)
CLOCK 0X68 (0X69, 0X6A, 0X6B)
EXPANDER 0X74
EXPANDER 0X75

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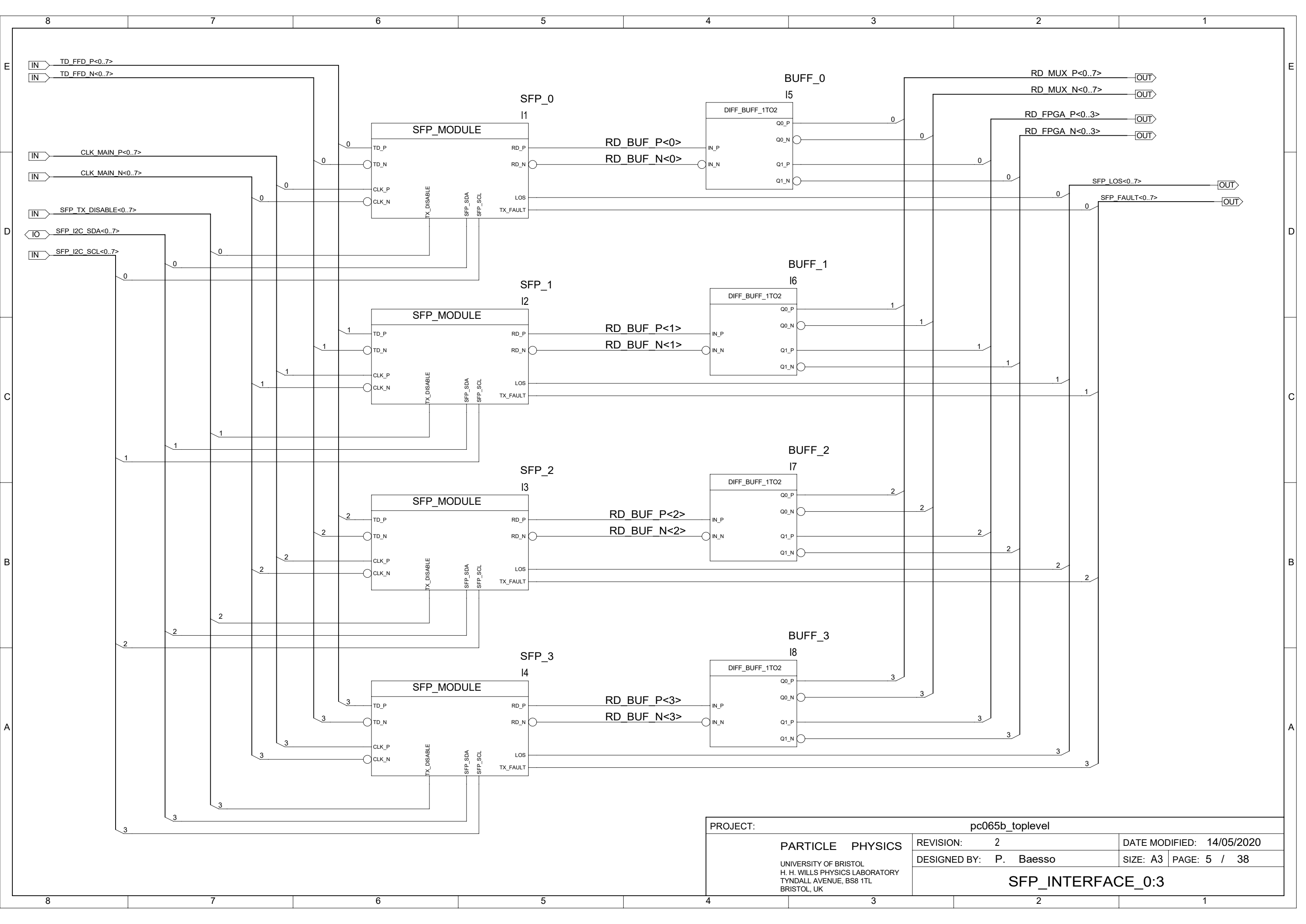
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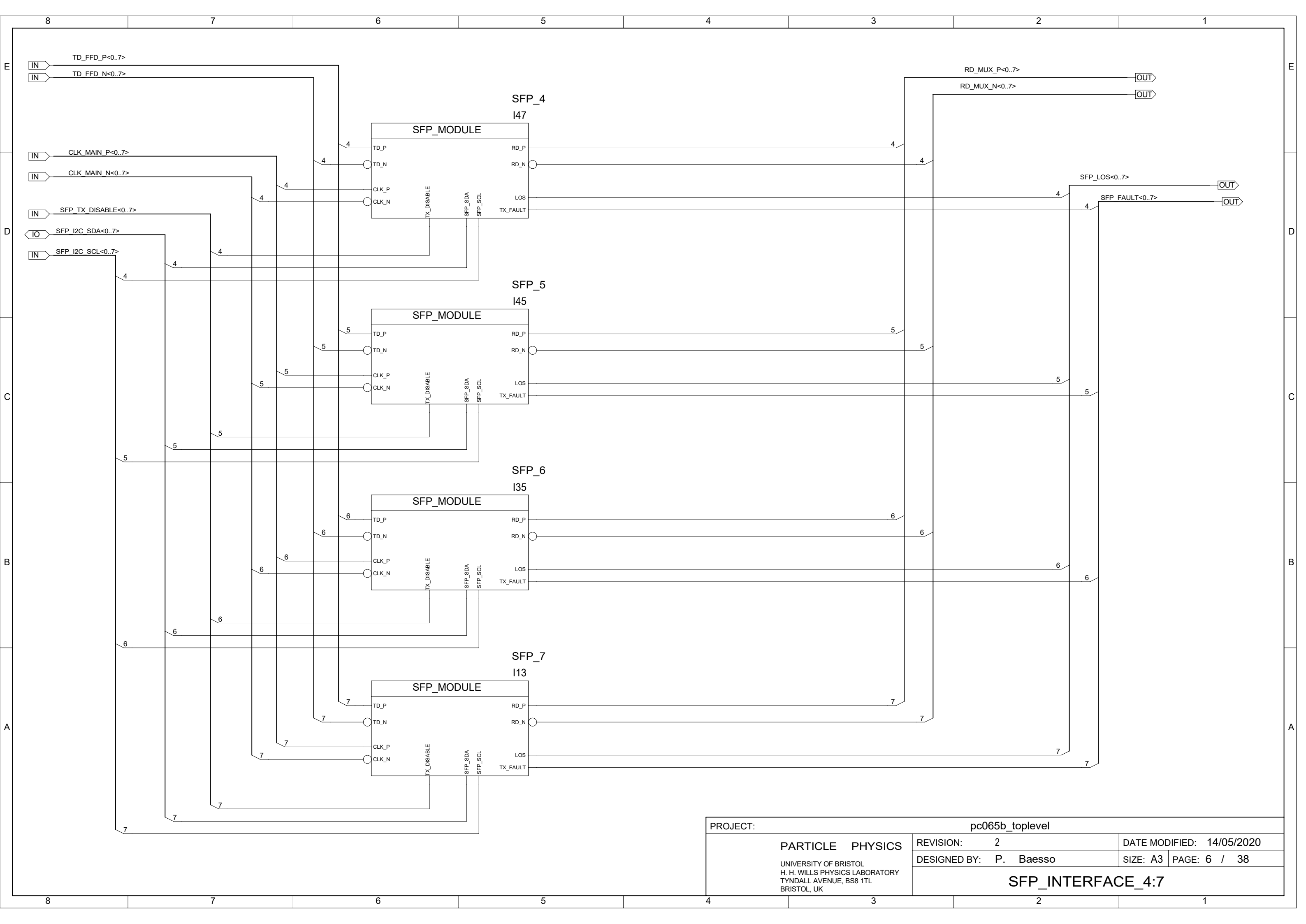




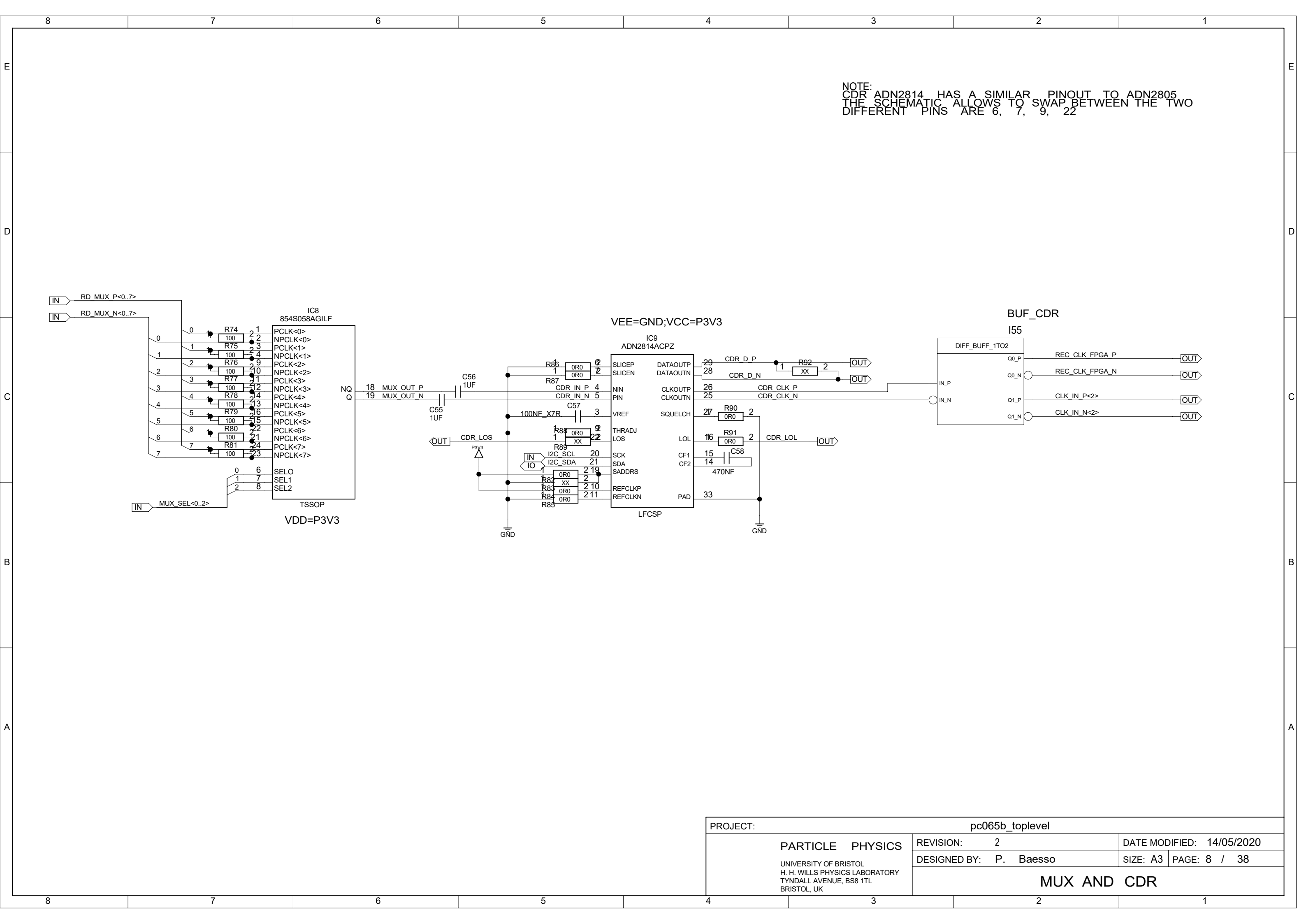
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| | FMC_CONNECTOR | | |



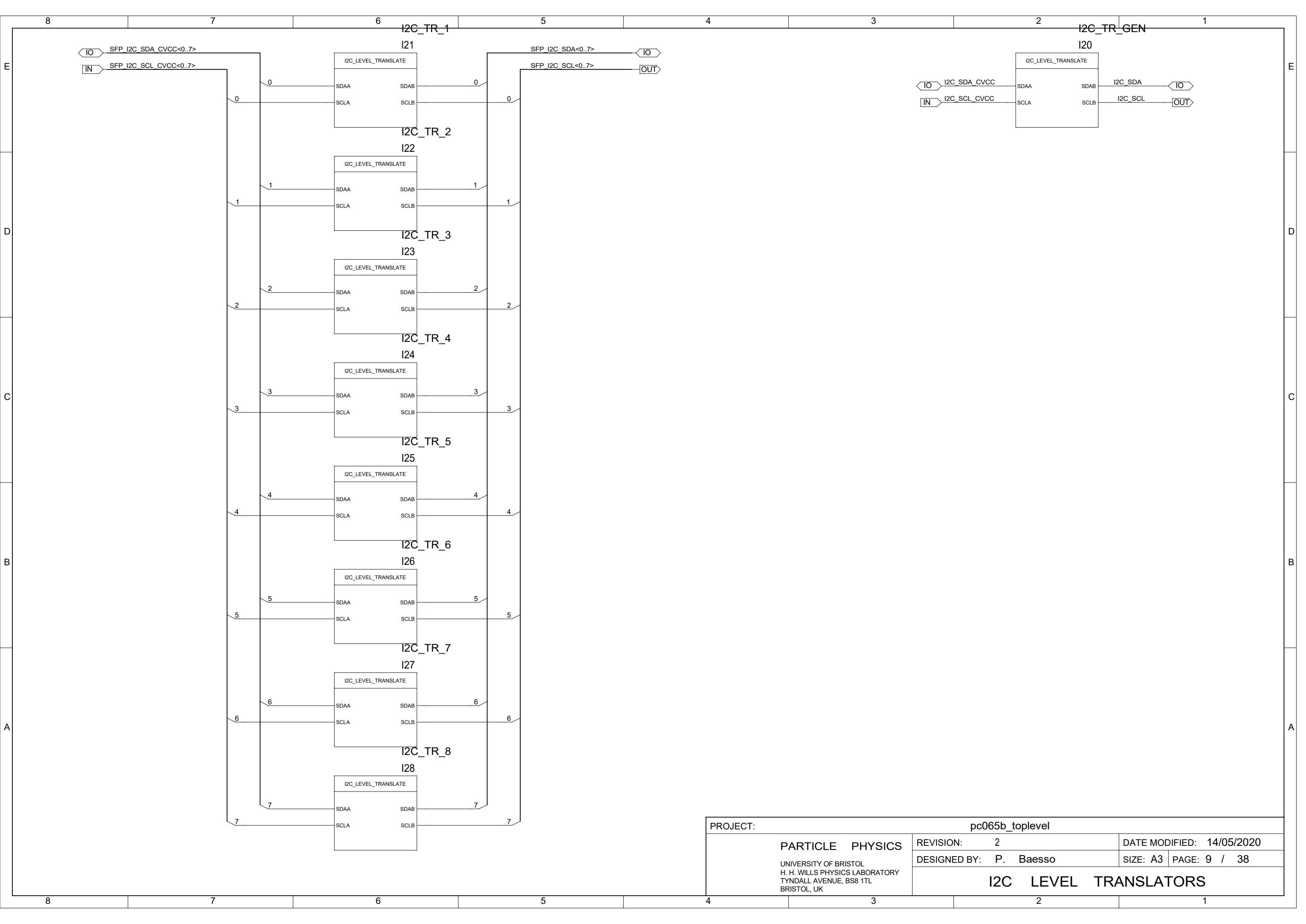
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| | SFP_INTERFACE_0:3 | | |

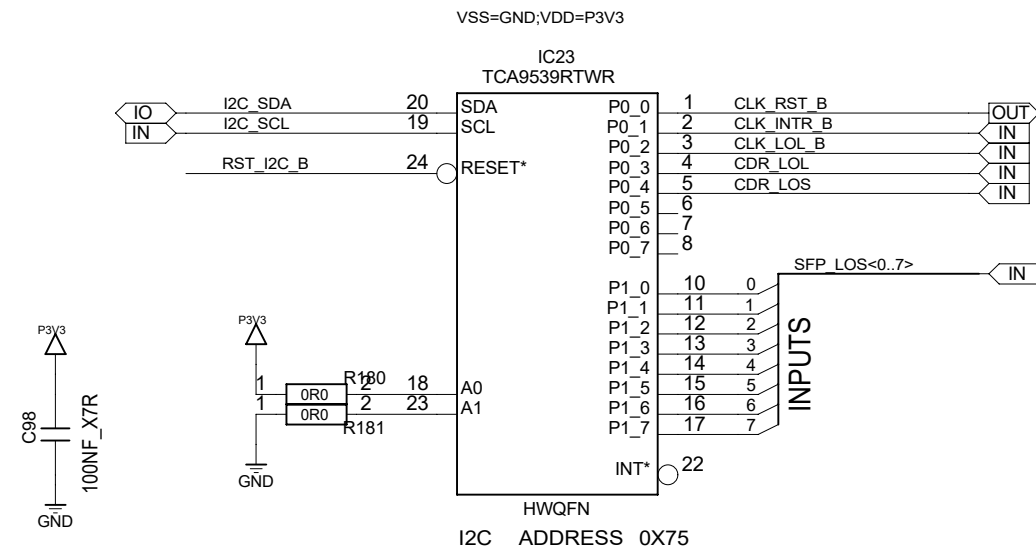
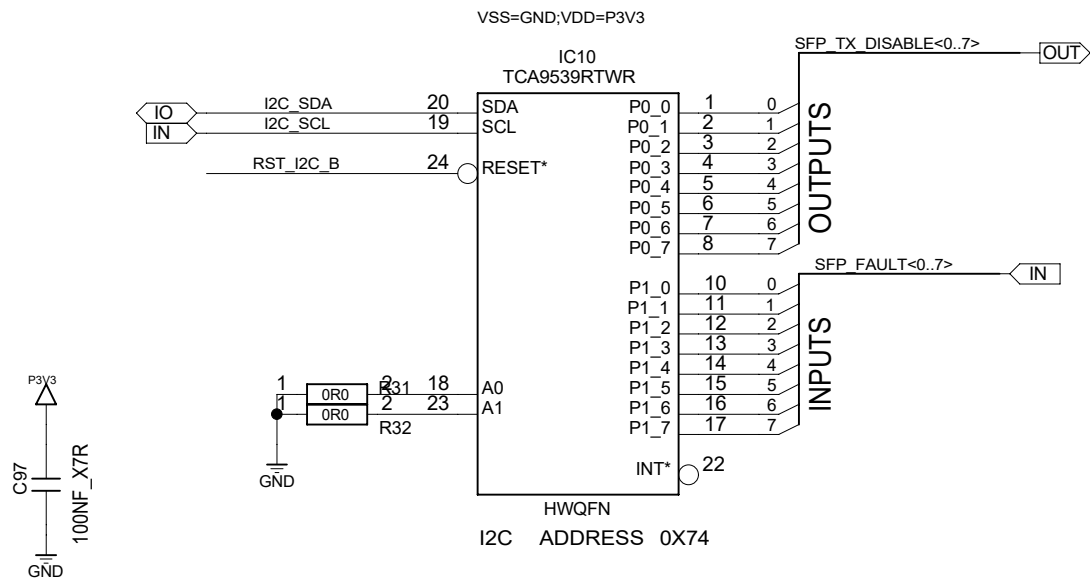


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| | MUX AND CDR | | |

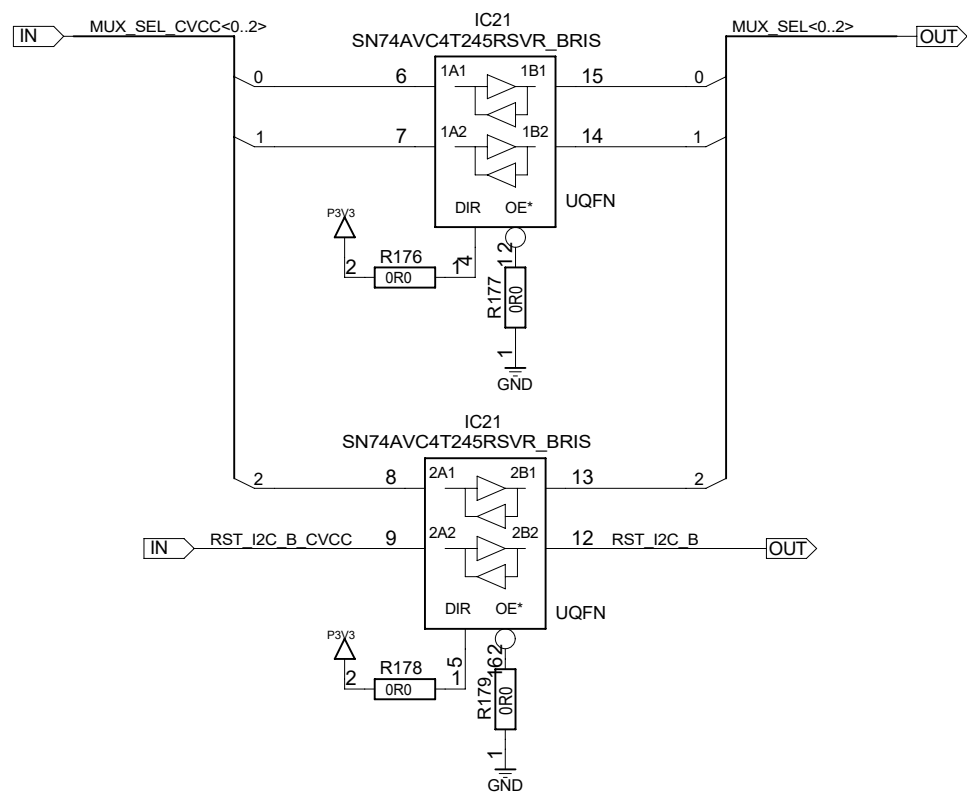




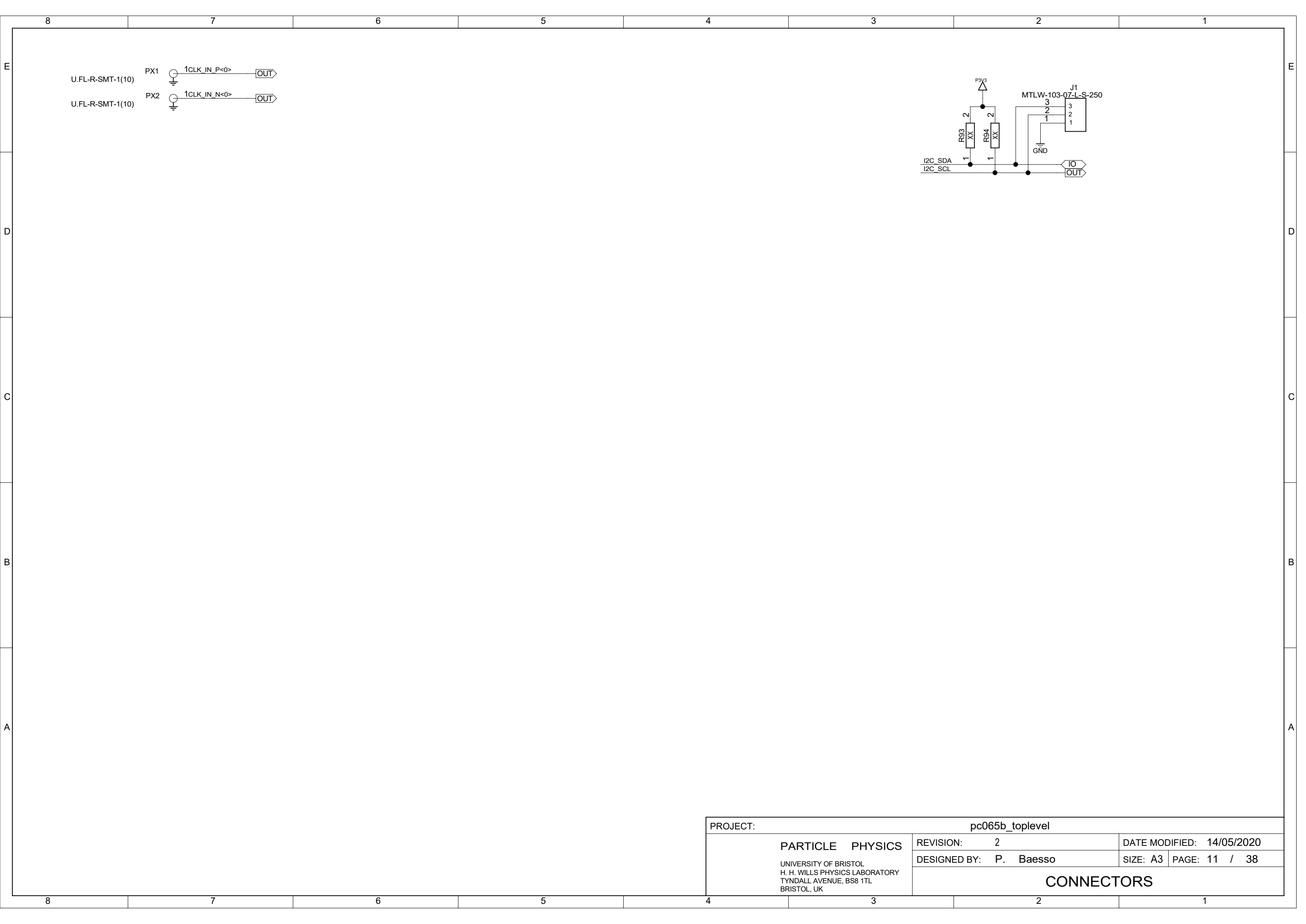
CHECK PINOUT AND FOOTPRINT!

DIR LOW: B >>> A
DIR HIGH: A >> B

VCCA=P2V5;VCCB=P3V3



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| | LEVEL TRANSLATORS | | |



PROJECT:

pc065b_toplevel

PARTICLE PHYSICS

UNIVERSITY OF BRISTOL

H. H. WILLS PHYSICS LABORATORY

TYNDALL AVENUE, BS8 1TL

BRISTOL, UK

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CONNECTORS

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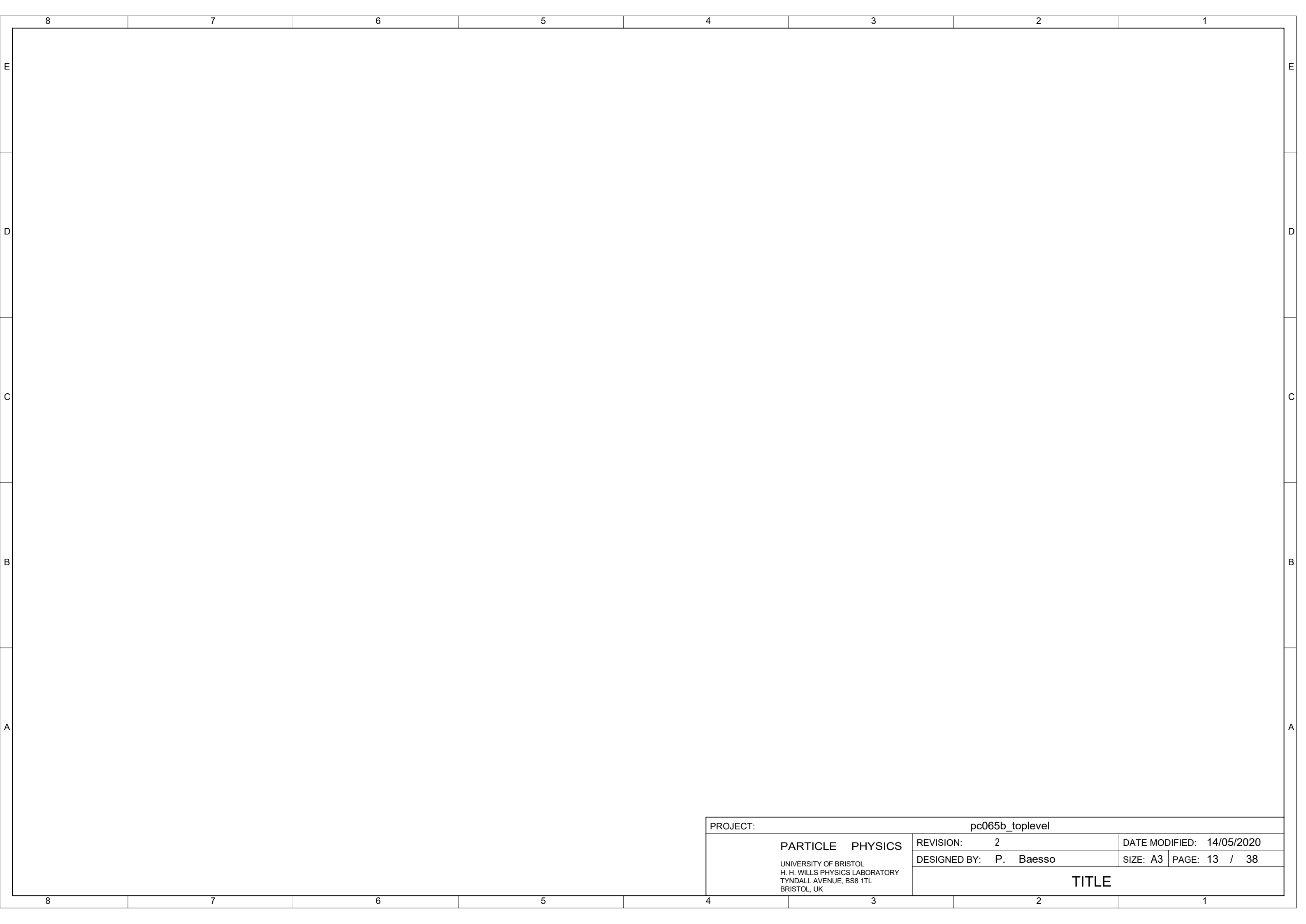
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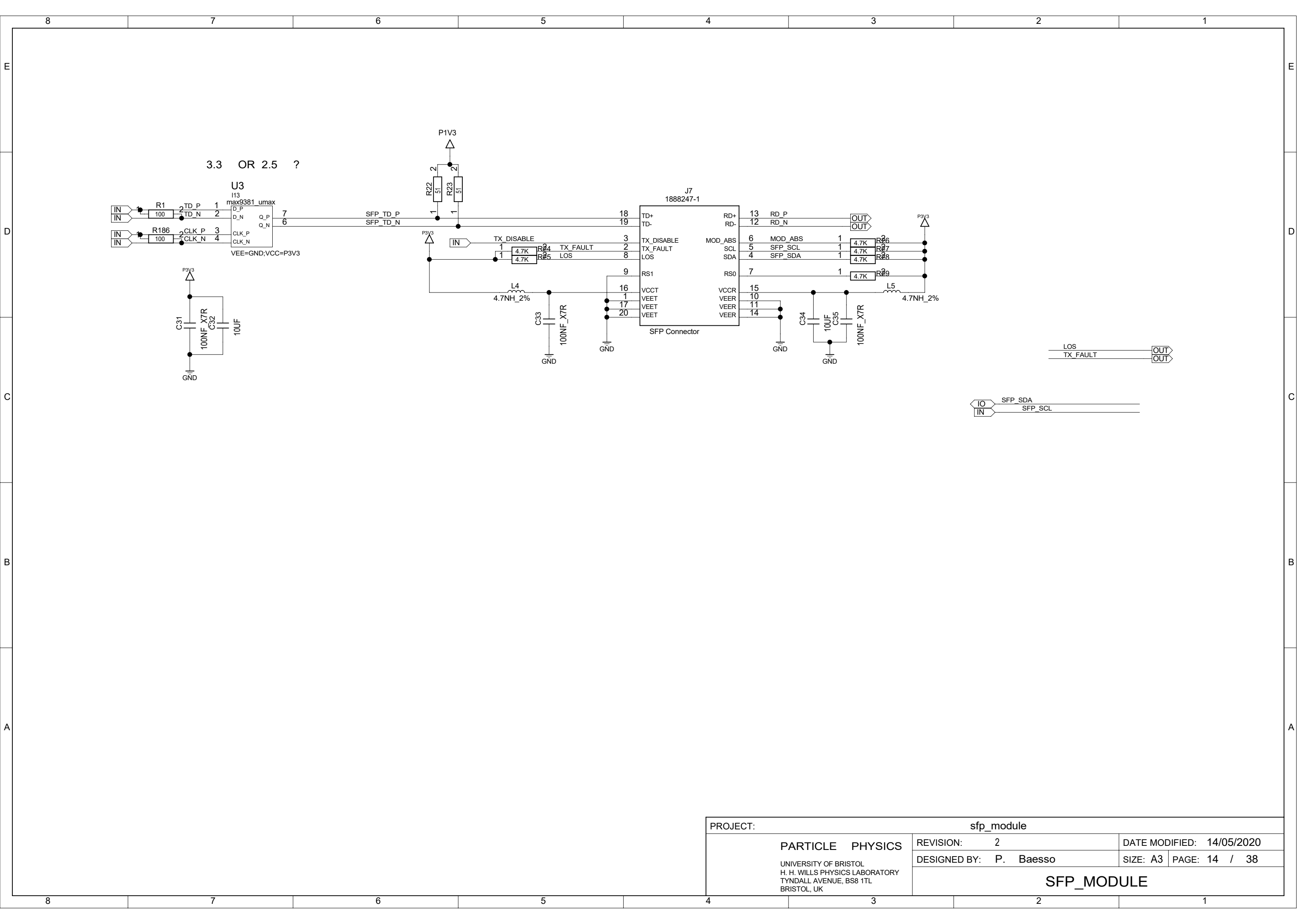
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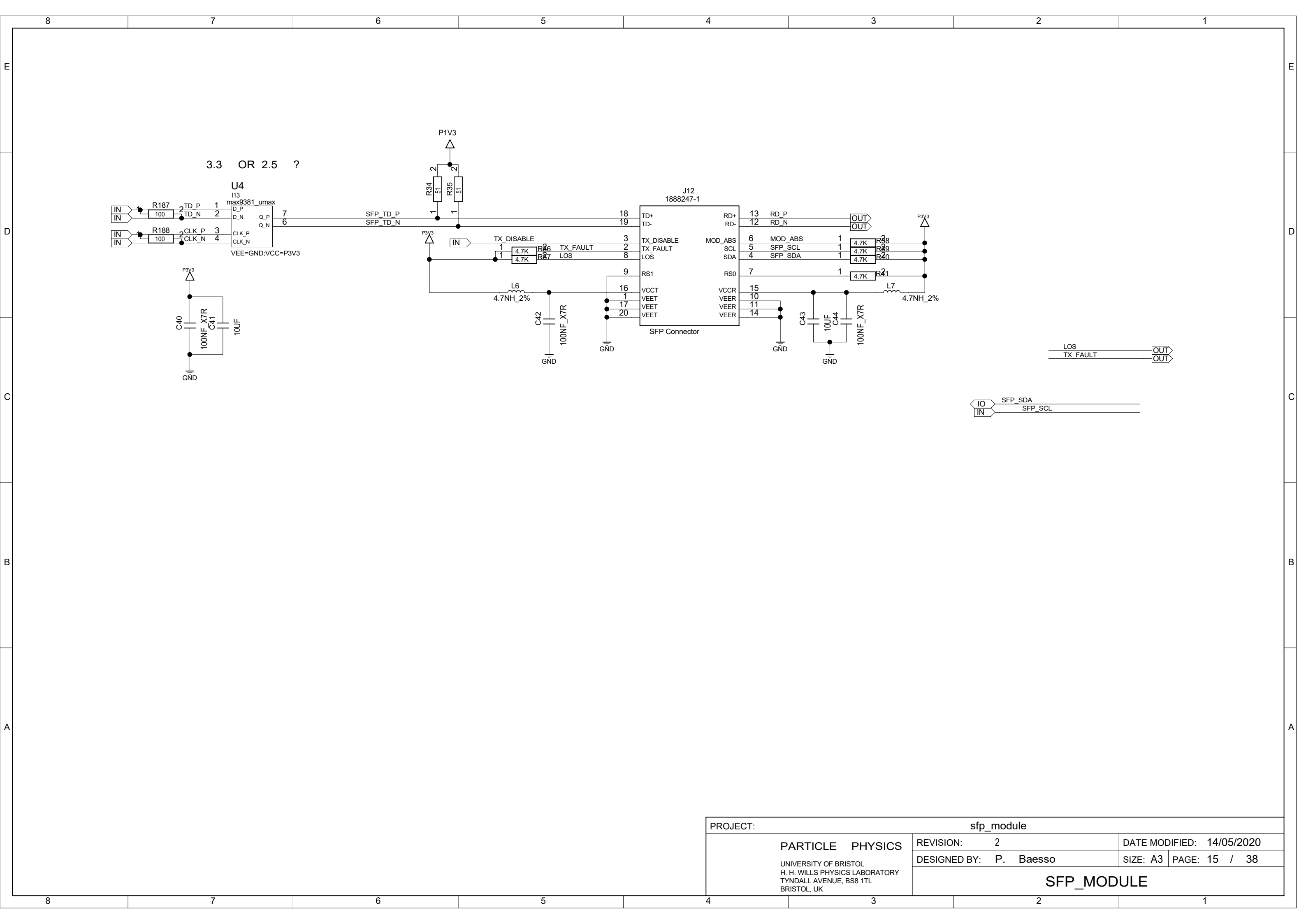
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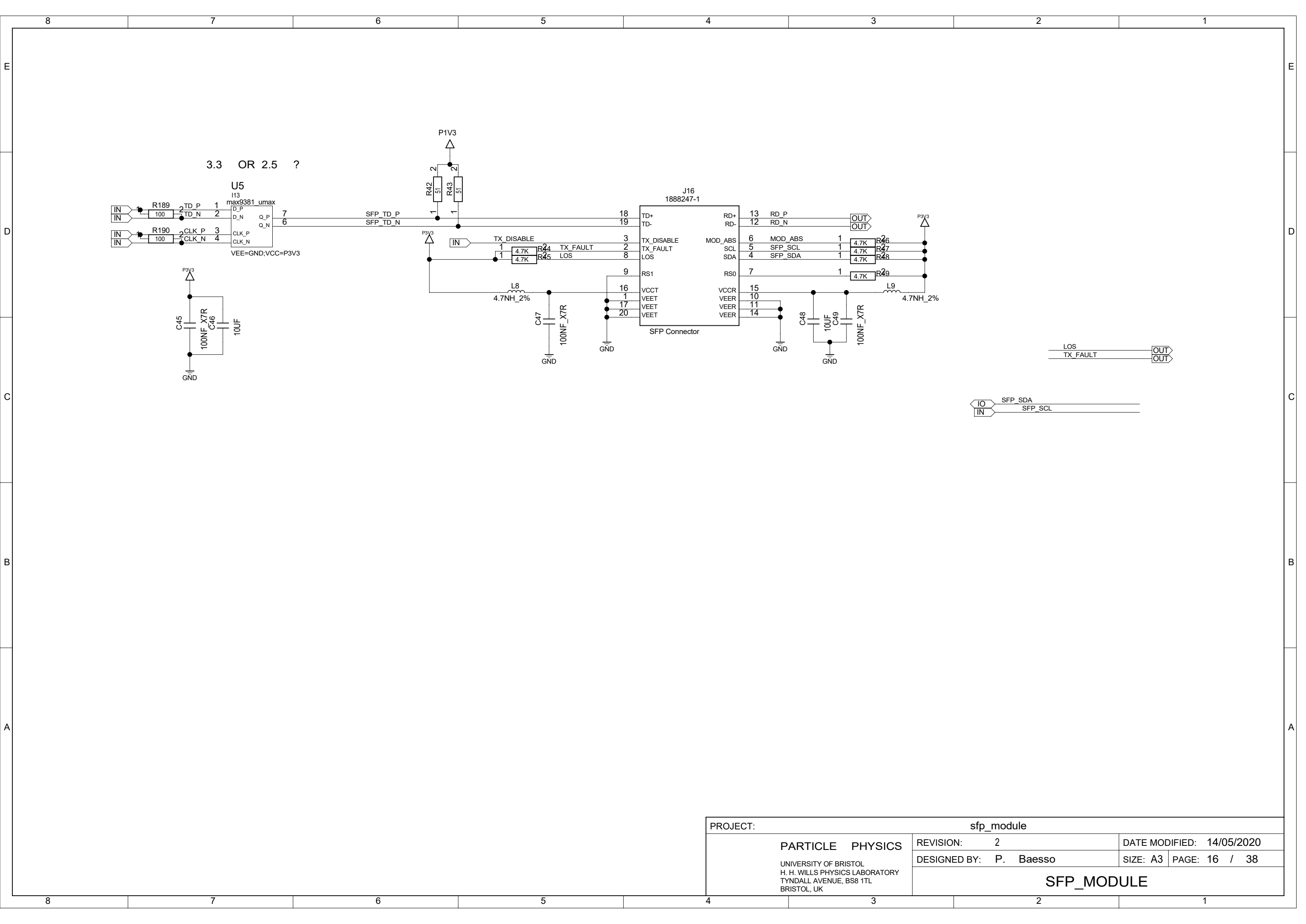
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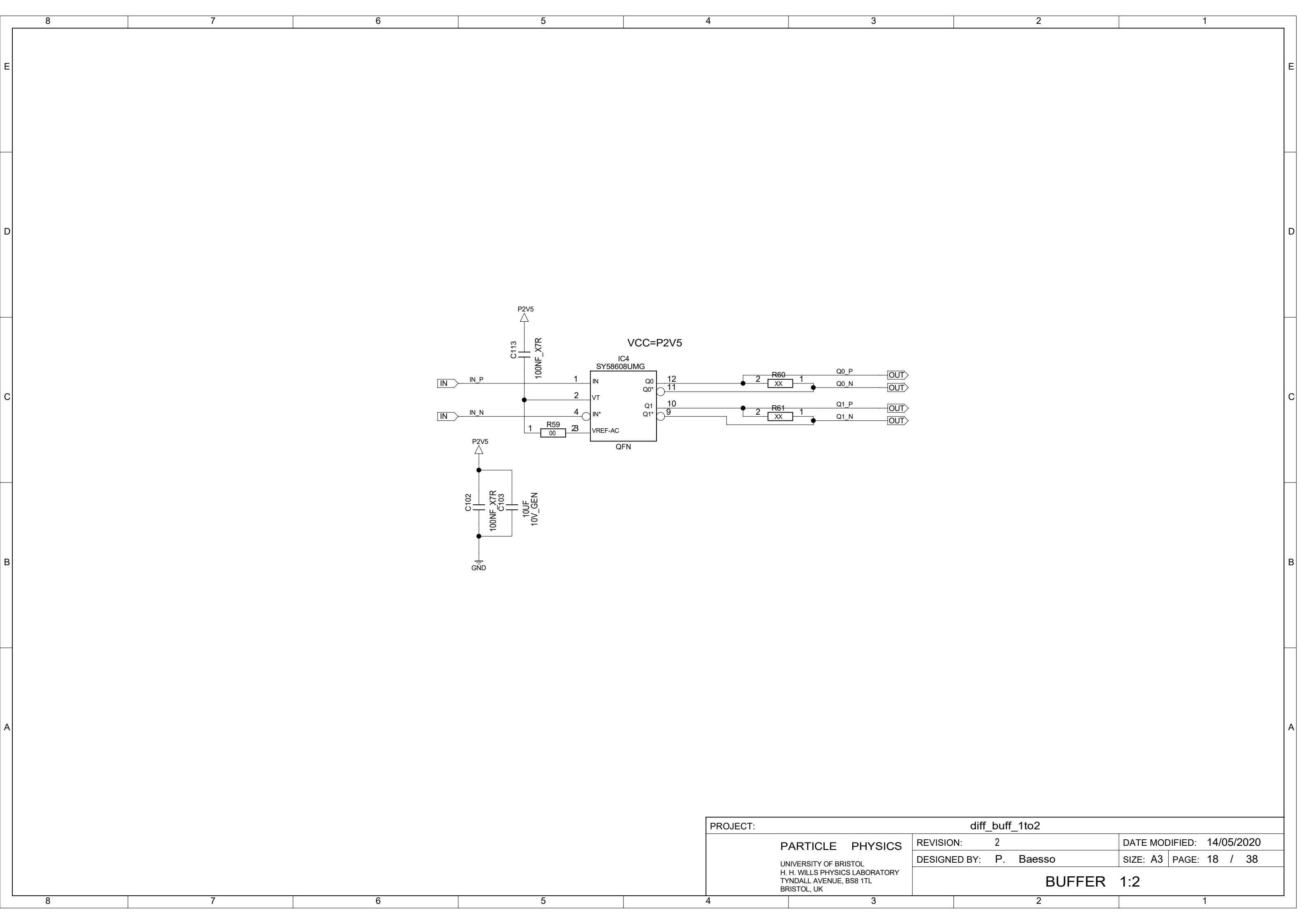
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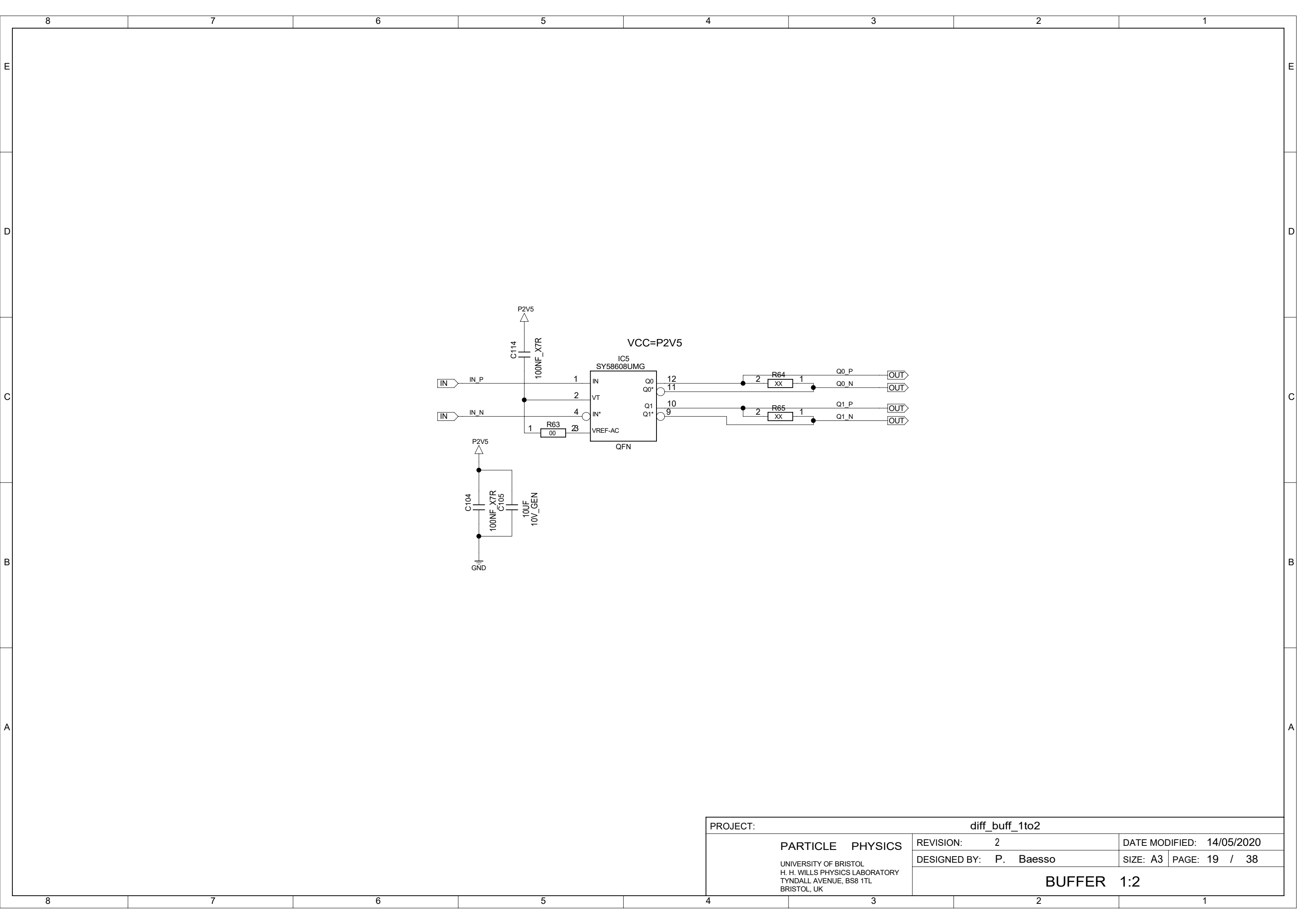
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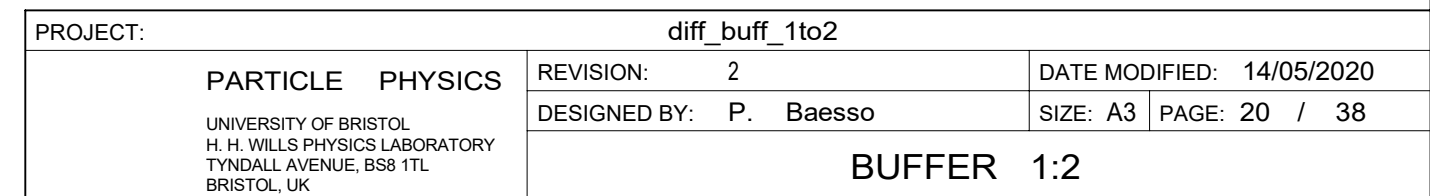


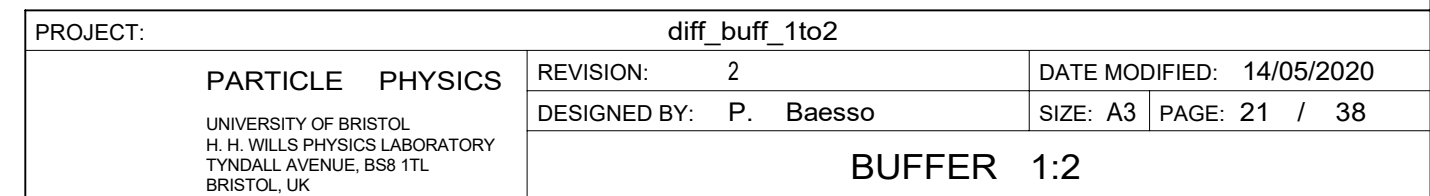
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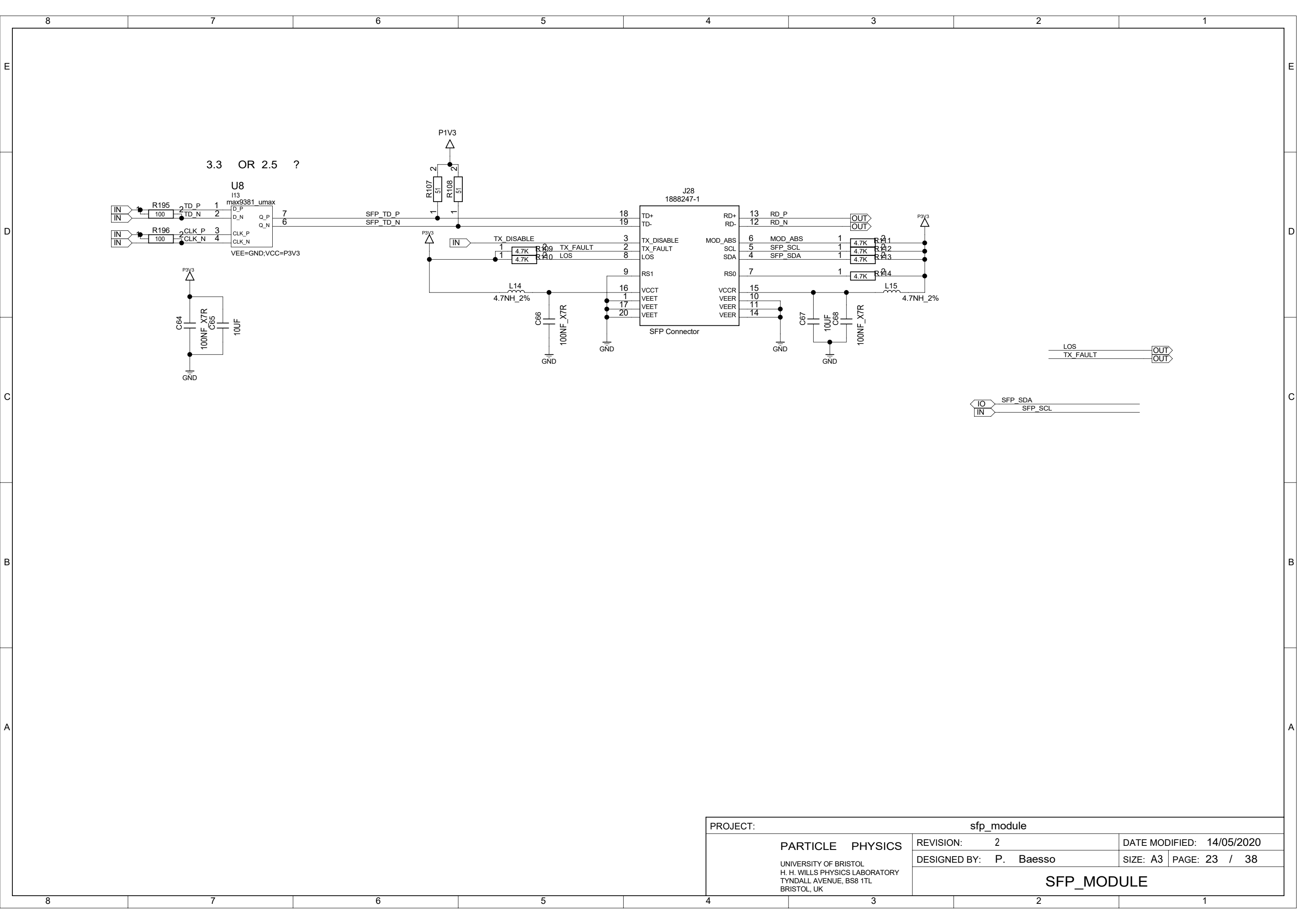


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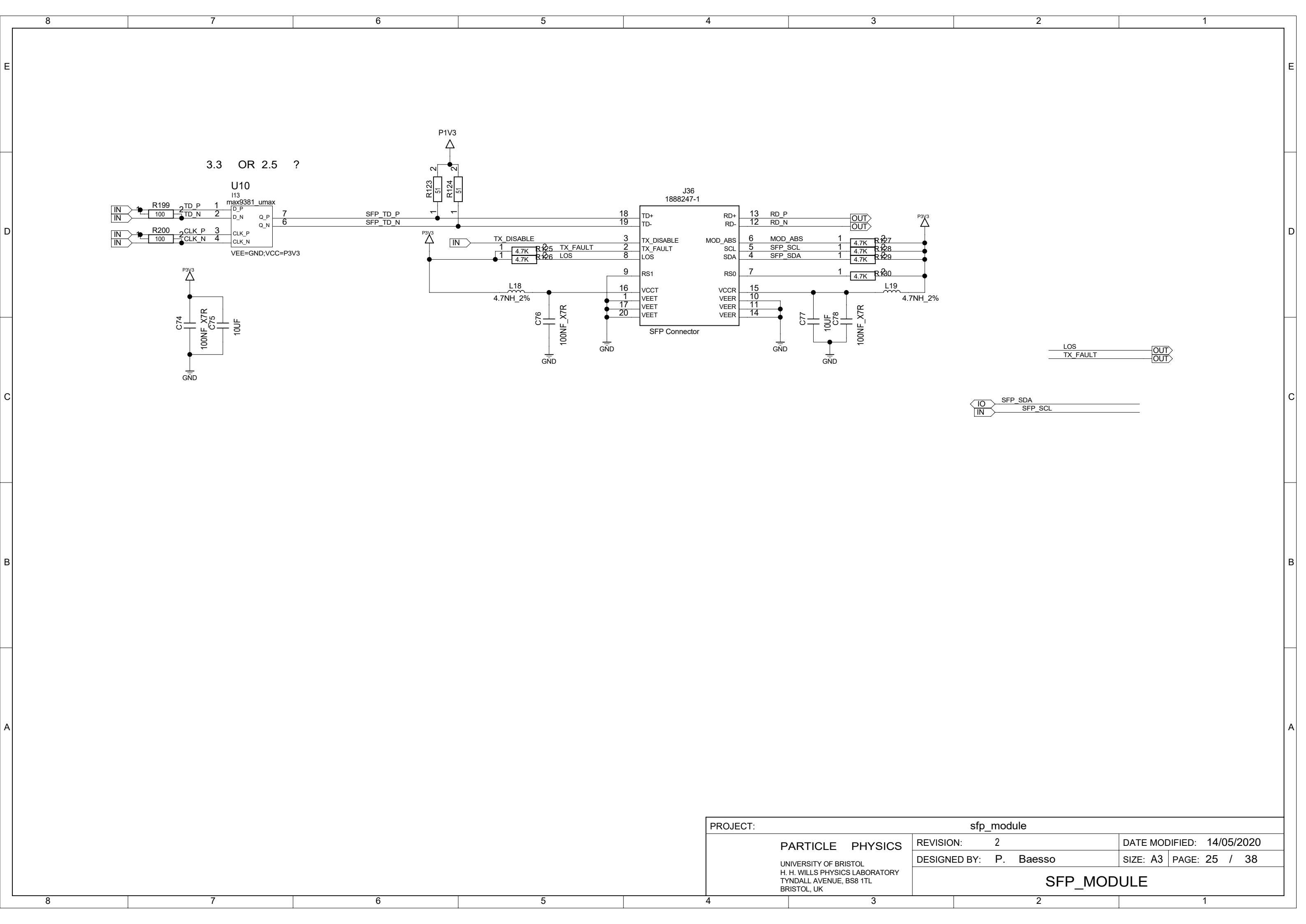




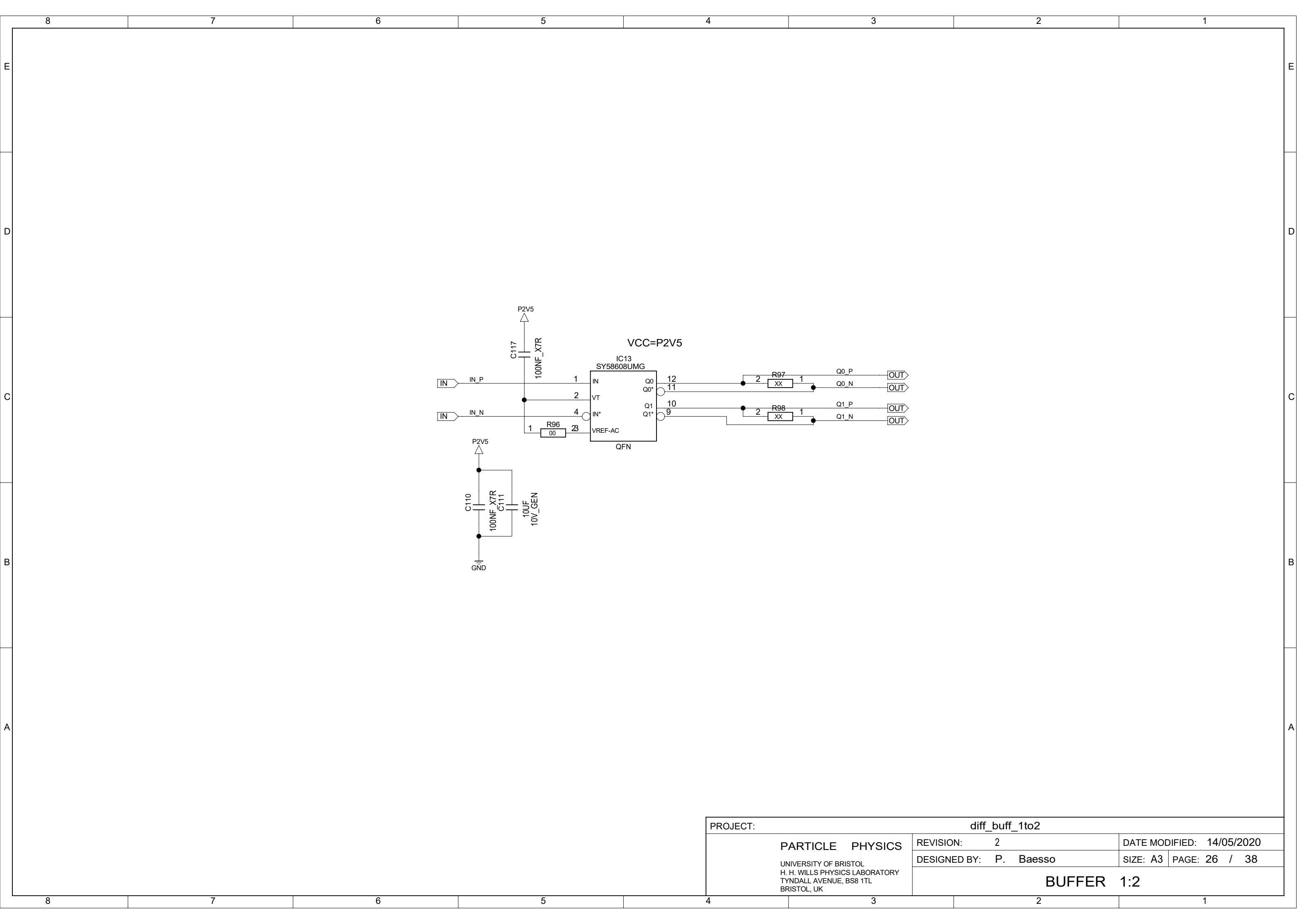




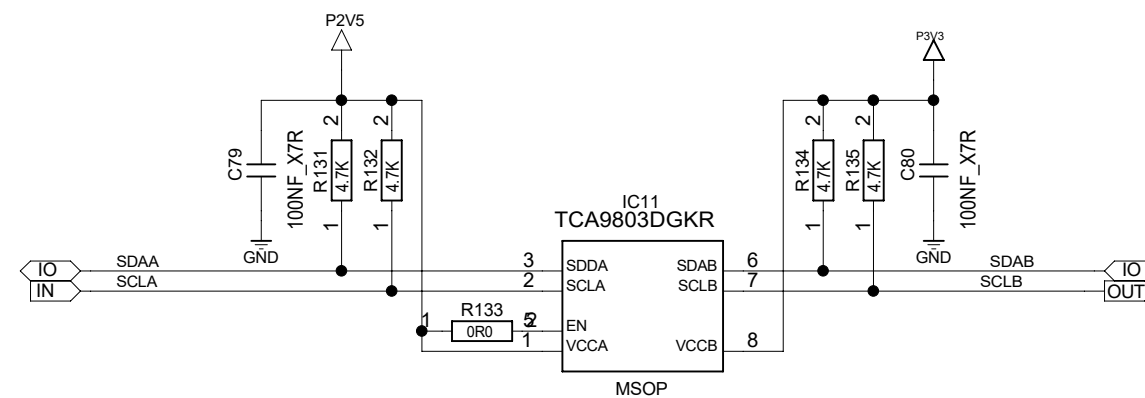
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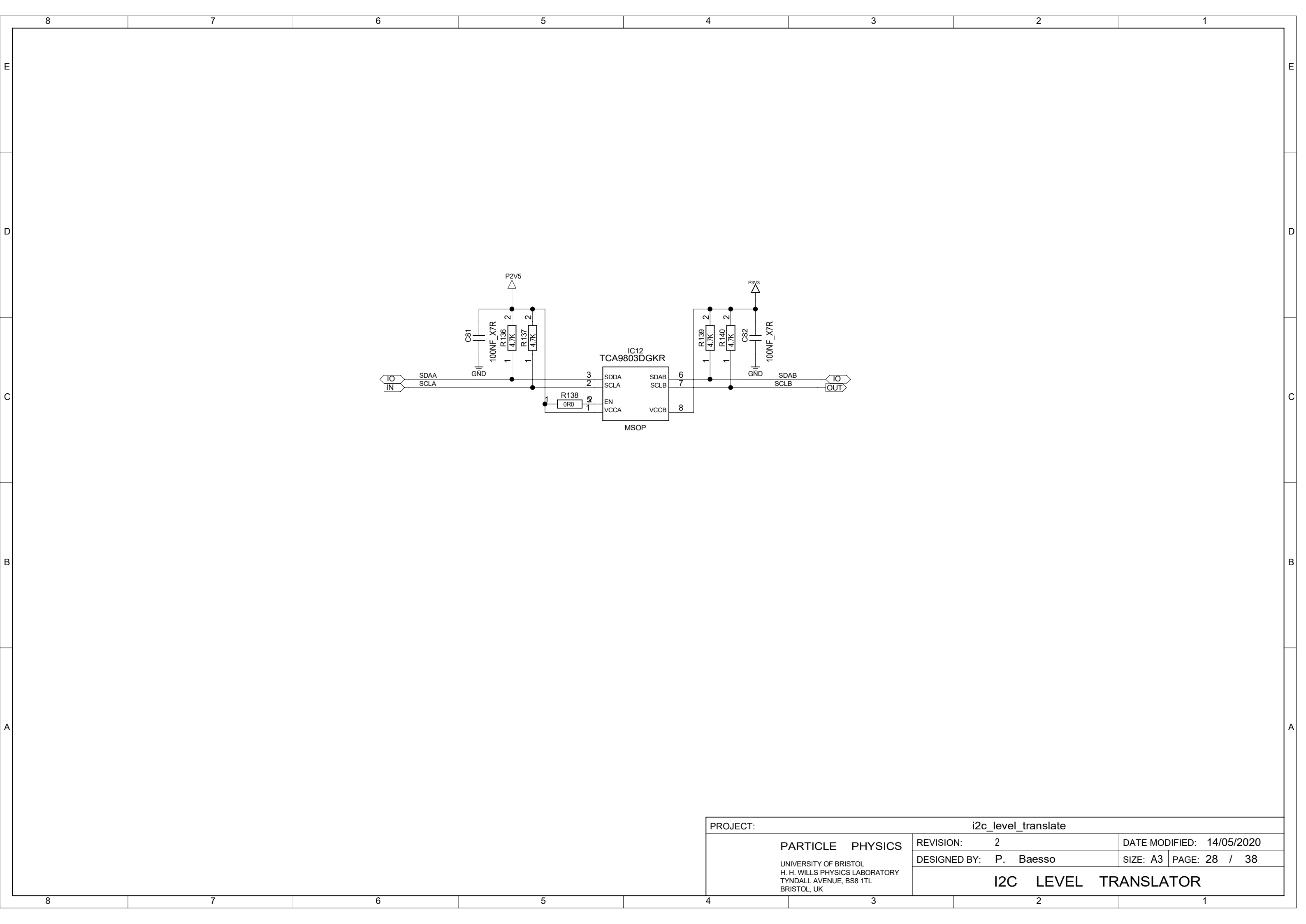
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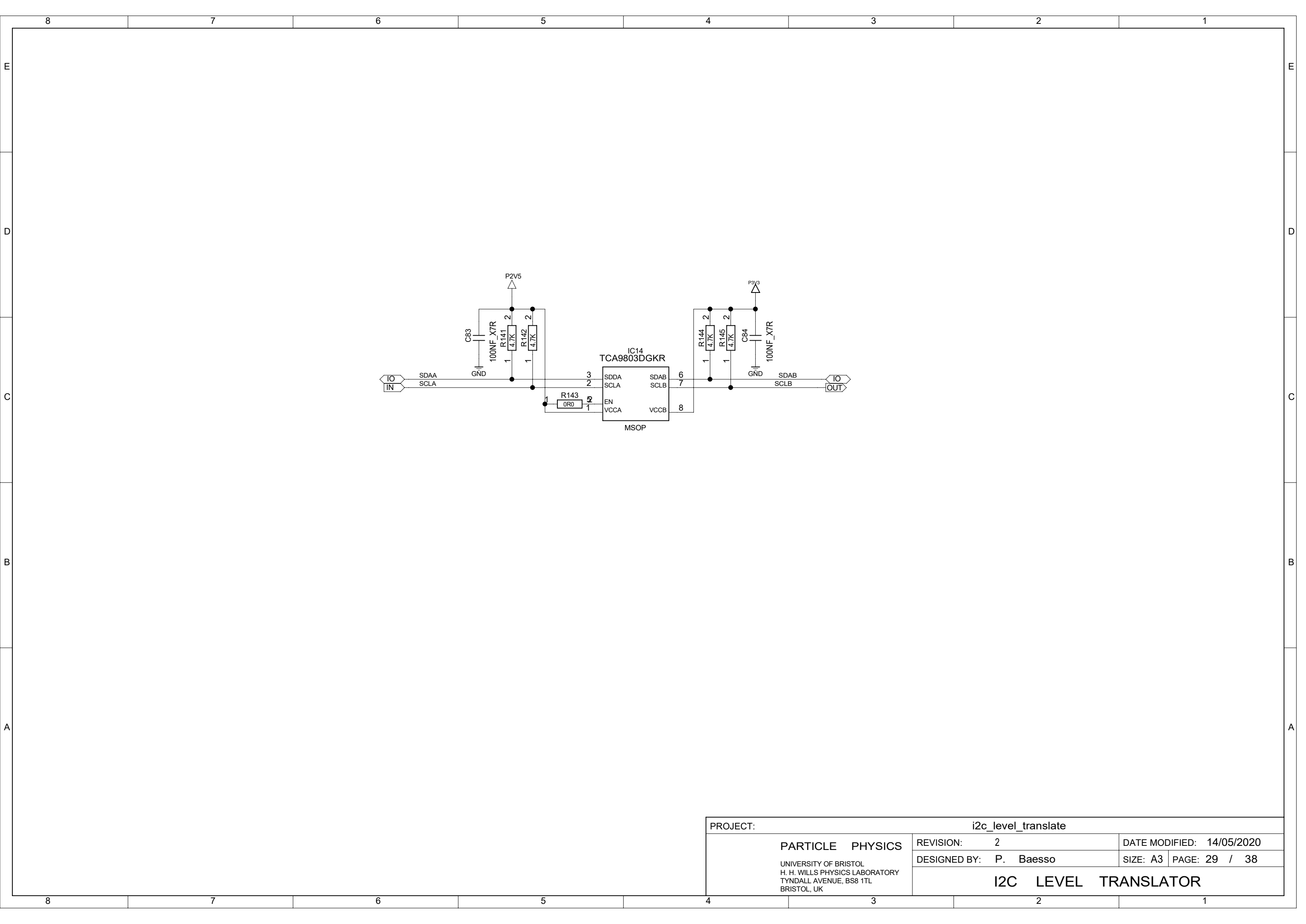
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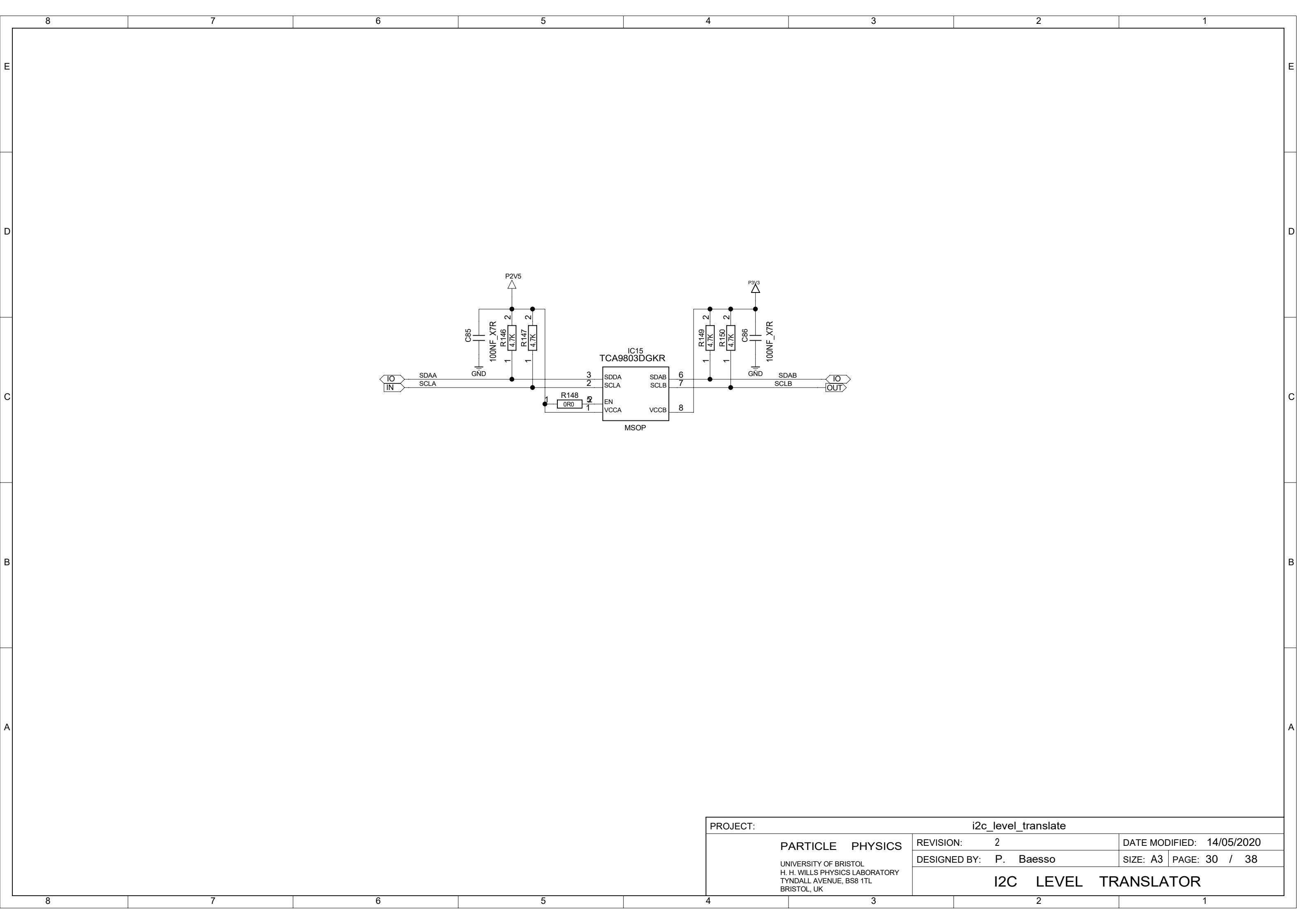
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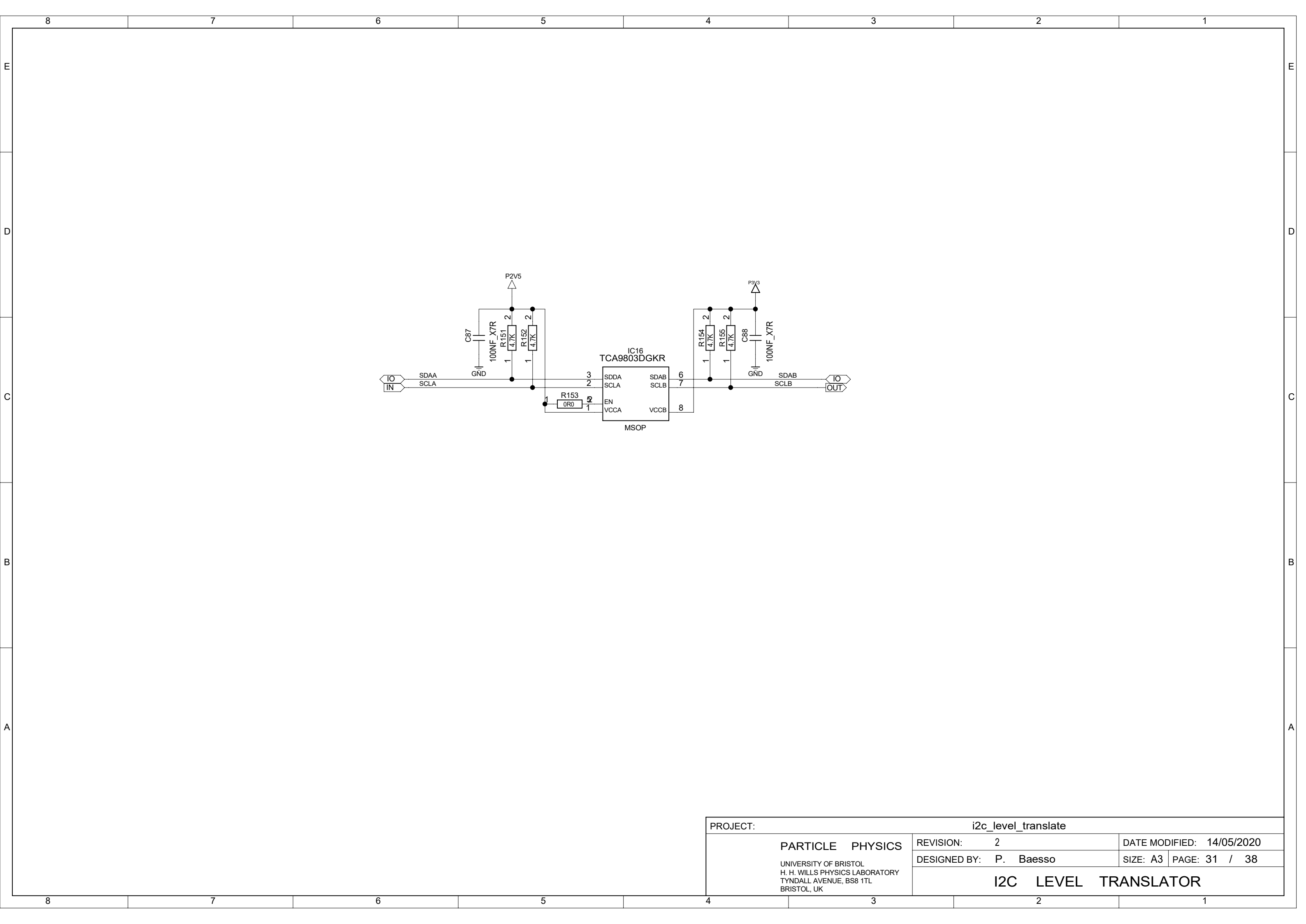
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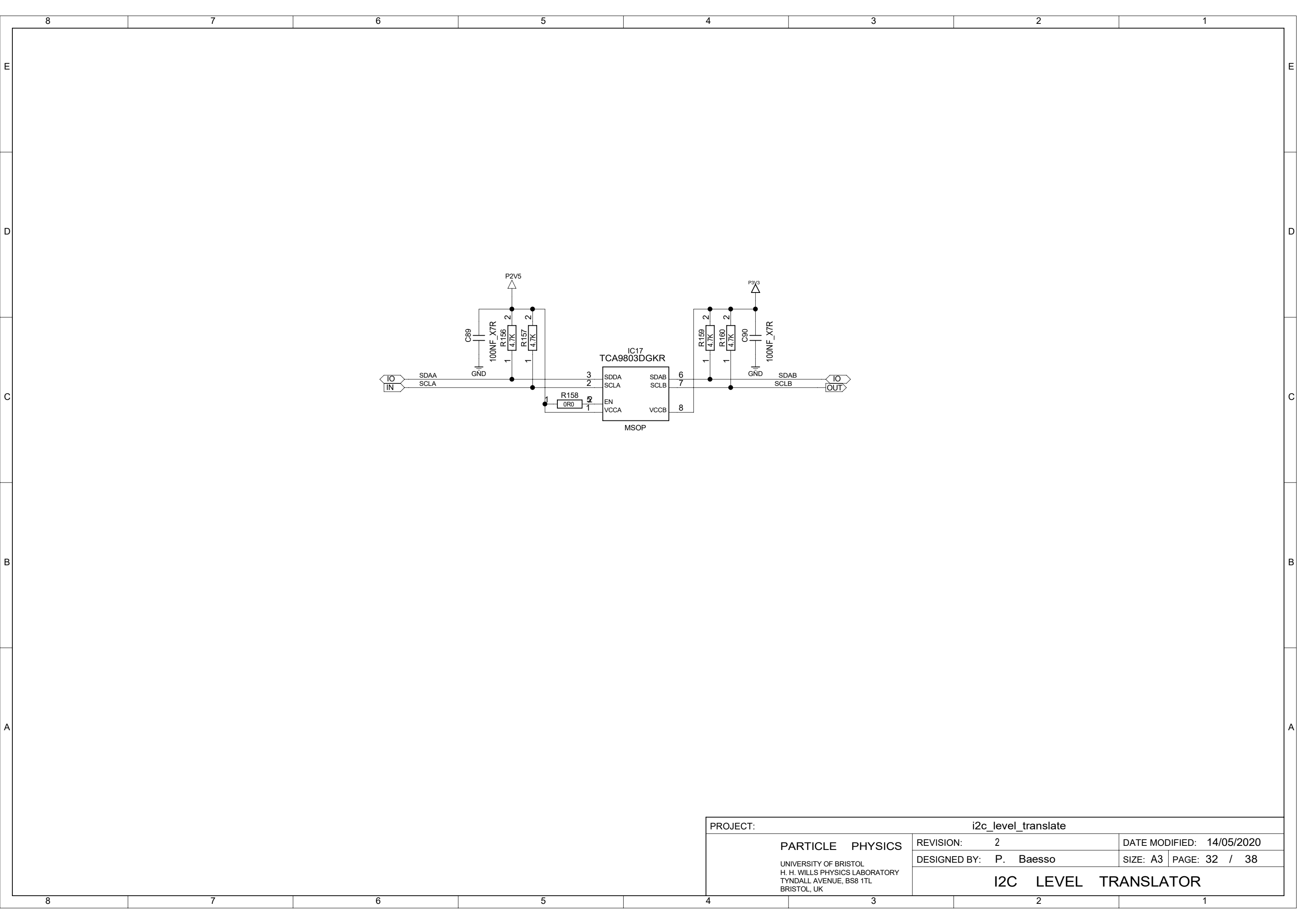
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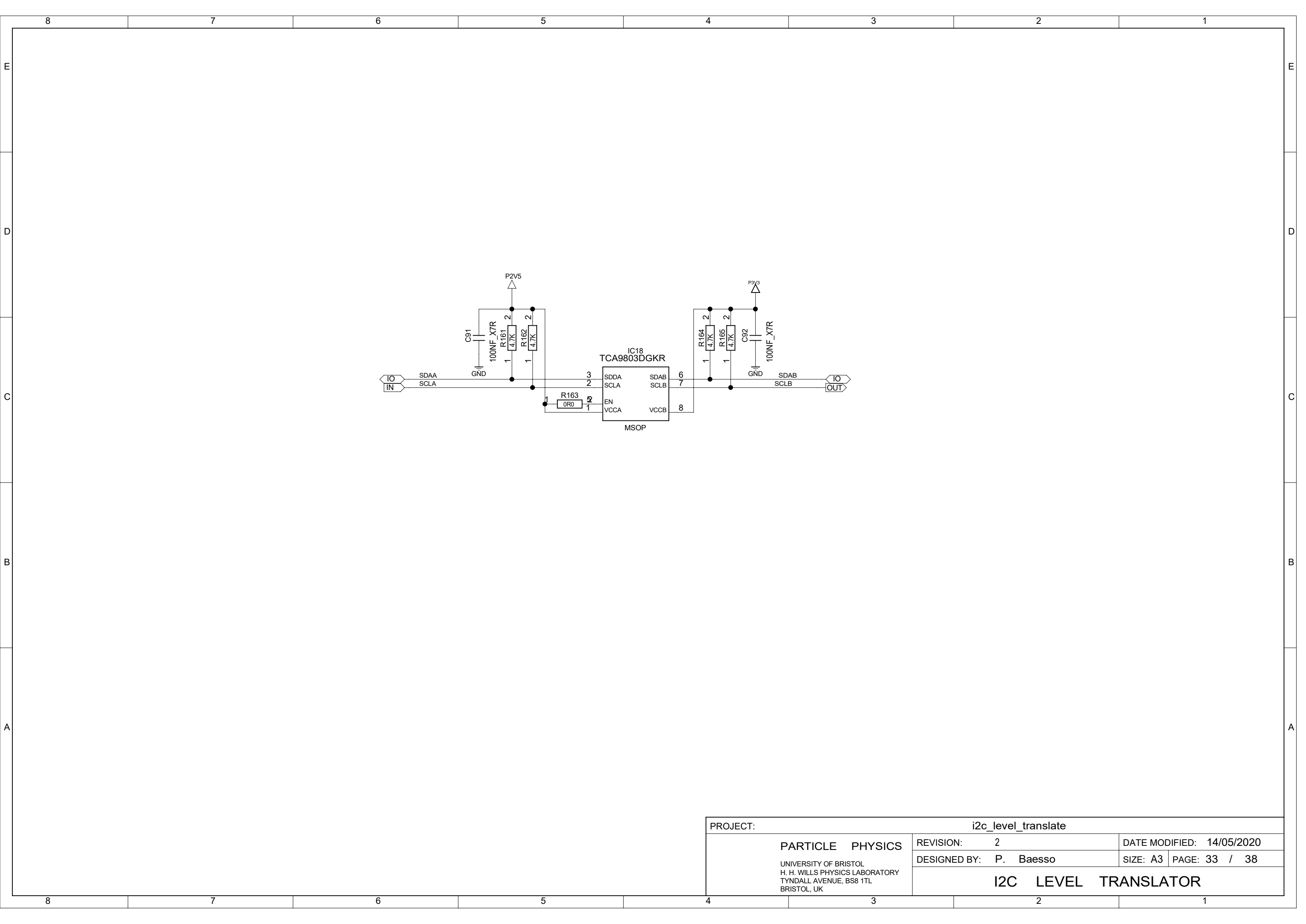
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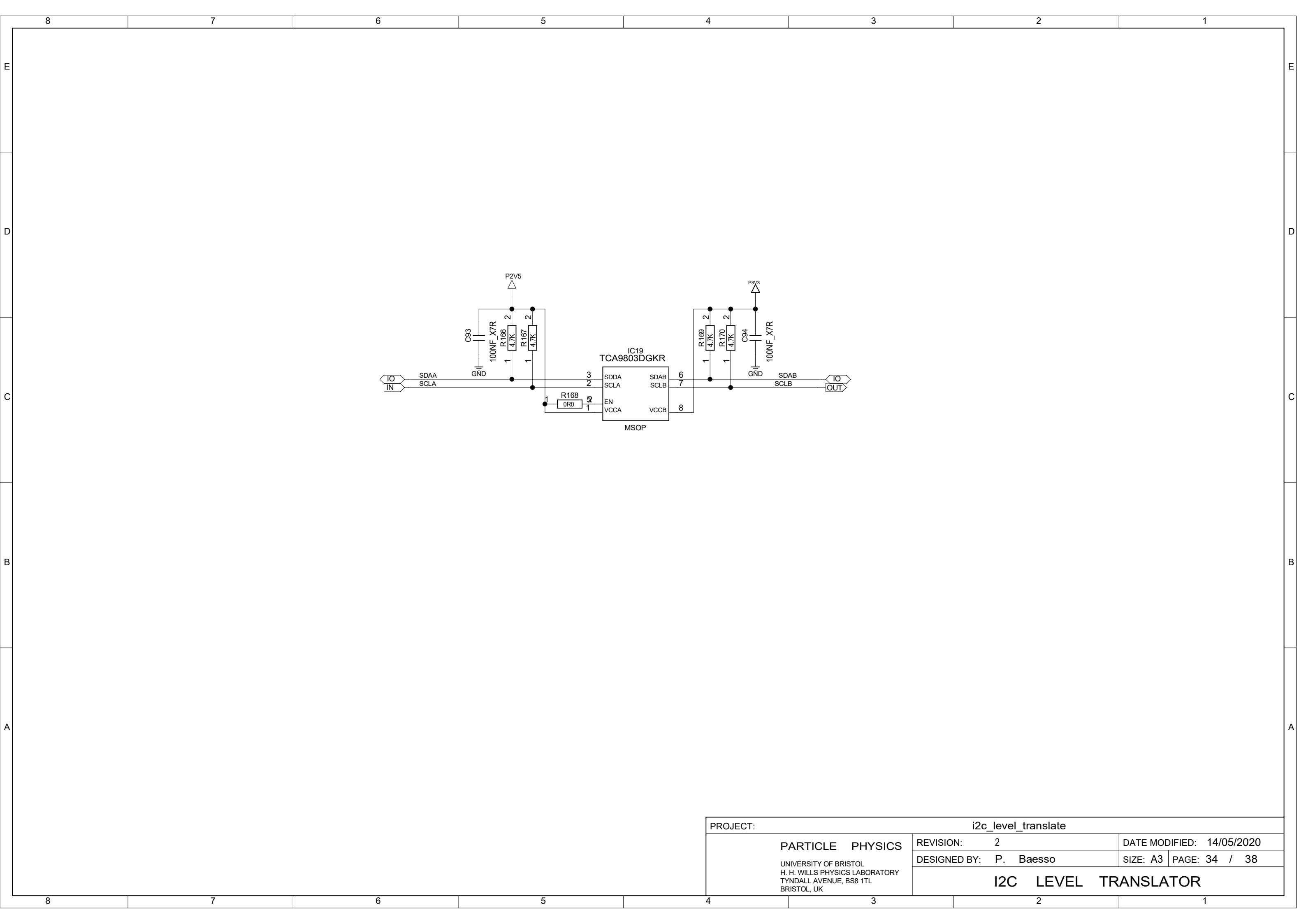
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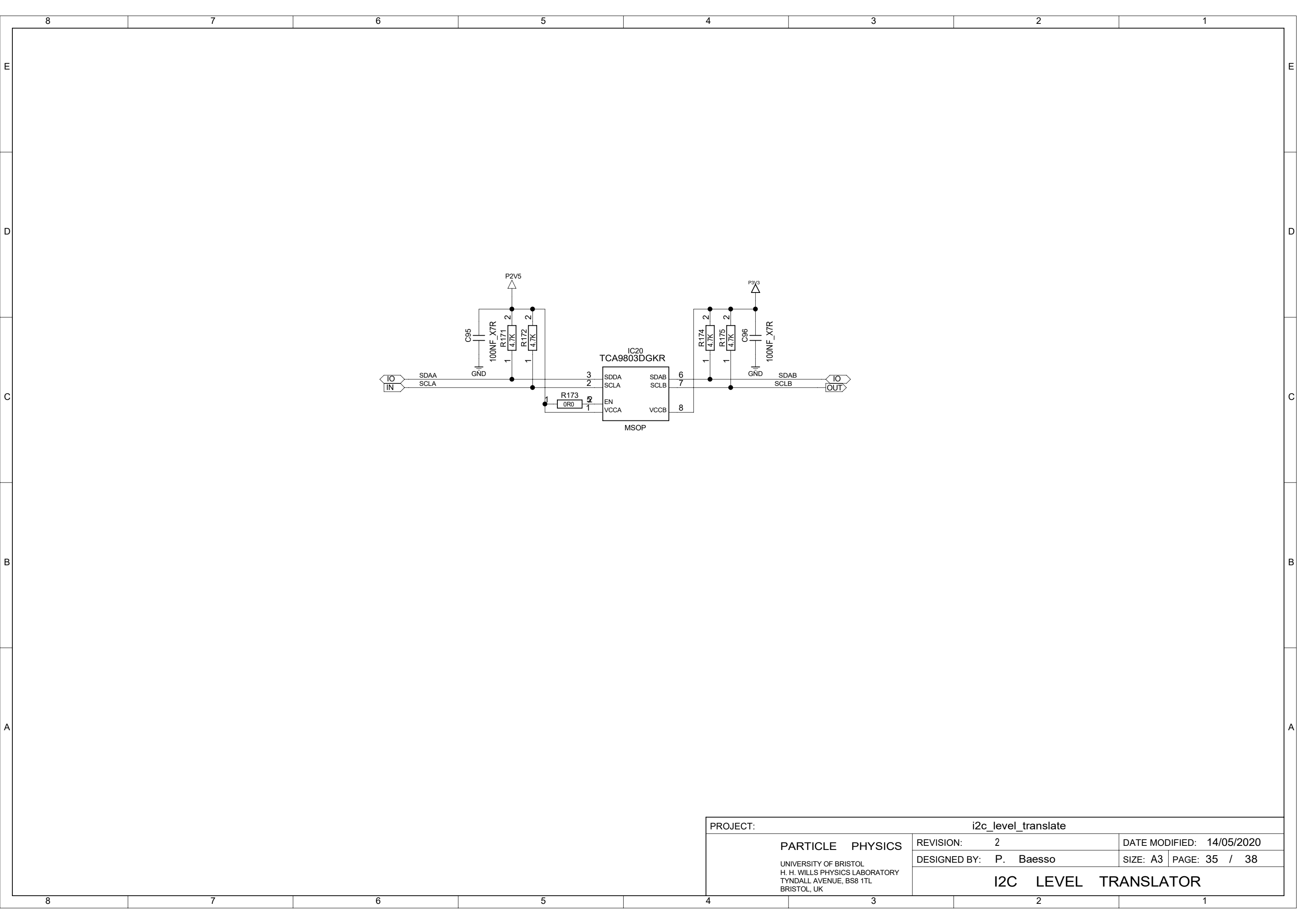
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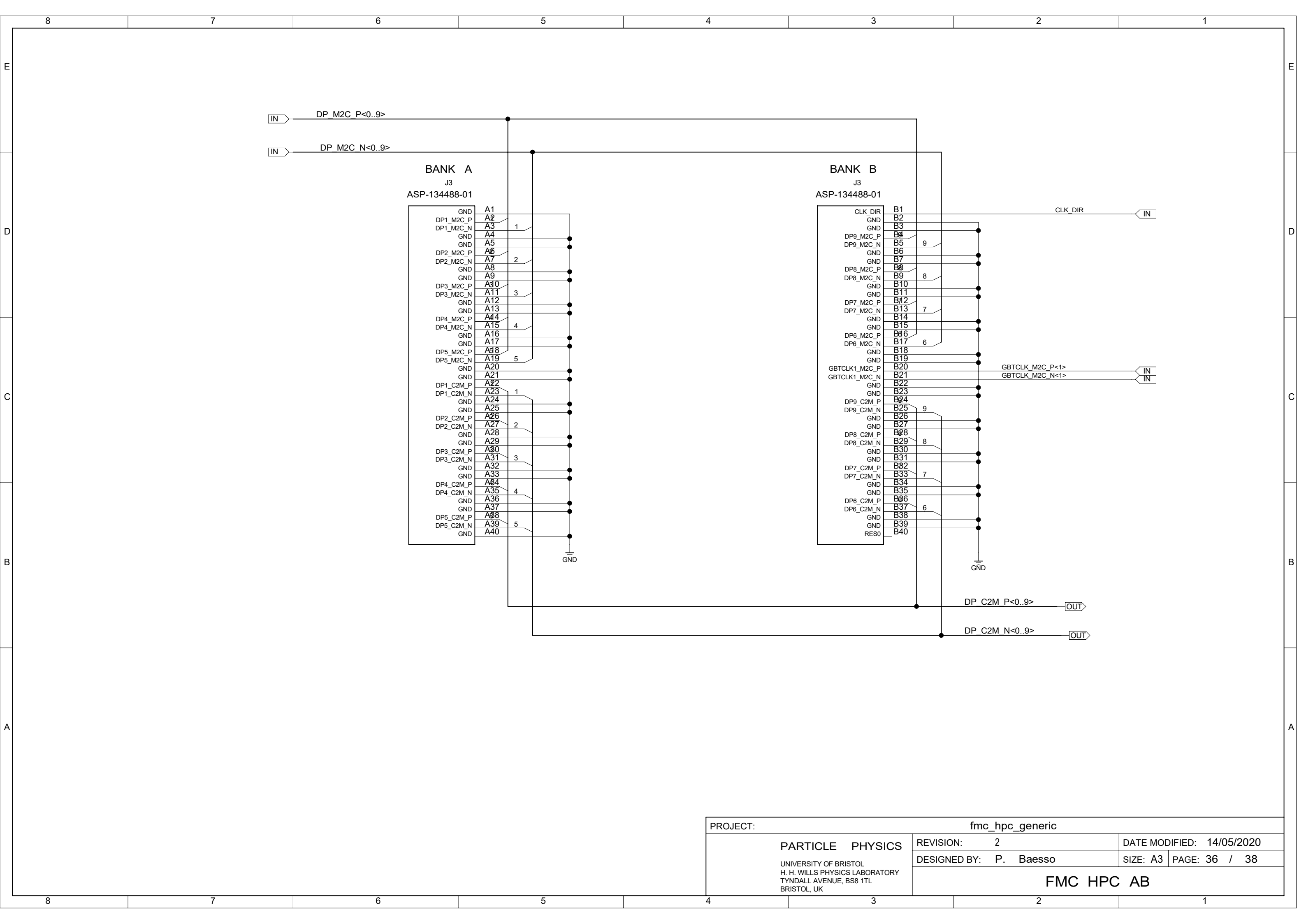
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| | | FMC HPC AB | |

