8 7 6 5 4 3 2 1

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1	TABLE OF CONTENT	pc065b_fibv2_toplevel					
2	POWER	pc065b_fibv2_toplevel					
3	FMC_CONNECTOR	pc065b_fibv2_toplevel					
4	SFP_CONNECTORS	pc065b_fibv2_toplevel					
5	SFP_INTERFACES_0:3	pc065b_fibv2_toplevel					
6	SFP_INTERFACES_4:7	pc065b_fibv2_toplevel					
7	CLOCK	pc065b_fibv2_toplevel					
8	I2C LEVEL TRANSLATORS	pc065b_fibv2_toplevel					
9	LEVEL TRANSLATORS	pc065b_fibv2_toplevel					
10	CONNECTORS	pc065b_fibv2_toplevel					
11	EEPROM	pc065b_fibv2_toplevel					
12	page1	sfp_module					
13	page1	sfp_module					
14	page1	sfp_module					
15	page1	sfp_module					

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SHEET NO.	SHEET NAME	BLOCK NAME				
16	page1	sfp_module				
17	page1	sfp_module				
18	page1	sfp_module				
19	page1	sfp_module				
20	I2C LEVEL TRANSLATOR	i2c_level_translate				
21	I2C LEVEL TRANSLATOR	i2c_level_translate				
22	I2C LEVEL TRANSLATOR	i2c_level_translate				
23	I2C LEVEL TRANSLATOR	i2c_level_translate				
24	I2C LEVEL TRANSLATOR	i2c_level_translate				
25	I2C LEVEL TRANSLATOR	i2c_level_translate				
26	I2C LEVEL TRANSLATOR	i2c_level_translate				
27	I2C LEVEL TRANSLATOR	i2c_level_translate				
28	I2C LEVEL TRANSLATOR	i2c_level_translate				
29	AB	fmc_hpc_generic				
30	CDGH	fmc_hpc_generic				

I2C ADDRESSES
EEPROM 0X50 (ALTERNATIVE: 0X51)
CDR 0X60 (ALTERNATIVE: 0X40)
CLOCK 0X68 (0X69, 0X6A, 0X6B)
EXPANDER 0X74
EXPANDER 0X75

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SHEET NO.	SHEET NAME	BLOCK NAME
31	EFJK	fmc_hpc_generic

PROJECT:	pc065b_fibv2_toplevel												
	PARTICLE PHYSIC		REVISION:		2	DATE MODIFIED: 14/				14/0	05/2020		
UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORA' TYNDALL AVENUE, BS8 1TL BRISTOL, UK	UNIVERSITY OF BE	ERSITY OF BRISTOI	DESIGNED	BY:	P.	Baesso		SIZE: A3	PAGE:	1 /	31		
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