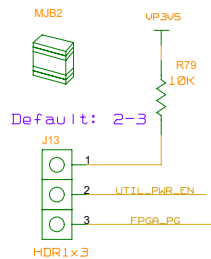
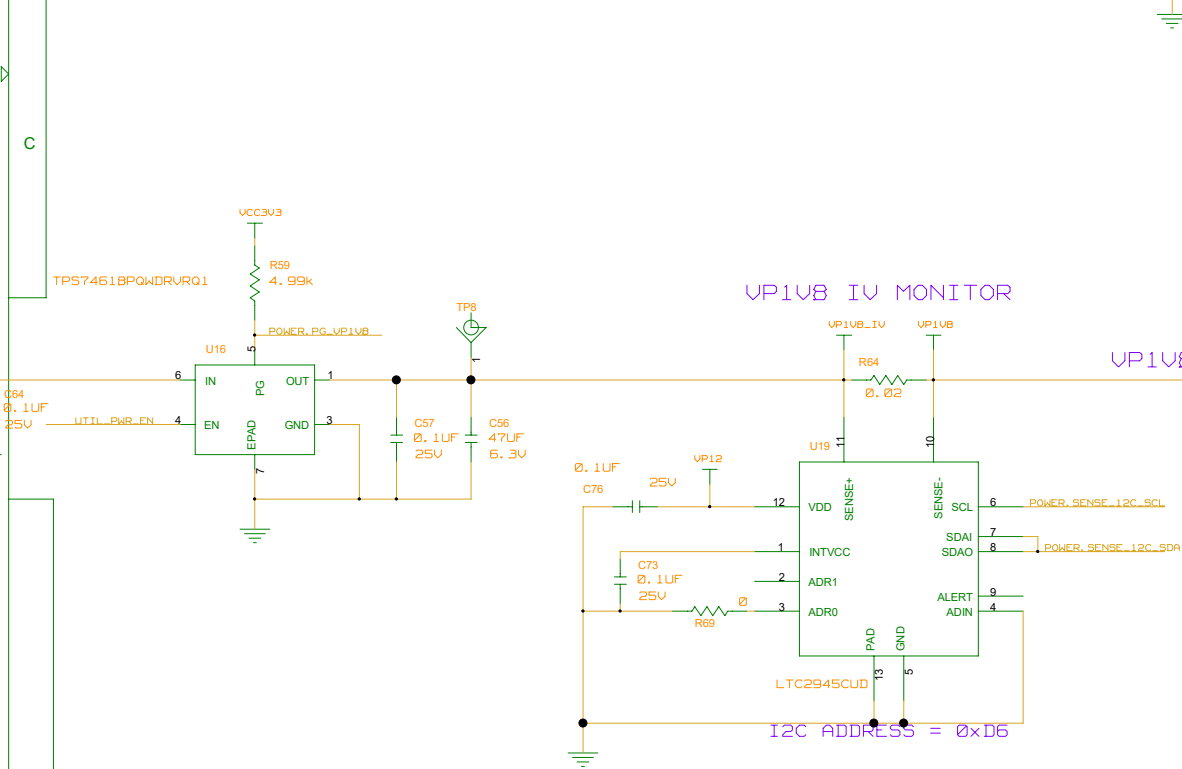
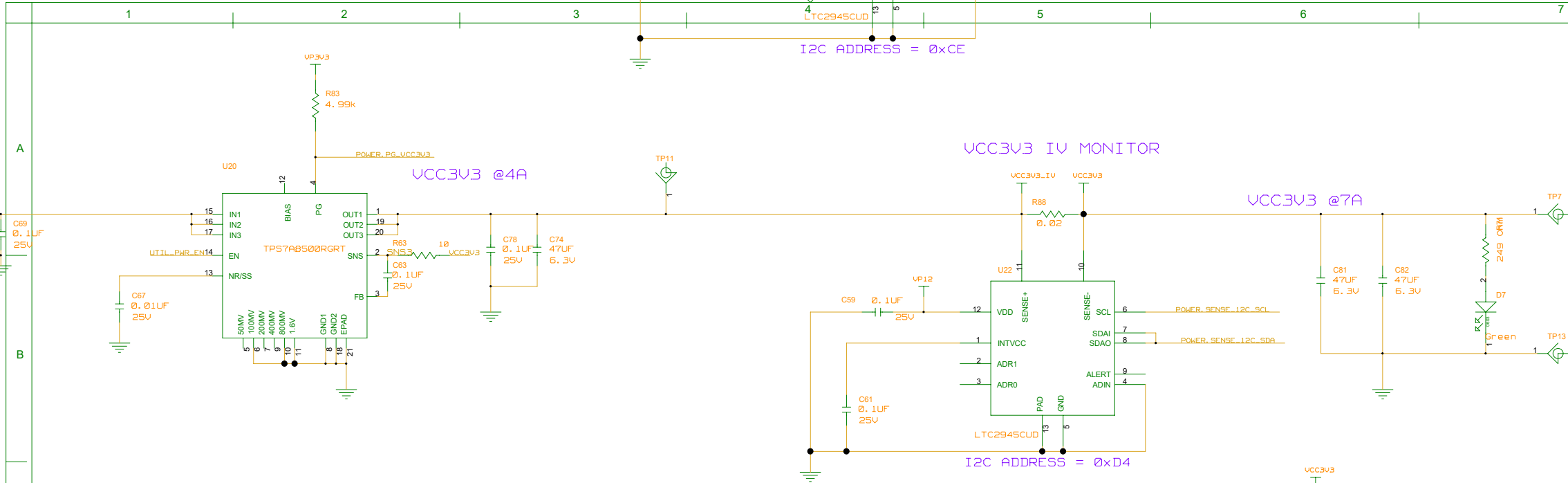
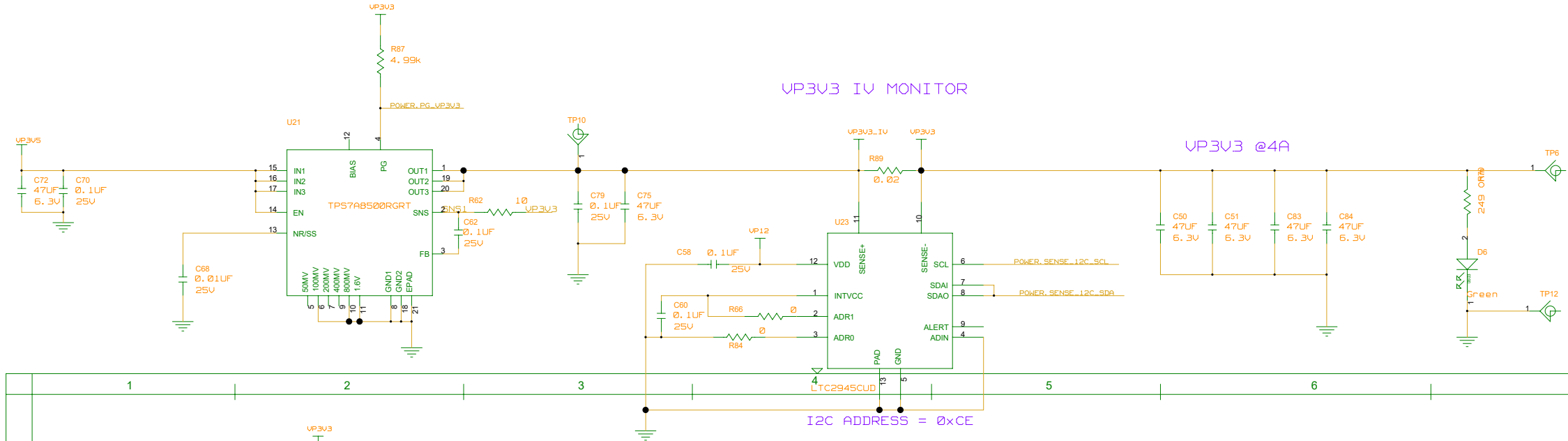


N.B. THIS IS A DRAWING, NO CONNECTIVITY

|                           |                          |   |                     |       |              |        |
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|                           | Fri Jan 20 14:05:19 2023 |   |                     |       |              |        |
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| HIGH ENERGY PHYSICS GROUP |                          |   |                     |       |              |        |
| TITLE                     |                          | mtca_interface_board_reocc                            |                     |       |              |        |
| MODULE:                   |                          | top   |                     |       |              |        |
| A 2                       |                          | MODULE PAGE: 1 OF 2                                   |                     |       |              |        |
|                           |                          | OVERALL PAGE: 1 OF 15                                 |                     |       |              |        |
|                           |                          |   | TOTAL NO. OF SHEETS |       |              |        |



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UTILITY PWR FORCE ENABLE

|         |      |         |         |       |       |        |
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TITLE mtca\_interface\_board\_reocc

MODULE: 02\_ power

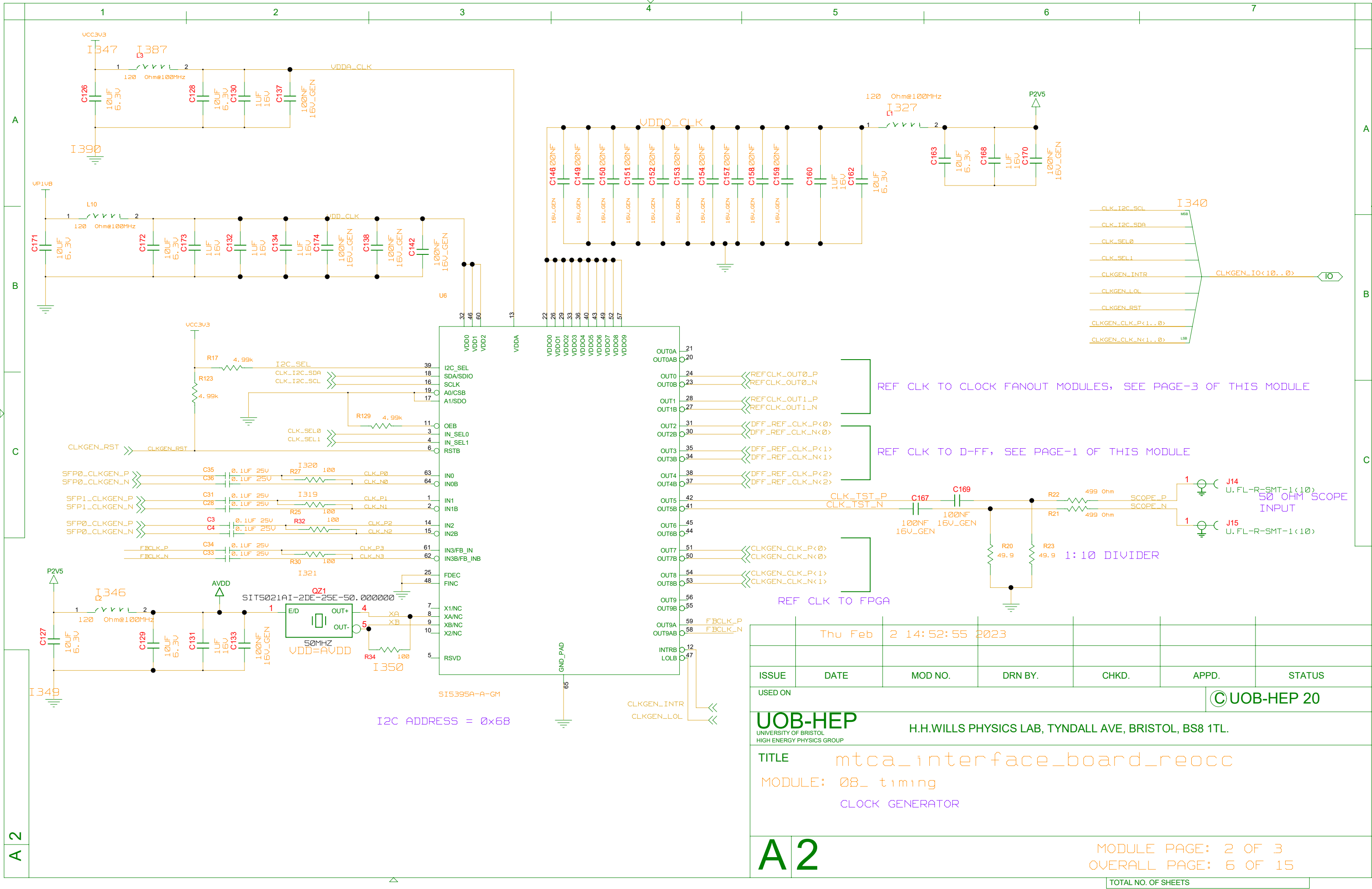
A2

MODULE PAGE: 2 OF 2  
OVERALL PAGE: 4 OF 15

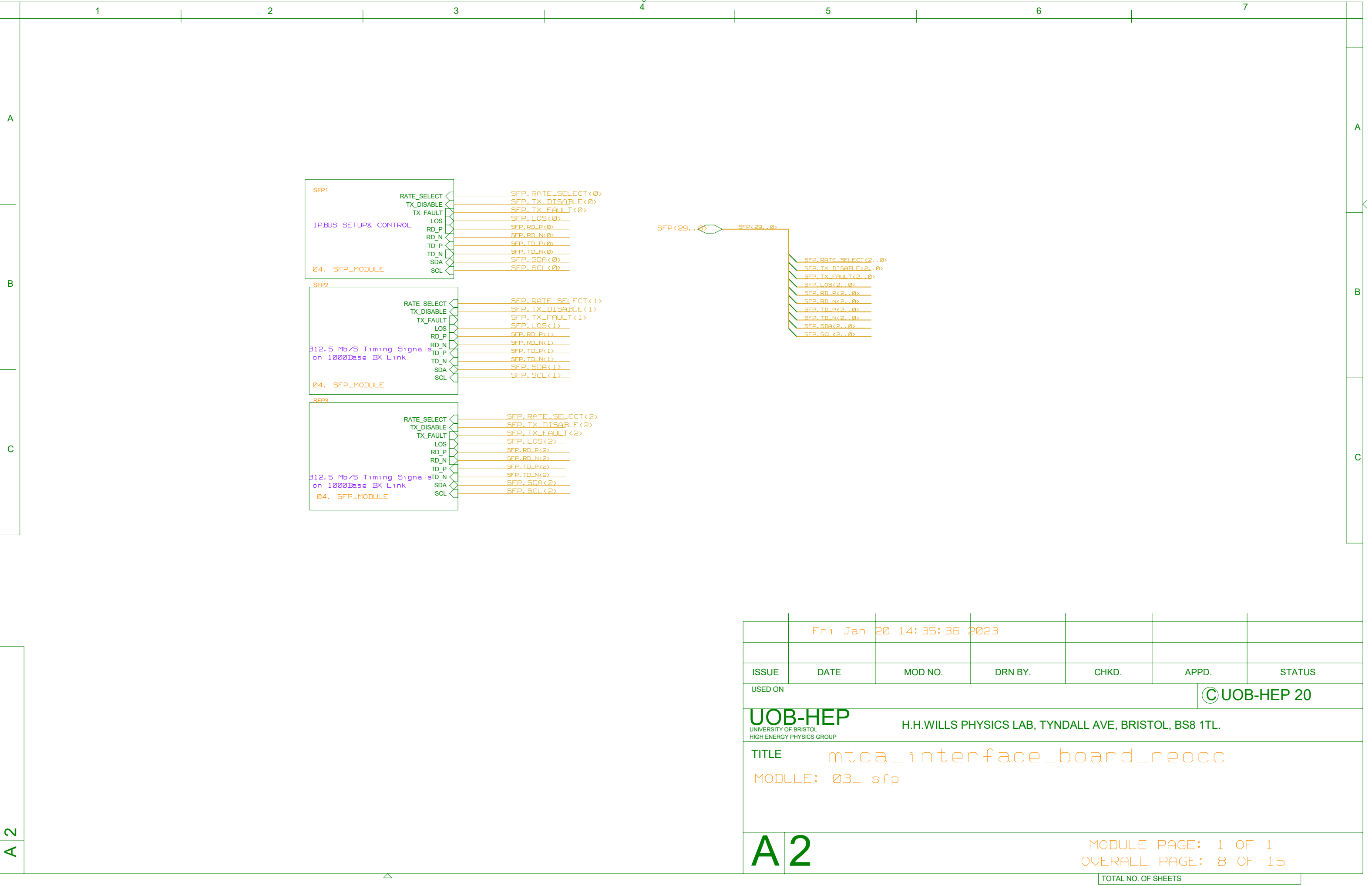
TOTAL NO. OF SHEETS

|   |                             |                  |         |
|---|-----------------------------|------------------|---------|
| UNIVERSITY OF PENNSYLVANIA<br>HIGH ENERGY PHYSICS<br>209 S 33rd Street<br>Philadelphia PA 19104 |                             |                  |         |
| Design Title:   | MTCA INTERFACE BOARD        |                  |         |
| Schematic Title:  | 02. POWER                   |                  |         |
| Page Title:   | 05. LDO-Regulators          |                  |         |
| Size  | Date:                       | Document Number: | Rev     |
| Custom  | Saturday, December 31, 2022 |                  | 1       |
| Designer:   | Godwin Mayers               | Sheet:           | 5 of 15 |









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TITLE

mtca\_interface\_board\_reocc

MODULE: 03\_ sfp

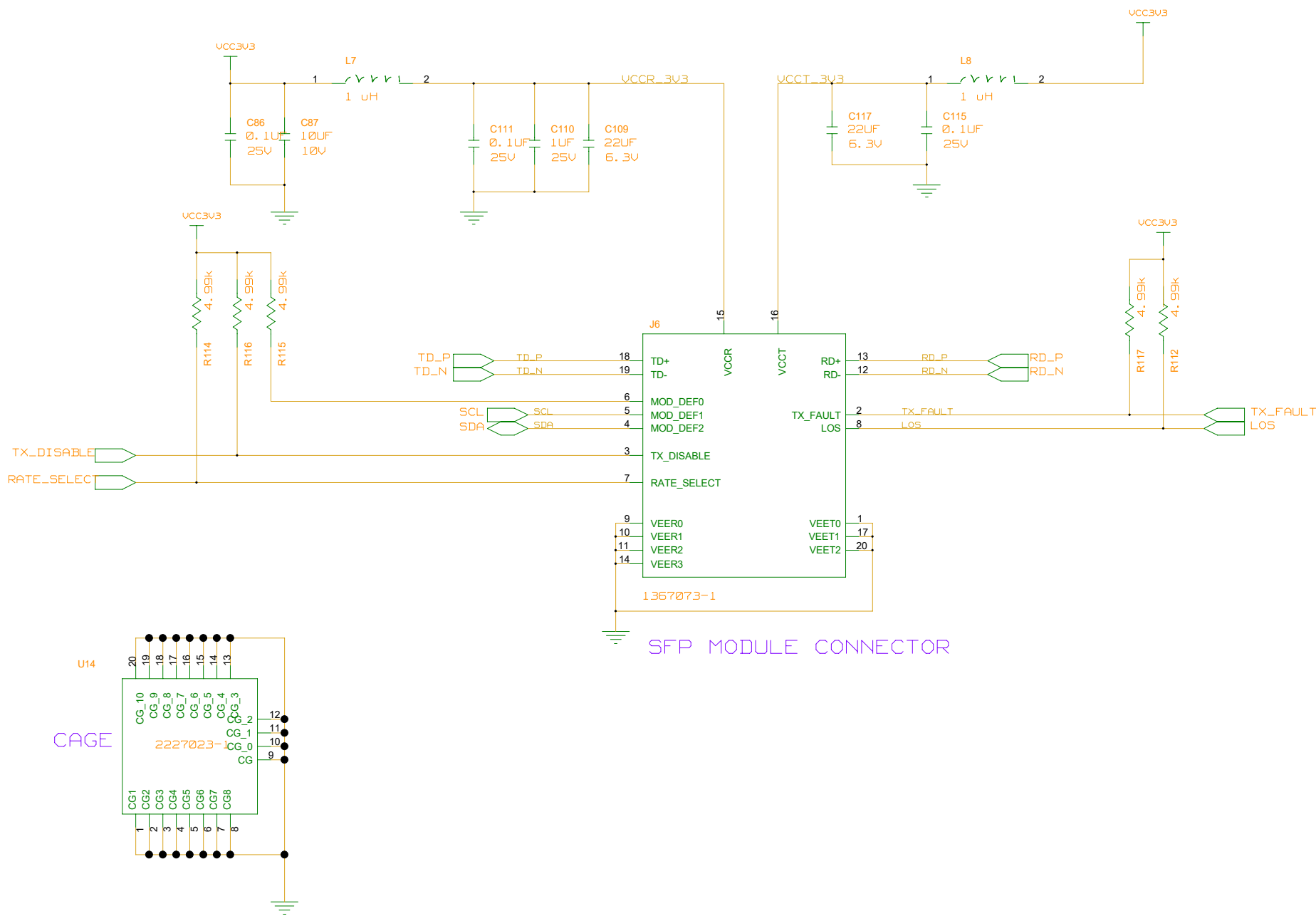
A2

MODULE PAGE: 1 OF 1

OVERALL PAGE: 8 OF 15

TOTAL NO. OF SHEETS





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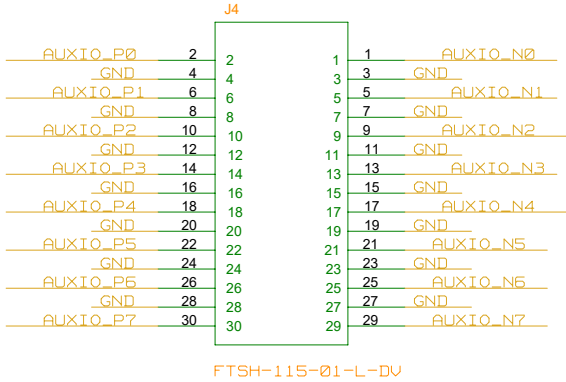
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TITLE

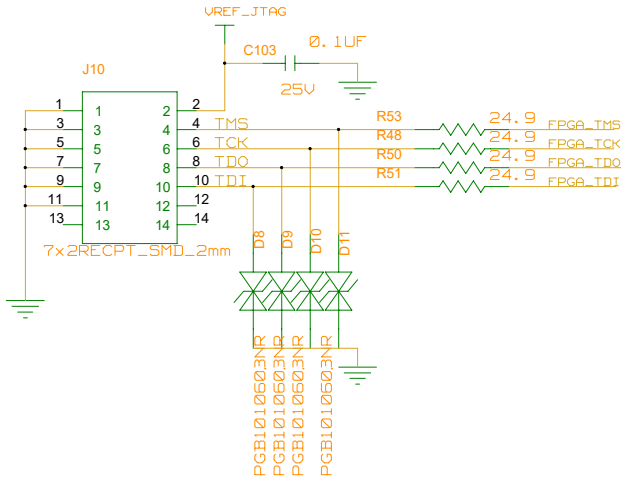
mtca\_interface\_board\_reocc

MODULE: 04\_ sfp\_module

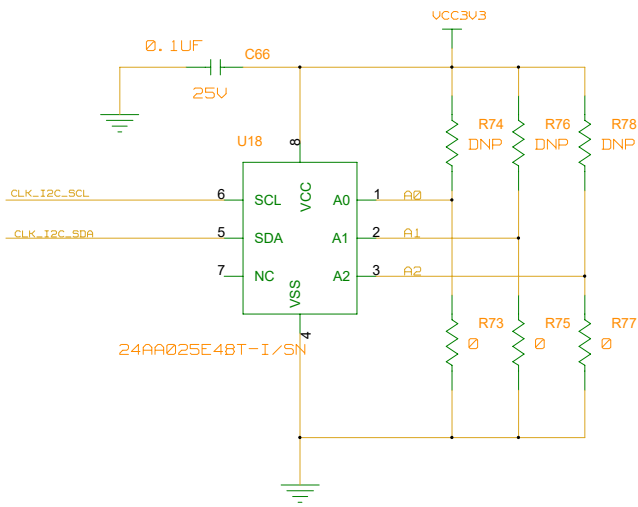
AUX IO INTERFACE



JTAG CONNECTOR

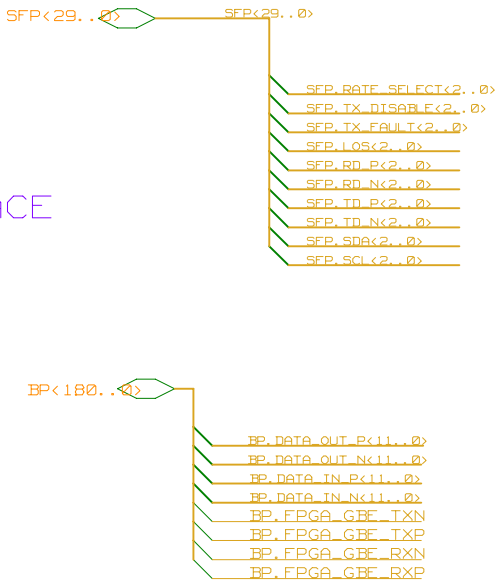
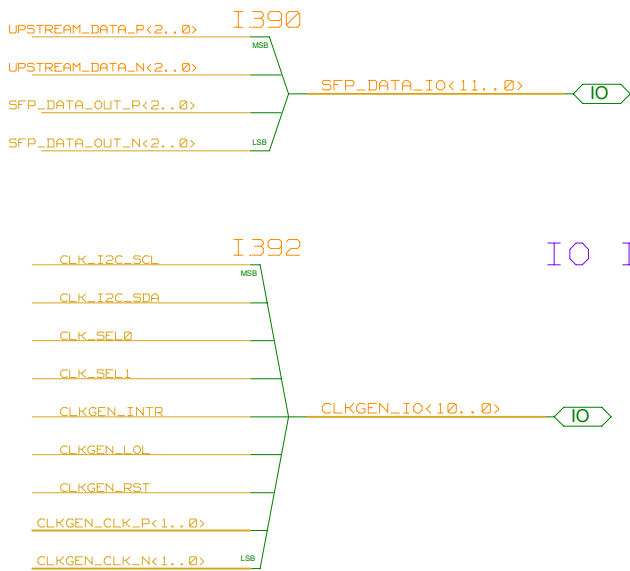


EEPROM

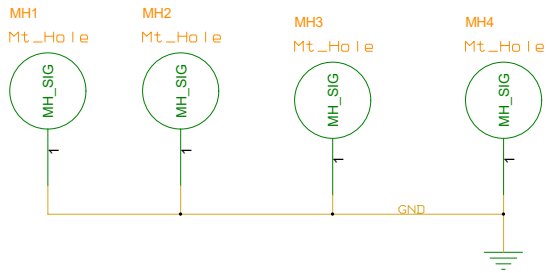


I2C ADDRESS = 0xA0

IO INTERFACE



TRENZ TE0712 FPGA MODULE MOUNT HOLES



|                           |                         |   |         |       |              |        |
|---------------------------|-------------------------|---|---------|-------|--------------|--------|
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| TITLE                     |                         | mtca_interface_board_reocc                            |         |       |              |        |
| MODULE:                   |                         | 05 trenz te0712 fpga module                           |         |       |              |        |
| A2                        |                         | MODULE PAGE: 1 OF 2<br>OVERALL PAGE: 12 OF 15         |         |       |              |        |
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B

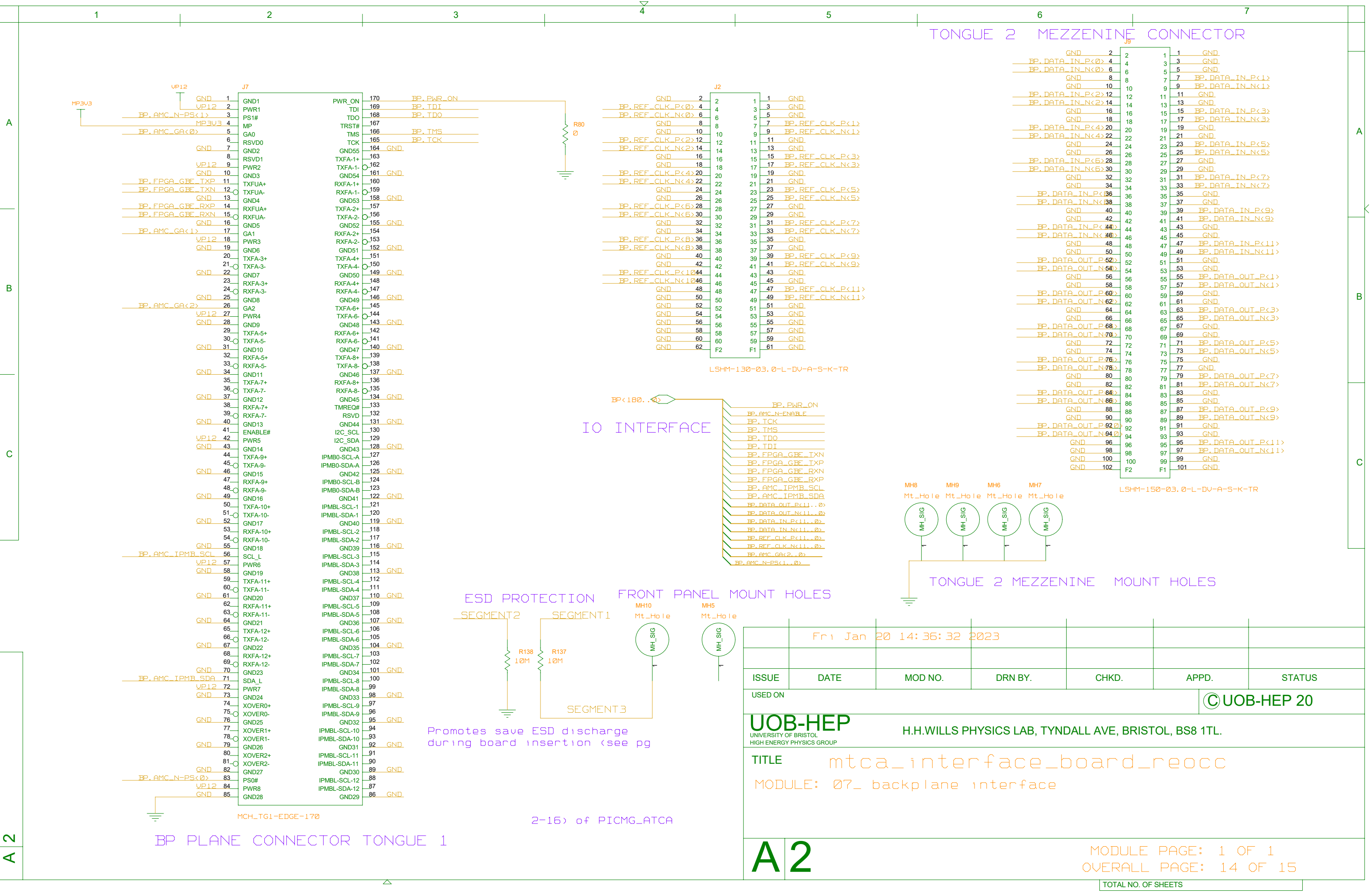
C

A 2

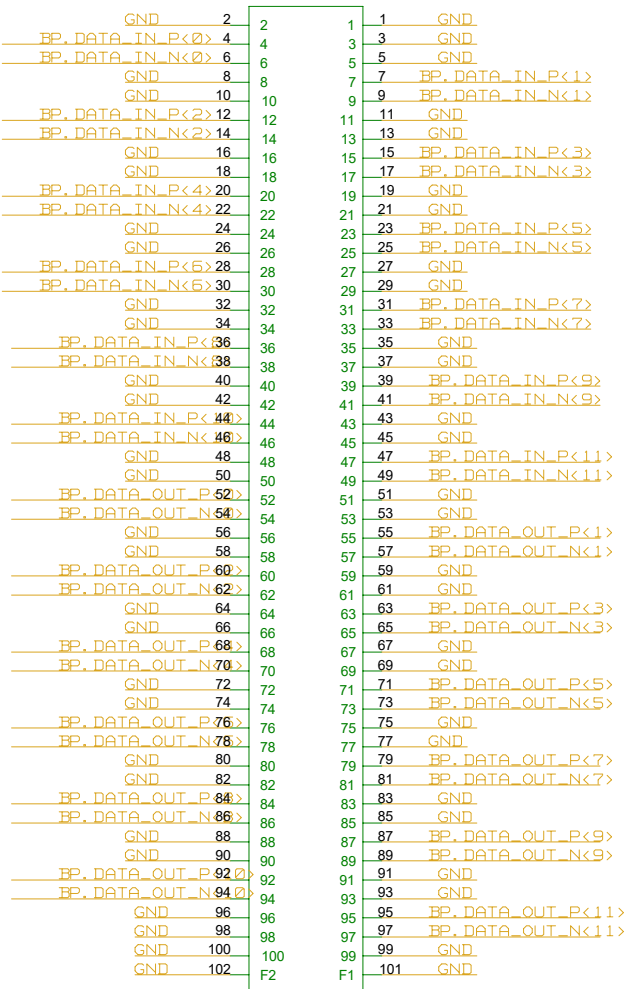
A

B

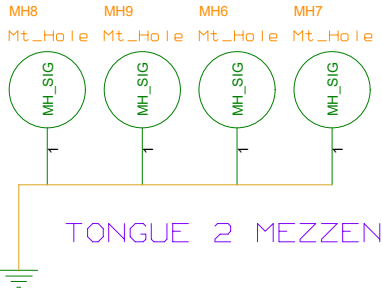
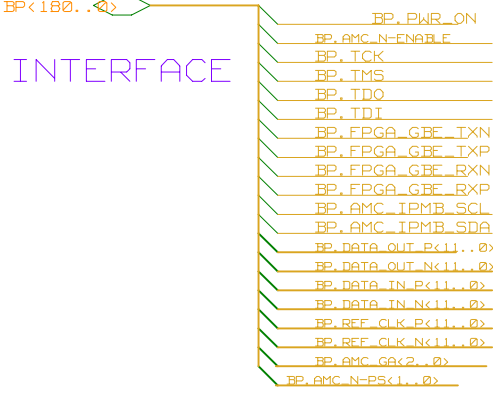
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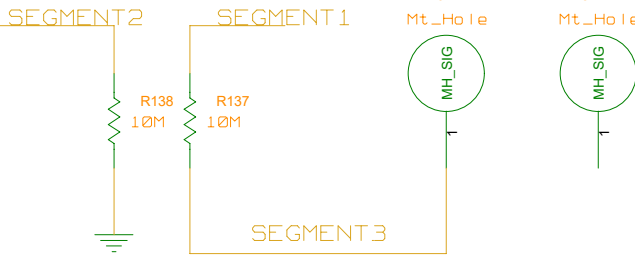
TONGUE 2 MEZZENINE CONNECTOR



IO INTERFACE



ESD PROTECTION FRONT PANEL MOUNT HOLES



Promotes save ESD discharge during board insertion (see pg

BP PLANE CONNECTOR TONGUE 1

2-16 of PICMG\_ATCA

|       |                          |         |         |       |       |        |
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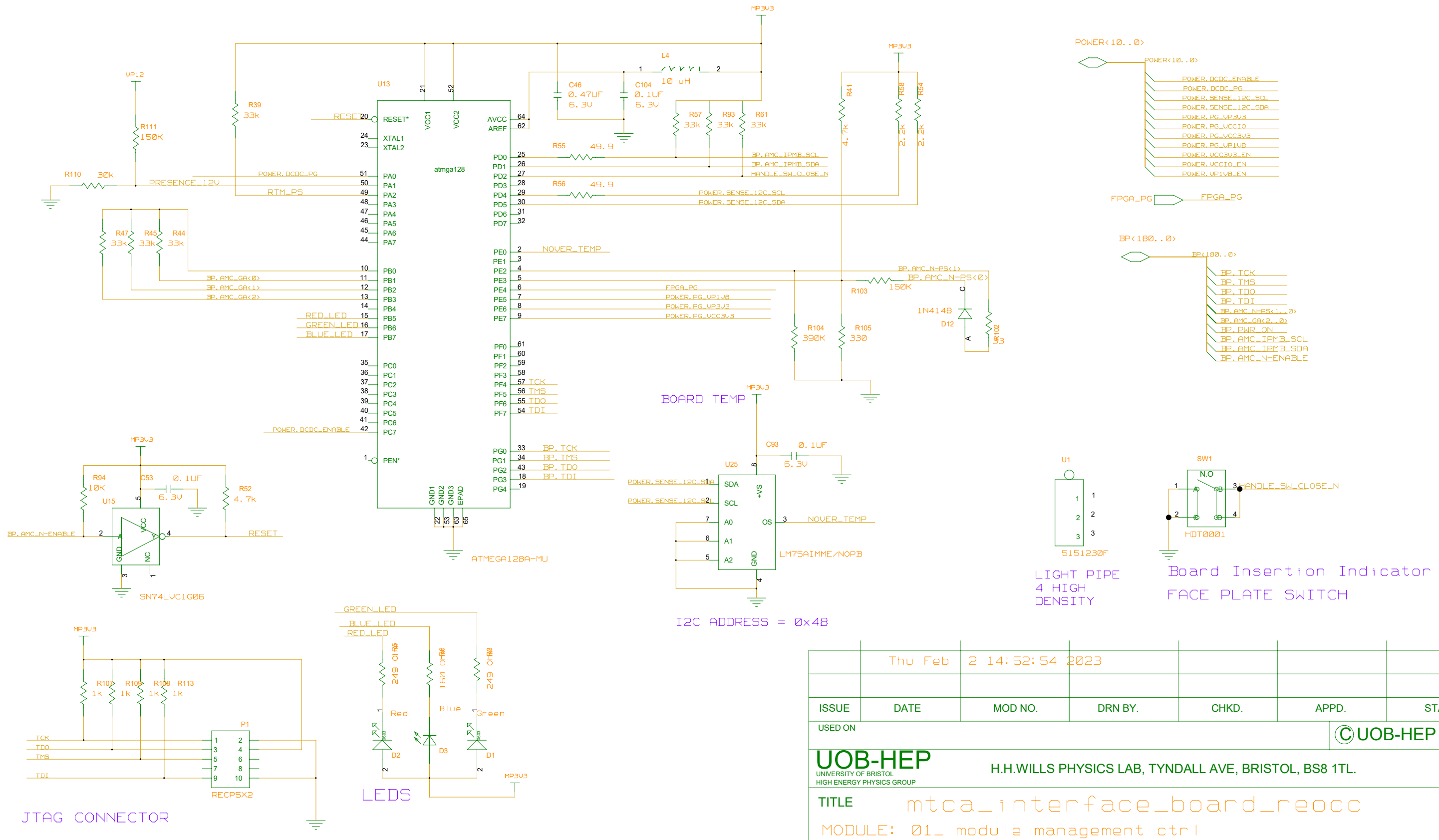
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TITLE mtca\_interface\_board\_reocc  
MODULE: 07\_ backplane interface

## MODULE MANAGEMENT CONTROL

## IO INTERFACE



|       |                         |         |         |       |       |        |
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```
TITLE      mtca_interface_board_reocc
MODULE:    01_ module management ctrl
```