

N.B. THIS IS A DRAWING, NO CONNECTIVITY

	Fri Jun 7 16:04:20 2024					
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
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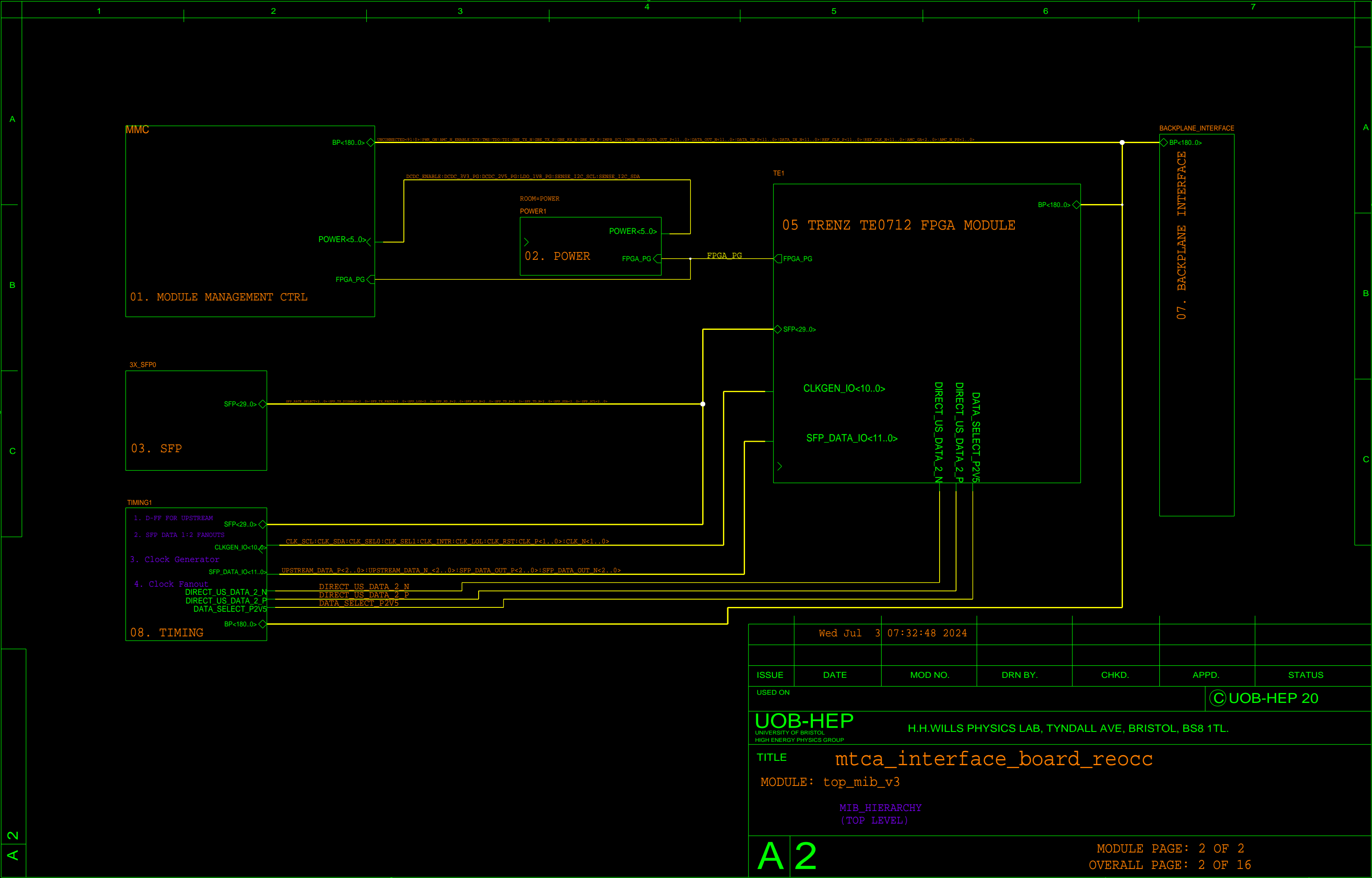
H.H.WILLS PHYSICS LAB, TYNDALL AVE, BRISTOL, BS8 1TL.

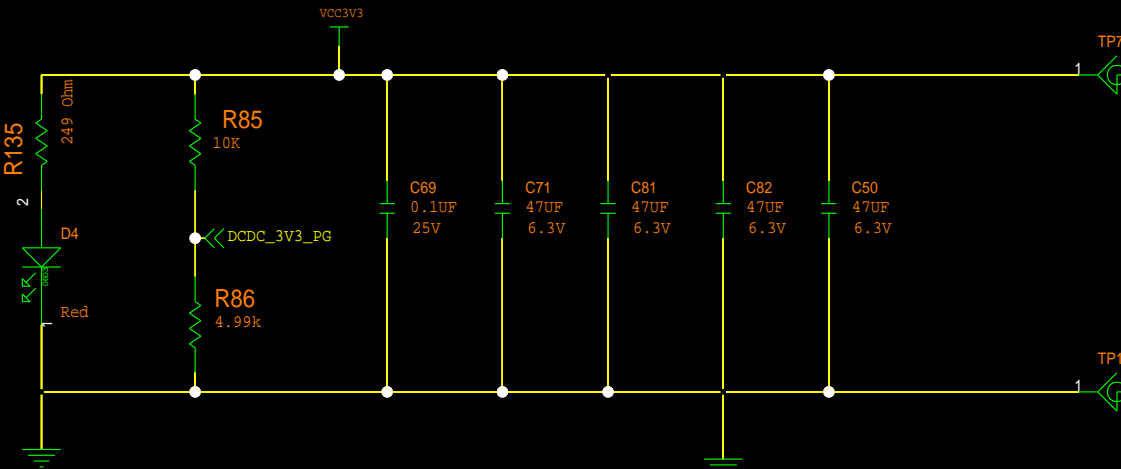
TITLE

mtca\_interface\_board\_reocc

MODULE:

top\_mib\_v3





	Thu Jan 23	13:09:22 2025				
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TITLE	mtca_interface_board_reocc					
MODULE:	02_ power					
	DC-DC (12V --> 3.3V)					





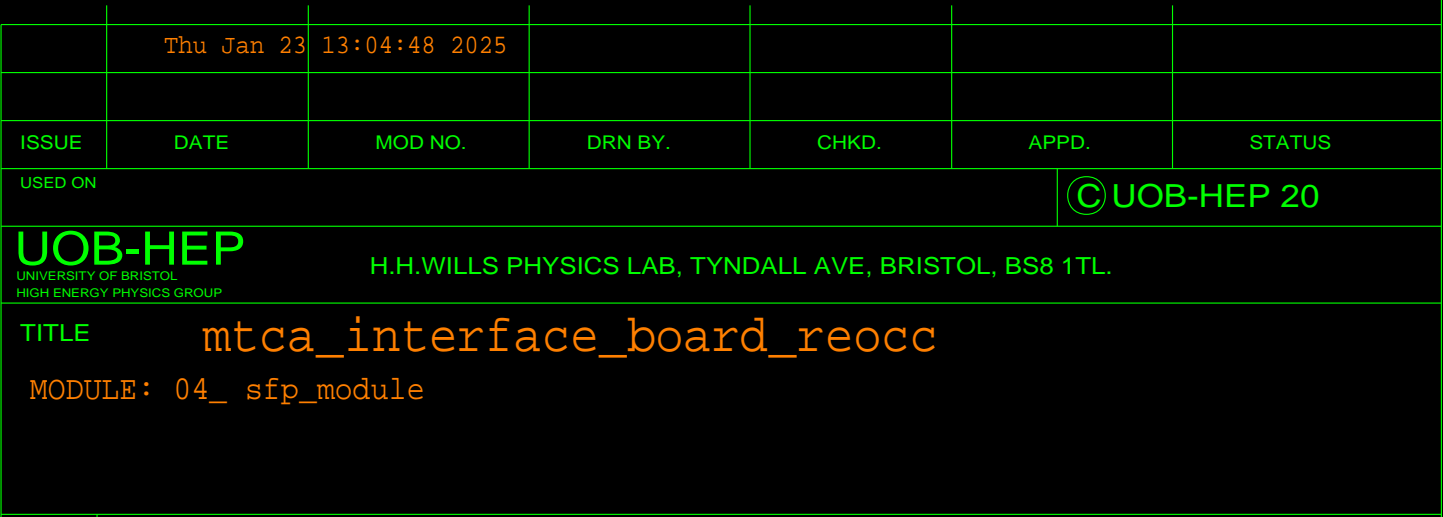






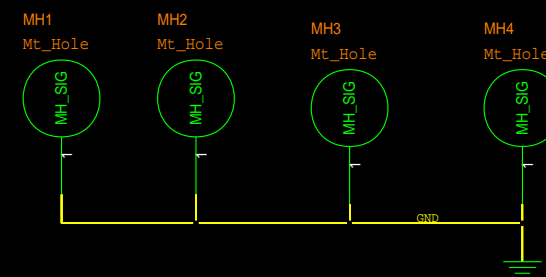
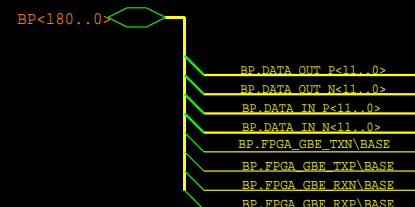
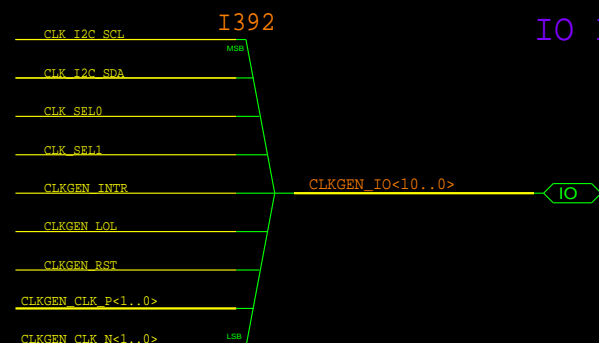
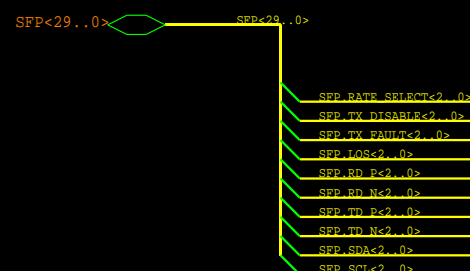
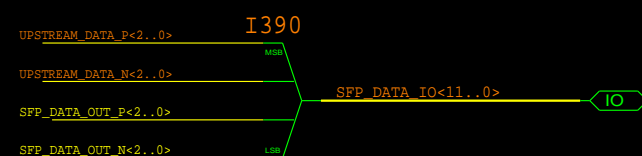
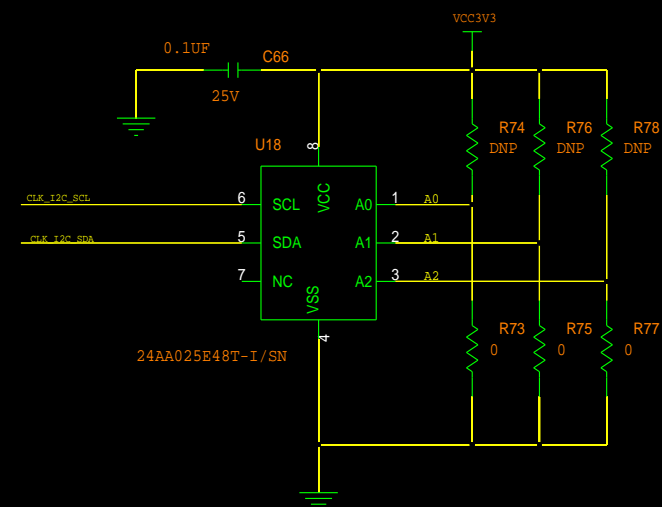
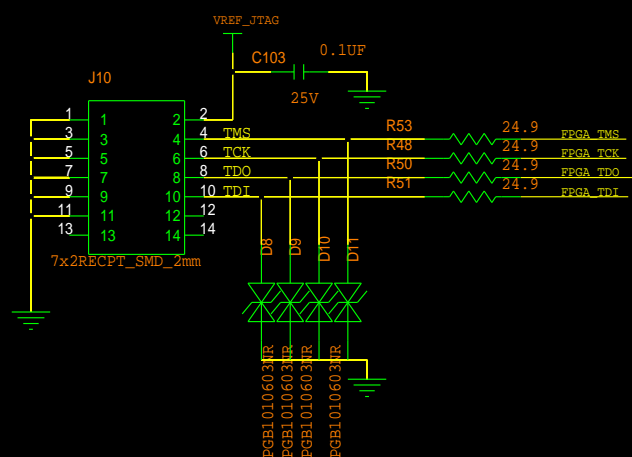
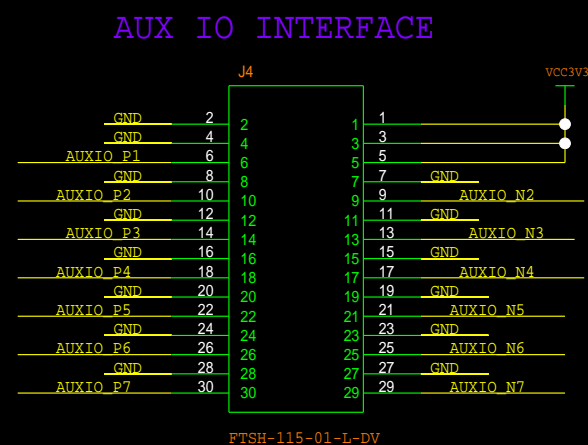






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TITLE      mtca_interface_board_reocc
```

```
MODULE: 05 trenz te0712 fpga module
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