Hardware	FPGAfirmware		Software	Hardware	FPGAfirmware		Software
Pin Name	Module	Pin	Signal name	Pin Name	Module	Pin	Signal name
1 GND	-	-	-	123 GND	-	-	-
2 AMC[0] - PS1#	I/O ext.	15	-	124 AMC[1] - PS1#	I/O ext.	7	-
3 AMC[0] - Enable#	I/O ext.	14	-	125 AMC[1] - Enable#	I/O ext.	6	-
4 AMC[0] - IPMB-L Enable	I/O ext.	0	-	126 AMC[1] - IPMB-L Enable	I/O ext.	5	-
5 AMC[0] - MP Enable	I/O ext.	1	-	127 AMC[1] - MP Enable	I/O ext.	4	-
6 AMC[0] - PWR Enable	I/O ext.	2	-	128 AMC[1] - PWR Enable	I/O ext.	3	-
7 AMC[0] - MP Good	I/O ext.	3	-	129 AMC[1] - MP Good	I/O ext.	2	-
8 AMC[0] - PWR Good	I/O ext.	4	-	130 AMC[1] - PWR Good	I/O ext.	1	-
9 AMC[0] - MP Fault	I/O ext.	5	-	131 AMC[1] - MP Fault	I/O ext.	0	-
10 AMC[0] - PWR Fault	I/O ext.	6	-	132 AMC[1] - PWR Fault	I/O ext.	15	-
11 AMC[0] - PWR Oring	I/O ext.	7	-	133 AMC[1] - PWR Oring	I/O ext.	14	-
12 GND	-	-	-	134 GND	-	-	-
13 AMC[2] - PS1#	I/O ext.	8	-	135 AMC[3] - PS1#	I/O ext.	13	-
14 AMC[2] - Enable#	I/O ext.	9	-	136 AMC[3] - Enable#	I/O ext.	12	-
15 AMC[2] - IPMB-L Enable	I/O ext.	10	-	137 AMC[3] - IPMB-L Enable	I/O ext.	11	-
16 AMC[2] - MP Enable	I/O ext.	11	-	138 AMC[3] - MP Enable	I/O ext.	10	-
17 AMC[2] - PWR Enable	I/O ext.	12	-	139 AMC[3] - PWR Enable	I/O ext.	9	-
18 AMC[2] - MP Good	I/O ext.	13	-	140 AMC[3] - MP Good	I/O ext.	8	-
19 AMC[2] - PWR Good	I/O ext.	14	-	141 AMC[3] - PWR Good	I/O ext.	7	-
20 AMC[2] - MP Fault	I/O ext.	15	-	142 AMC[3] - MP Fault	I/O ext.	6	-
21 AMC[2] - PWR Fault	I/O ext.	0	-	143 AMC[3] - PWR Fault	I/O ext.	5	-
22 AMC[2] - PWR Oring	I/O ext.	1	-	144 AMC[3] - PWR Oring	I/O ext.	4	-
23 GND	-	-	-	145 GND	-	-	-
24 AMC[4] - PS1#	I/O ext.	2	-	146 AMC[5] - PS1#	I/O ext.	3	-
25 AMC[4] - Enable#	I/O ext.	3	-	147 AMC[5] - Enable#	I/O ext.	2	-
26 AMC[4] - IPMB-L Enable	I/O ext.	4	-	148 AMC[5] - IPMB-L Enable	I/O ext.	1	-
27 AMC[4] - MP Enable	I/O ext.	5	-	149 AMC[5] - MP Enable	I/O ext.	0	-
28 AMC[4] - PWR Enable	I/O ext.	6	-	150 AMC[5] - PWR Enable	I/O ext.	15	-
29 AMC[4] - MP Good	I/O ext.	7	-	151 AMC[5] - MP Good	I/O ext.	14	-
30 AMC[4] - PWR Good	I/O ext.	8	-	152 AMC[5] - PWR Good	I/O ext.	13	-
31 AMC[4] - MP Fault	I/O ext.	9	-	153 AMC[5] - MP Fault	I/O ext.	12	-
32 AMC[4] - PWR Fault	I/O ext.	10	-	154 AMC[5] - PWR Fault	I/O ext.	11	-
33 AMC[4] - PWR Oring	I/O ext.	11	-	155 AMC[5] - PWR Oring	I/O ext.	10	-
34 GND	-		-	156 GND	-		-
35 AMC[6] - PS1#	I/O ext.	12	-	157 AMC[7] - PS1#	I/O ext.	9	-
36 AMC[6] - Enable#	I/O ext.	13	-	158 AMC[7] - Enable#	I/O ext.	8	-
37 AMC[6] - IPMB-L Enable	I/O ext.	14	-	159 AMC[7] - IPMB-L Enable	I/O ext.	/	-
38 AMC[6] - MP Enable	I/O ext.	15	-	160 AMC[7] - MP Enable	I/O ext.	6	-
39 AMC[6] - PWR Enable	I/O ext. I/O ext.	0	-	161 AMC[7] - PWR Enable	I/O ext.	5	-
40 AMC[6] - MP Good	I/O ext.	2	-	162 AMC[7] - MP Good	I/O ext.	4	-
41 AMC[6] - PWR Good	I/O ext.	3	-	163 AMC[7] - PWR Good	I/O ext. I/O ext.	3	-
42 AMC[6] - MP Fault	I/O ext.	3	-	164 AMC[7] - MP Fault 165 AMC[7] - PWR Fault	I/O ext.	1	-
43 AMC[6] - PWR Fault 44 AMC[6] - PWR Oring	I/O ext.	4	-	166 AMC[7] - PWR Oring	I/O ext.	0	-
45 GND	1/0 ext.	- 3	-	167 GND	I/O ext.	- 0	-
46 AMC[8] - PS1#	I/O ext.	- 6	_	168 IPMB-L SCL			
47 AMC[8] - Enable#	I/O ext.	7	_	169 IPMB-L SDA			
48 AMC[8] - IPMB-L Enable	I/O ext.	8	_	170 GND			
49 AMC[8] - MP Enable	I/O ext.	9	_	171 Ethernet Tx+	Ethernet Phy.	_	
50 AMC[8] - PWR Enable	I/O ext.	10	_	172 Ethernet Tx-	Ethernet Phy.	_	_
51 AMC[8] - MP Good	I/O ext.	11	_	173 GND	-		_
52 AMC[8] - PWR Good	I/O ext.	12	_	174 Ethernet Rx+	Ethernet Phy.	_	_
53 AMC[8] - MP Fault	I/O ext.	13	_	175 Ethernet Rx-	Ethernet Phy.	_	<u> </u>
54 AMC[8] - PWR Fault	I/O ext.	14	_	176 GND		_	_
55 AMC[8] - PWR Oring	I/O ext.	15	_	177 NC			_
56 GND	., o c.n		_	178 NC	_	_	<u> </u>
57 UART Tx	ARM Core	1	_	179 GND	_		_
58 NC	-	_	_	180 NC	_	_	_
59 GND	_	_	_	181 NC	_	_	<u> </u>
60 UART RX	ARM Core	1	_	182 GND	_	_	_
61 NC		1	_	183 Sensor I2C - SCL	ARM GPIO	6	_
	1	1		-00 00:1001 120 002	01 10		

Hardware	FPGAfirmware		Software		Hardware	FPGAfirmware		Software
Pin Name	Module	Pin	Signal name	Pin	Name	Module	Pin	Signal name
62 GND	ivioudie		Signal name		Sensor I2C - SDA	ARM GPIO		Jighai hame
63 IPM I/O [0]	FPGA Core GPIO [3]	15	IPM IO 0		GND	ANN GFIO		
64 IPM I/O [1]	FPGA Core GPIO [3]		IPM IO 1		IPM I/O [2]	FPGA Core GPIO [3]	13	IPM IO 2
65 VCC	Trancore ario [5]				IPM I/O [3]	FPGA Core GPIO [3]		IPM IO 3
66 IPM I/O [4]	FPGA Core GPIO [3]	11	IPM IO 4		IPM I/O [6]	FPGA Core GPIO [3]		IPM IO 6
67 IPM I/O [5]	FPGA Core GPIO [3]		IPM_IO_5		IPM I/O [7]	FPGA Core GPIO [3]		IPM_IO_7
68 VCC	-	- 10	- IFWI_10_5		GND	rda core drio [5]		
69 IPM I/O [8]	FPGA Core GPIO [3]	7	IPM IO 8		IPM I/O [10]	FPGA Core GPIO [3]	5	IPM IO 10
70 IPM I/O [9]	FPGA Core GPIO [3]		IPM_IO_9		IPM I/O [10]	FPGA Core GPIO [3]	4	IPM_IO_11
71 VCC	Trancore ario [5]				GND	Transcore ario [5]	-	
72 IPM I/O [12]	FPGA Core GPIO [3]	٠ ء	IPM IO 12		IPM I/O [14]	FPGA Core GPIO [3]	1	IPM IO 14
73 IPM I/O [13]	FPGA Core GPIO [3]		IPM_IO_13		IPM I/O [15]	FPGA Core GPIO [3]		IPM IO 15
74 VCC	Trancore ario [5]		II IM_IO_IS		GND	Transcore ario [5]		
75 USER I/O [0]	FPGA Core GPIO [3]	16	USER IO 0		USER I/O [2]	FPGA Core GPIO [3]	10	USER IO 2
76 USER I/O [1]	FPGA Core GPIO [3]		USER IO 1		USER I/O [3]	FPGA Core GPIO [3]		USER IO 3
77 VCC	FFGA COTE GFIO [5]	- 17	03EK_10_1		GND	FFGA COTE GFIO [5]	- 19	U3EN_IU_3
78 USER I/O [4]	FPGA Core GPIO [3]	20	USER_IO_4		USER I/O [6]	FPGA Core GPIO [3]	ຳ	USER IO 6
78 USER I/O [4] 79 USER I/O [5]	FPGA Core GPIO [5]		USER_IO_4 USER_IO_5		USER I/O [7]	FPGA Core GPIO [3]		USER_IO_6
80 VCC	-		5551, 10_5		GND	-	23	
81 USER I/O [8]	FPGA Core GPIO [3]	2/	USER_IO_8		USER I/O [10]	FPGA Core GPIO [3]	26	USER_IO_10
82 USER I/O [9]	FPGA Core GPIO [3]		USER_IO_9		USER I/O [11]	FPGA Core GPIO [3]		USER_IO_10
83 VCC	av core acio [3]	. 23			GND	TI OA COIE OFIO [3]	/	
84 USER I/O [12]	FPGA Core GPIO [3]	- 20	USER IO 12		USER I/O [14]	FPGA Core GPIO [3]	- 20	USER_IO_14
85 USER I/O [13]	FPGA Core GPIO [3]		USER IO 13		USER I/O [15]	FPGA Core GPIO [3]		USER IO 15
86 VCC	Trua core ario [3]	23	03EN_IO_13		GND	Trax core ario [5]	31	03EN_10_13
87 USER I/O [16]	FPGA Core GPIO [2]		USER_IO_16		USER I/O [18]	FPGA Core GPIO [2]	- 2	USER_IO_18
88 USER I/O [17]	FPGA Core GPIO [2]		USER IO 17		USER I/O [19]	FPGA Core GPIO [2]		USER IO 19
89 GND	Trua core ario [2]	_	03EN_10_17		GND	Trancole ario [2]	,	03EN_10_13
90 USER I/O [20]	FPGA Core GPIO [2]		USER_IO_20		USER I/O [21]	FPGA Core GPIO [2]	- 5	USER_IO_21
91 USER I/O [22]	FPGA Core GPIO [2]		USER IO 22		USER I/O [23]	FPGA Core GPIO [2]		USER IO 23
92 USER I/O [24]	FPGA Core GPIO [2]		USER_IO_24		USER I/O [25]	FPGA Core GPIO [2]		USER IO 25
93 USER I/O [26]	FPGA Core GPIO [2]		USER_IO_26		USER I/O [27]	FPGA Core GPIO [2]		USER IO 27
94 USER I/O [28]	FPGA Core GPIO [2]		USER_IO_28		USER I/O [29]	FPGA Core GPIO [2]		USER IO 29
95 USER I/O [30]	FPGA Core GPIO [2]		USER IO 30		USER I/O [31]	FPGA Core GPIO [2]		USER IO 31
96 USER I/O [32]	FPGA Core GPIO [2]		USER_IO_32		USER I/O [33]	FPGA Core GPIO [2]		USER IO 33
97 GND	- Core di lo [2]		-		GND	Trust core of to [2]		
98 USER I/O [34]	FPGA Core GPIO [2]	18	USER IO 34		Management I2C - SCL	ARM GPIO	9	_
99 NC	-	-	-		Management I2C - SDA	ARM GPIO	8	_
100 NC	_	_	-		GND	-	-	-
101 NC	_	_	-		Payload reset#			
102 GND	_	-	-		Handle switch	FPGA Core GPIO [1]	9	CFG HANDLE SWITCH SIGNAL
103 BLUE LED	FPGA Core GPIO [2]	30	-		12V enable	FPGA Core GPIO [2]		CFG PAYLOAD DCDC EN SIGNAL
104 Additional LED [0]	FPGA Core GPIO [2]	27	-		Alarm A	FPGA Core GPIO [1]		CFG ALARM A
105 Additional LED [1]	FPGA Core GPIO [2]	31	-		Alarm B	FPGA Core GPIO [1]		CFG ALARM B
106 Additional LED [2]	FPGA Core GPIO [2]	28	-		PowerGood A	FPGA Core GPIO [1]		CFG PWRGOOD A
107 GND		- "	-		PowerGood B	FPGA Core GPIO [1]		CFG PWRGOOD B
108 JTAG Master - TRST	JTAG Module	-	-		GND			= '' = -
109 JTAG Master - TDO	JTAG Module	-	-		JTAG Master - TCK	JTAG Module	-	-
110 JTAG Master - TMS	JTAG Module	-	-		JTAG Master - TDI	JTAG Module	-	-
111 GND	-	-	-		GND	-	-	-
112 JTAG input - TDO	-	-	-		JTAG input - TCK	-	-	-
113 JTAG input - TMS	-	-	-		JTAG input - TDI	-	-	-
114 GND	-	-	-		GND	-	-	-
115 Hardware address [0]	FPGA Core GPIO [1]	0	-		Hardware address [1]	FPGA Core GPIO [1]	1	-
116 Hardware address [2]	FPGA Core GPIO [1]	2	-		Hardware address [3]	FPGA Core GPIO [1]	3	-
117 Hardware address [4]	FPGA Core GPIO [1]	4	-		Hardware address [5]	FPGA Core GPIO [1]	5	-
118 Hardware address [6]	ARM GPIO	18	-		Hardware address [7]	FPGA Core GPIO [1]	7	-
119 GND	-	-	-		GND			-
120 IPMB-A SCL	Internal I2C	-	-		IPMB-B SCL	Internal I2C	1	-
121 IPMB-A SDA	Internal I2C	-	-	243	IPMB-B SDA	Internal I2C		-
122 GND	-		-	244	GND	-	l	-