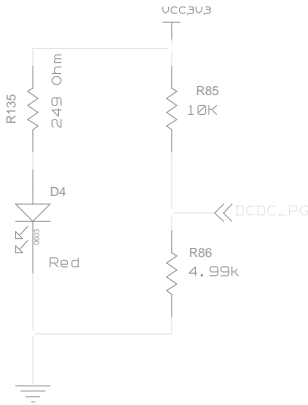
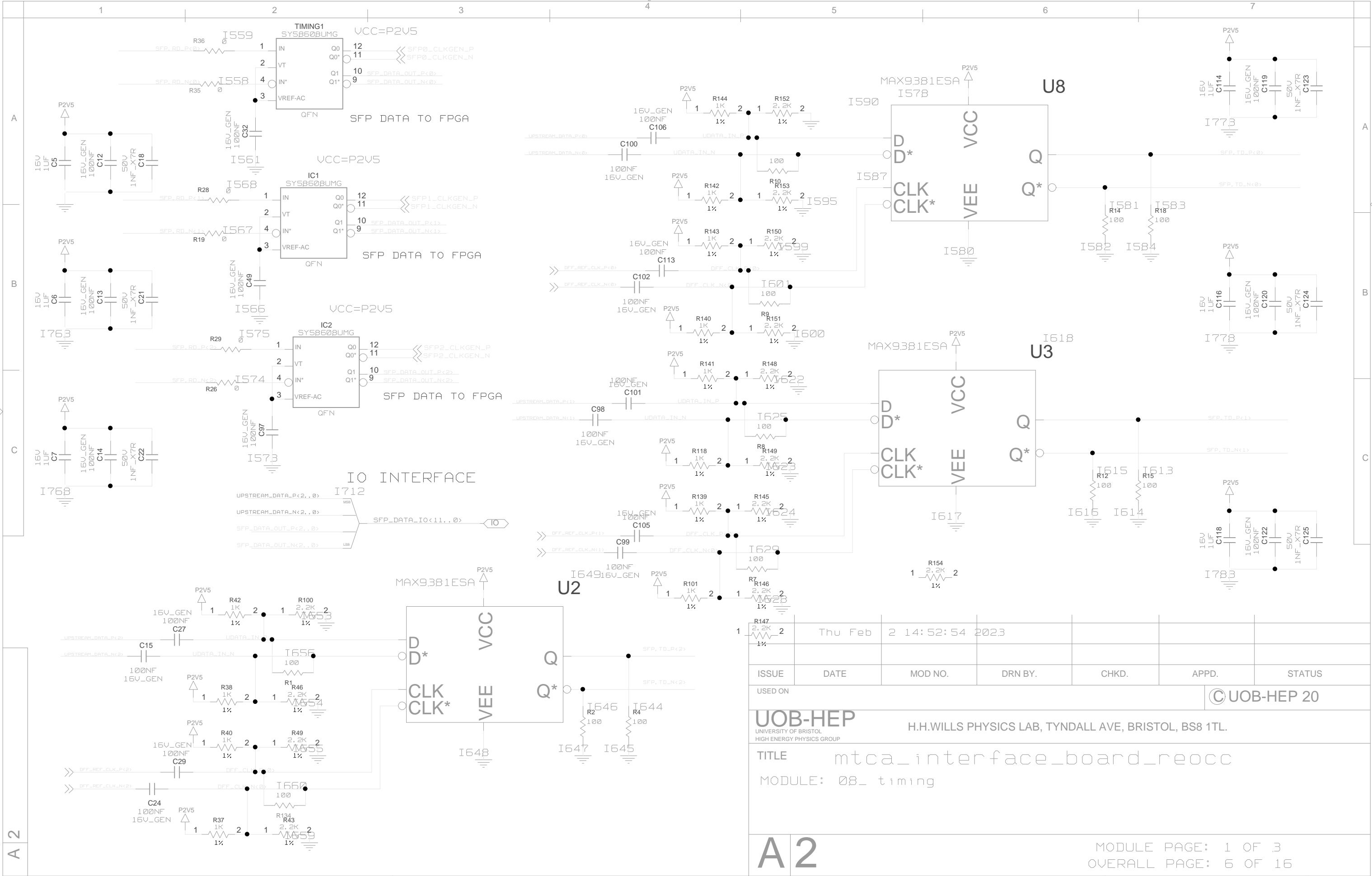


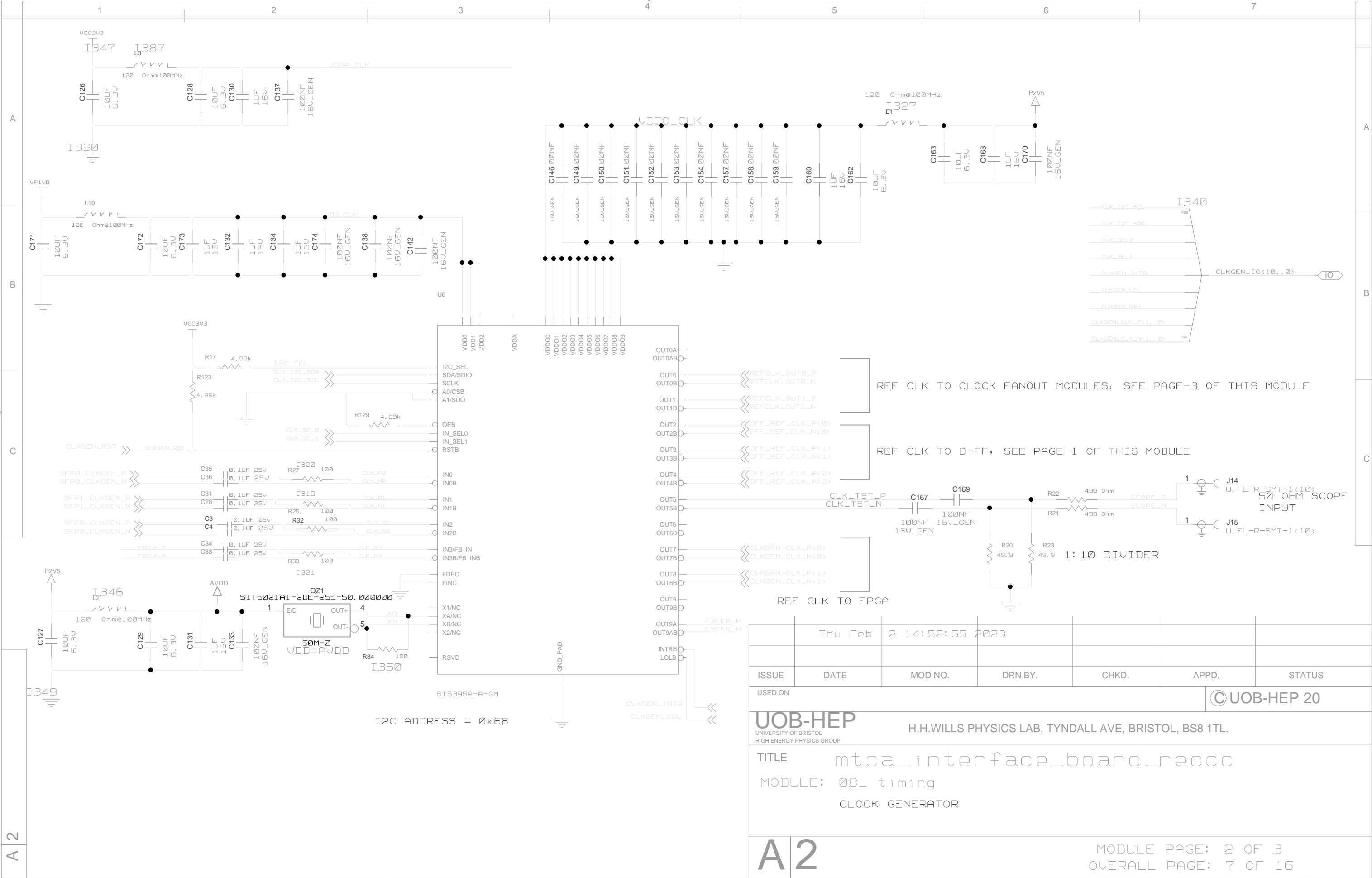
$$\frac{\nabla}{4}$$


A	2
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$$\frac{\nabla}{4}$$


A	2
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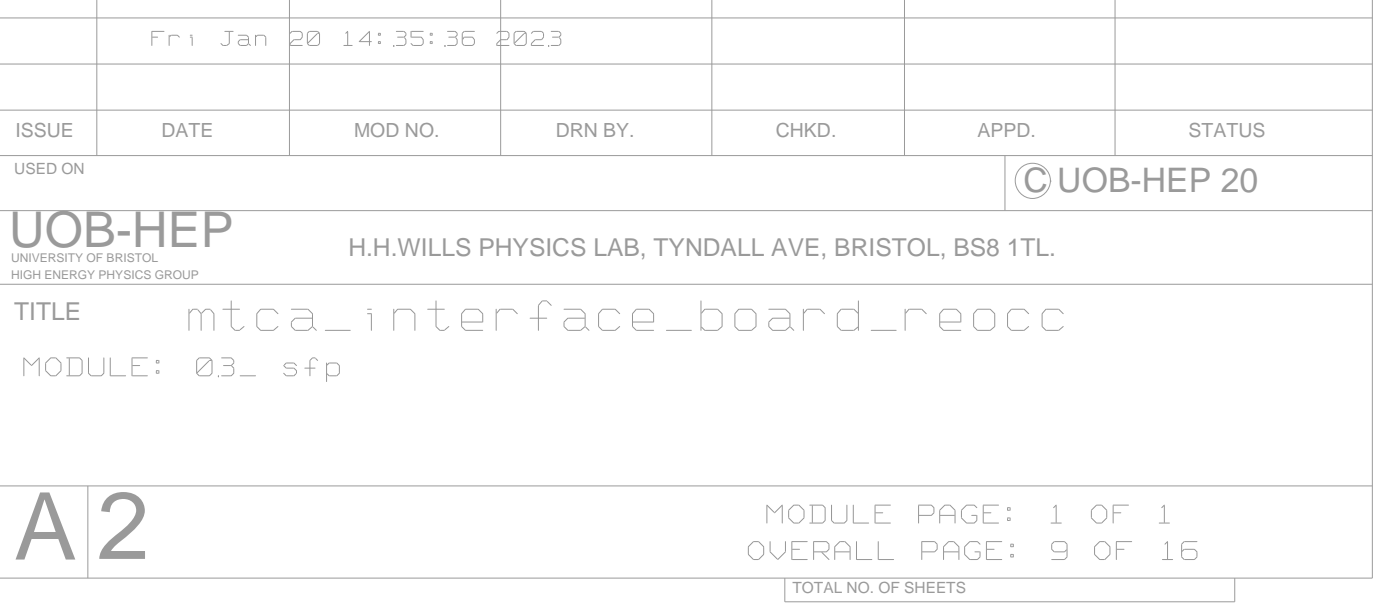
	Thu Feb	2 14:52:55	2023			
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TITLE	mtca_interface_board_reocc
MODULE:	08_timing
	CLOCK GENERATOR

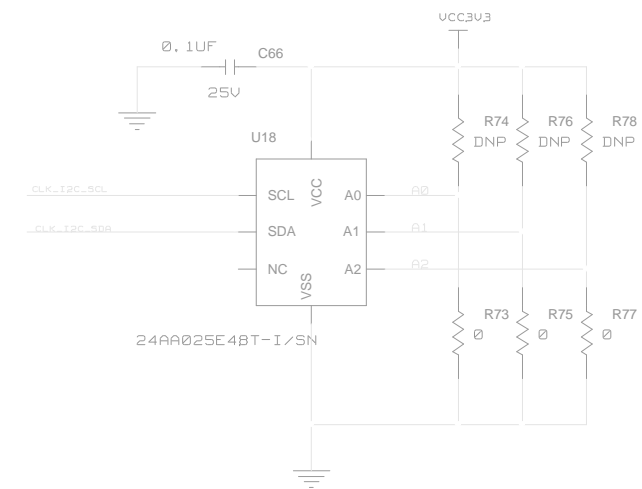
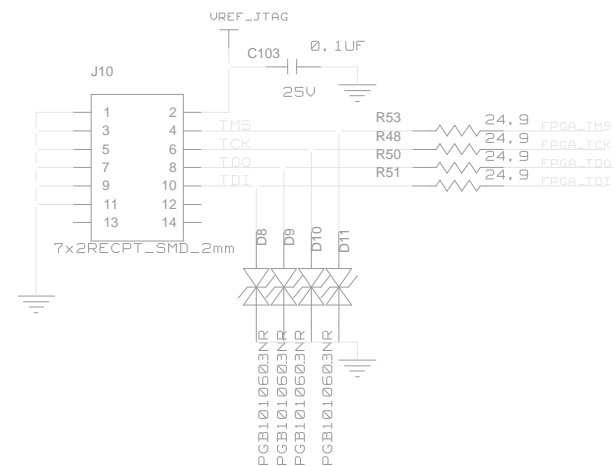
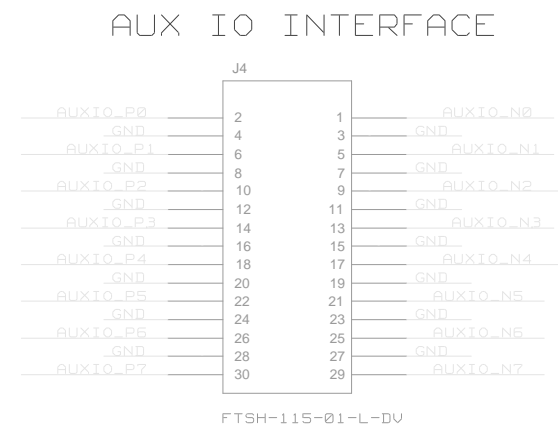
A2	MODULE PAGE: 2 OF 3 OVERALL PAGE: 7 OF 16
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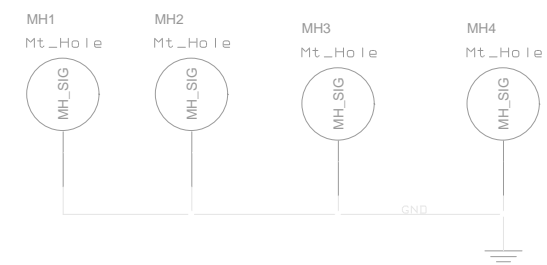
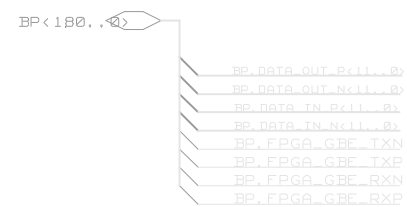
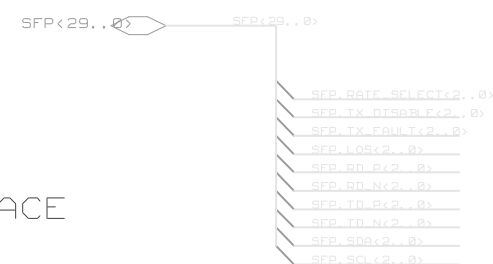
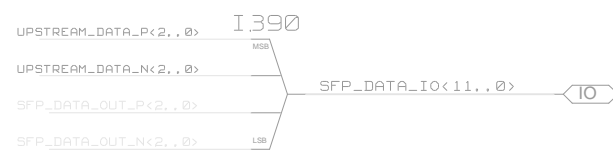








I2C ADDRESS = 0xA0



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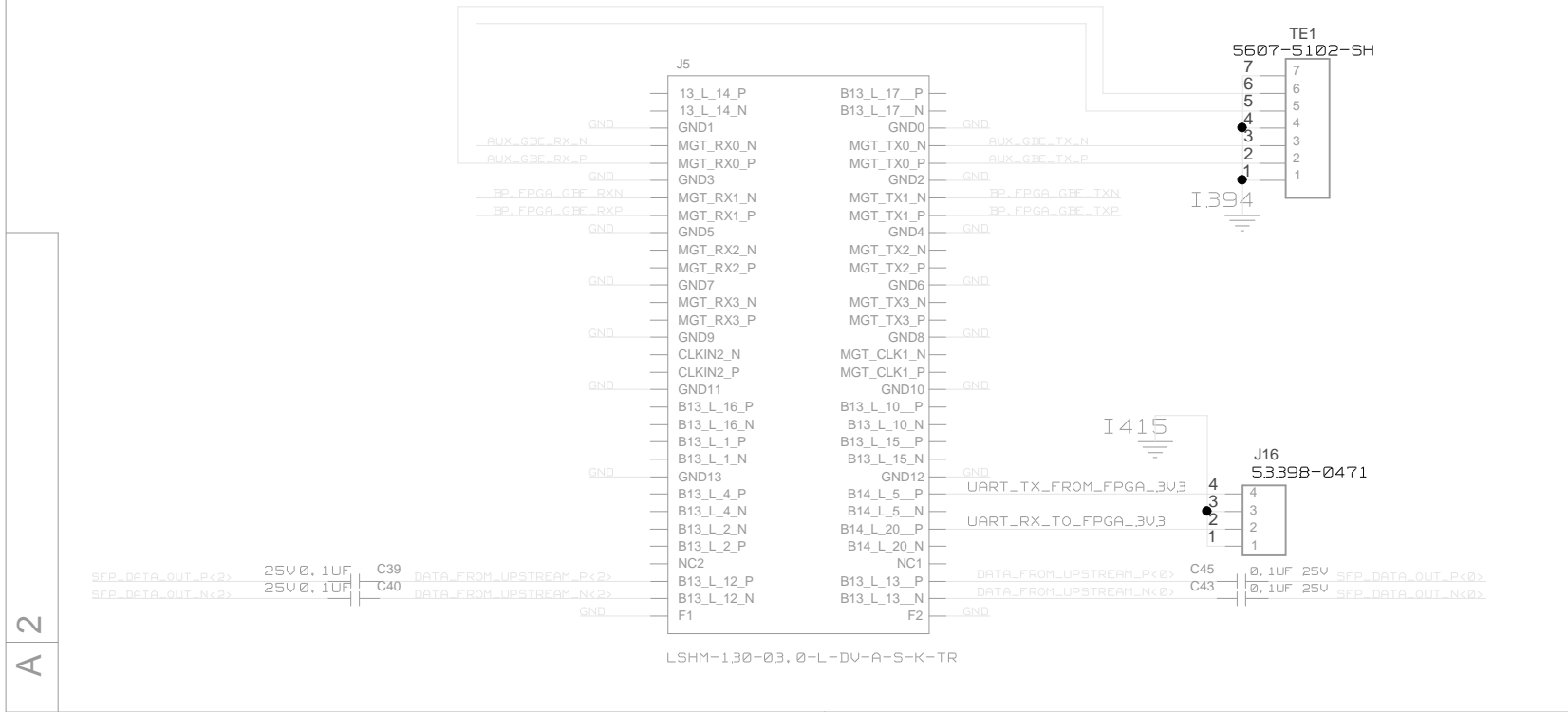
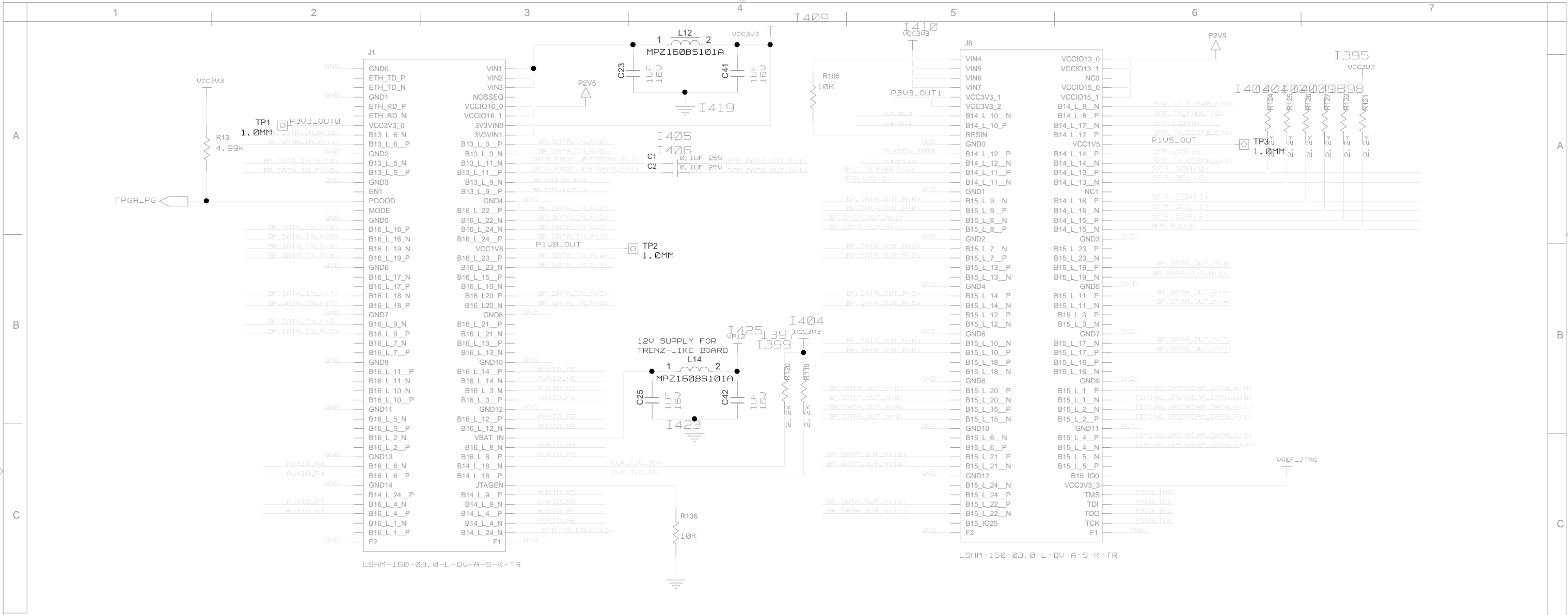
TITLE	mtca_interface_board_reocc
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MODULE: 05_trenz_te0712_fpga_module
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A	2
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MODULE PAGE: 1 OF 2
OVERALL PAGE: 13 OF 16

TOTAL NO. OF SHEETS



BANK 14 - 3.3V
BANKS 13, 16, 16 - 2.5V

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TITLE `mtca_interface_board_reocc`
MODULE: `05 trenz te0712 fpga module`
`TE0712 CONNECTORS`



