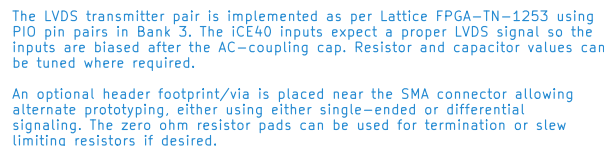


LVDS RX



LVDS TX

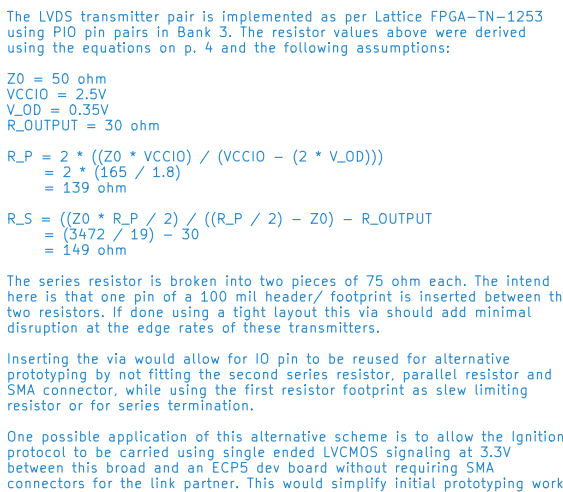


Diagram illustrating the ID strap connections for a V3P3 header. The header has pins 1 through 11. Pins 2 through 11 are connected to ID0 through ID5 respectively. Each ID strap is connected to a 10K resistor, which is then connected to a common ground line. Pin 1 is connected to V3P3. The diagram shows the physical layout of the ID straps and the electrical connections to ground via resistors.

ID straps allow setting the desired value. The header allows reuse of these pins for alternative prototyping

See RFD 142 for currently allocated ID values.

D?

LTST-C193KGKT-5A

PWR_EN

R?

280

GND

If: 5mA
Vf: 1.9V
Rlim: 280ohm

Sheet: Power

Power

File: ignitionlet-power.sch

Sheet: Config

Programming & Configuration

File: ignitionlet-config.sch

Sheet: /
File: ignitionlet.sch

Title: Ignition Target

Size: A3	Date: 2021-06-18
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Size: A3	Date: 2021-06-18
KiCad E.D.A. kicad (5.1.10-1-10_14)	

Rev: 1

Id: 1/3

