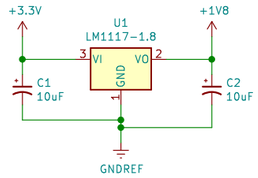
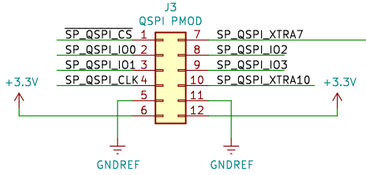
Generate 1V8 from SP header's 3V3 input using an LDO

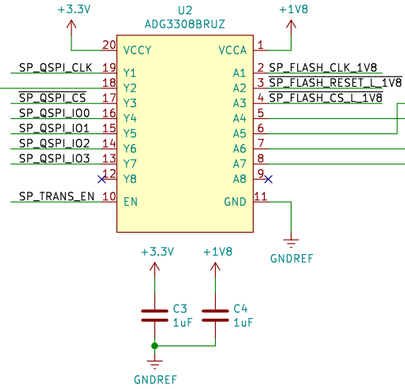


SP Connection

Matches gimletlet QSPI Header.
XTRA7 is FLASH_RESET_L
XTRA10 is SP_FLASH_MUX_SELECT

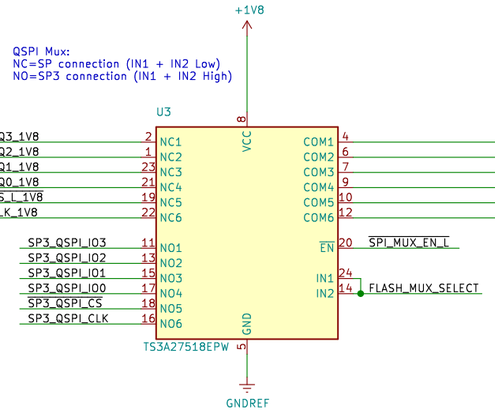


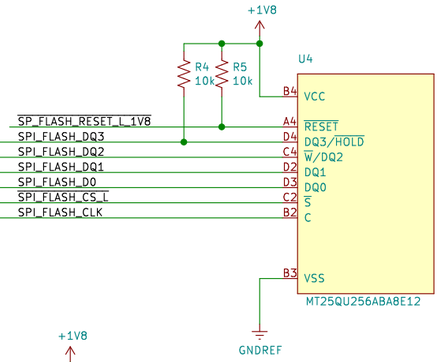
(SP_FLASH_RESET_L)

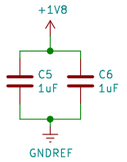


Decoupling for ADG330BBCPZ

QSPI Mux:
NC=SP connection (IN1 + IN2 Low)
NO=SP3 connection (IN1 + IN2 High)



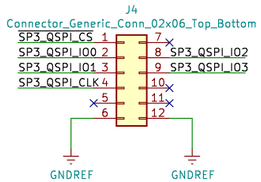




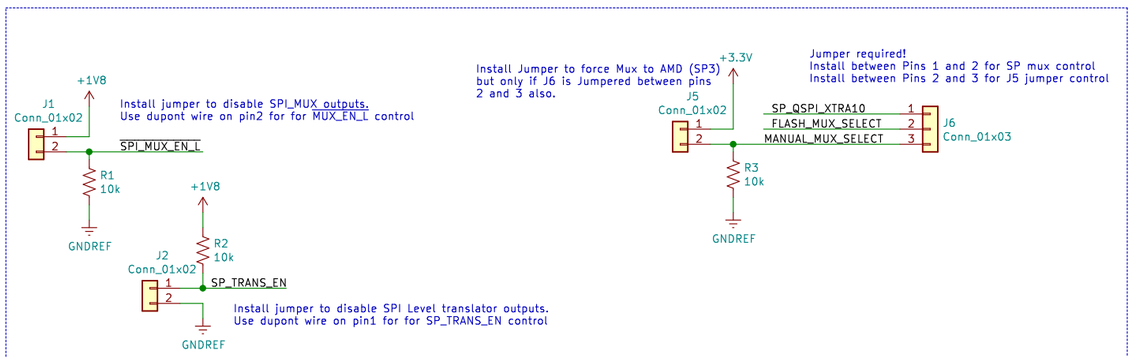
Decoupling for Mux and Flash

AMD (SP3) Connection

Dupont header for connection to an SP3 dev system.
Pin-out kept consistent with the Qspi but expects 1V8 voltage levels here



Manual Override Headers



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