

SM-G532F_DS_REV03

2016.09.09

Sheet01 : RF Part (TRANCEIVER, PAM. Etc..)

Sheet02 : Connectivity Part (BT/WIFI, GPS, NFC)

Sheet03 : MK6737(AP), MEMORY

Sheet04 : MK6328(PMIC), IF PMIC

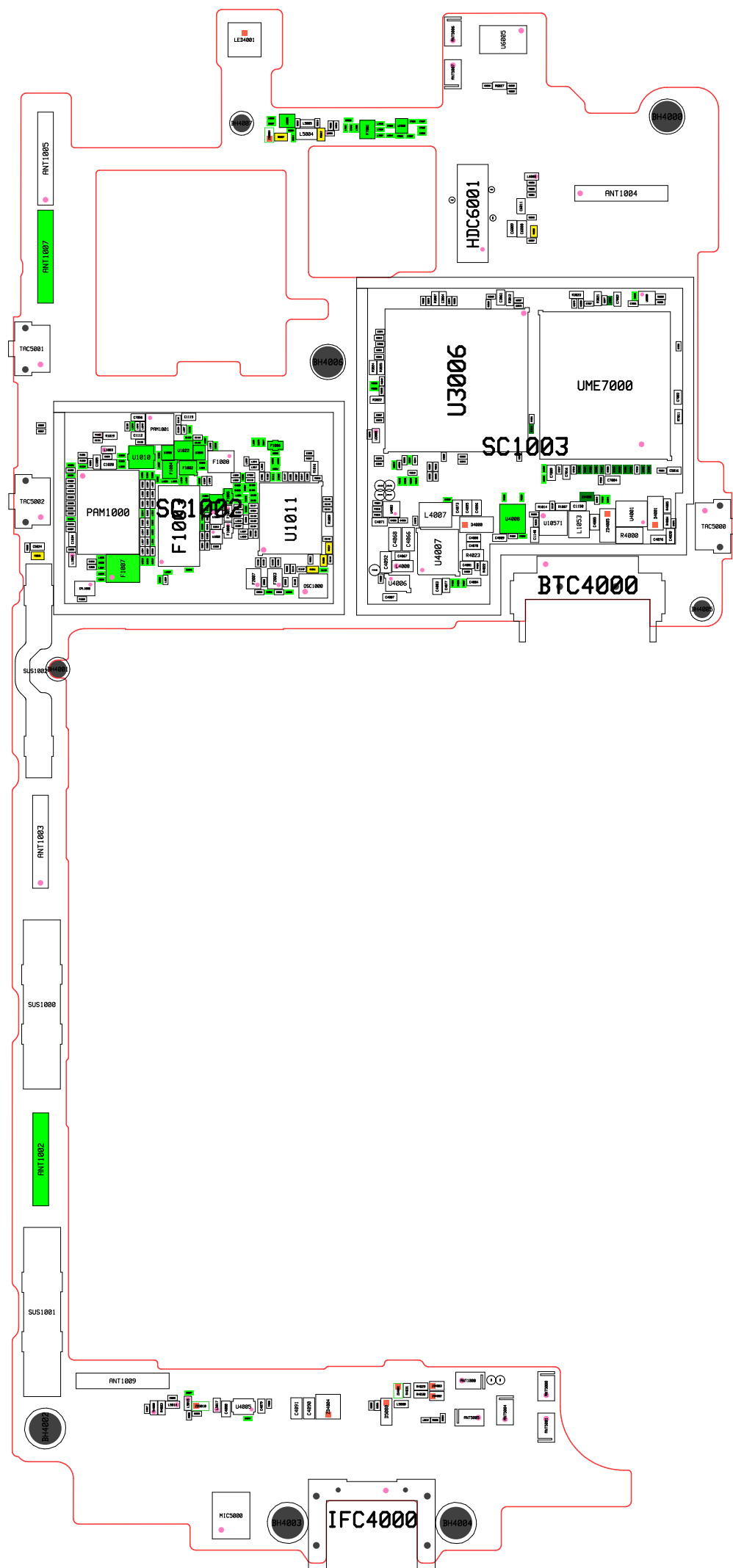
Sheet05 : SPK, EAR, MIC, MOTOR

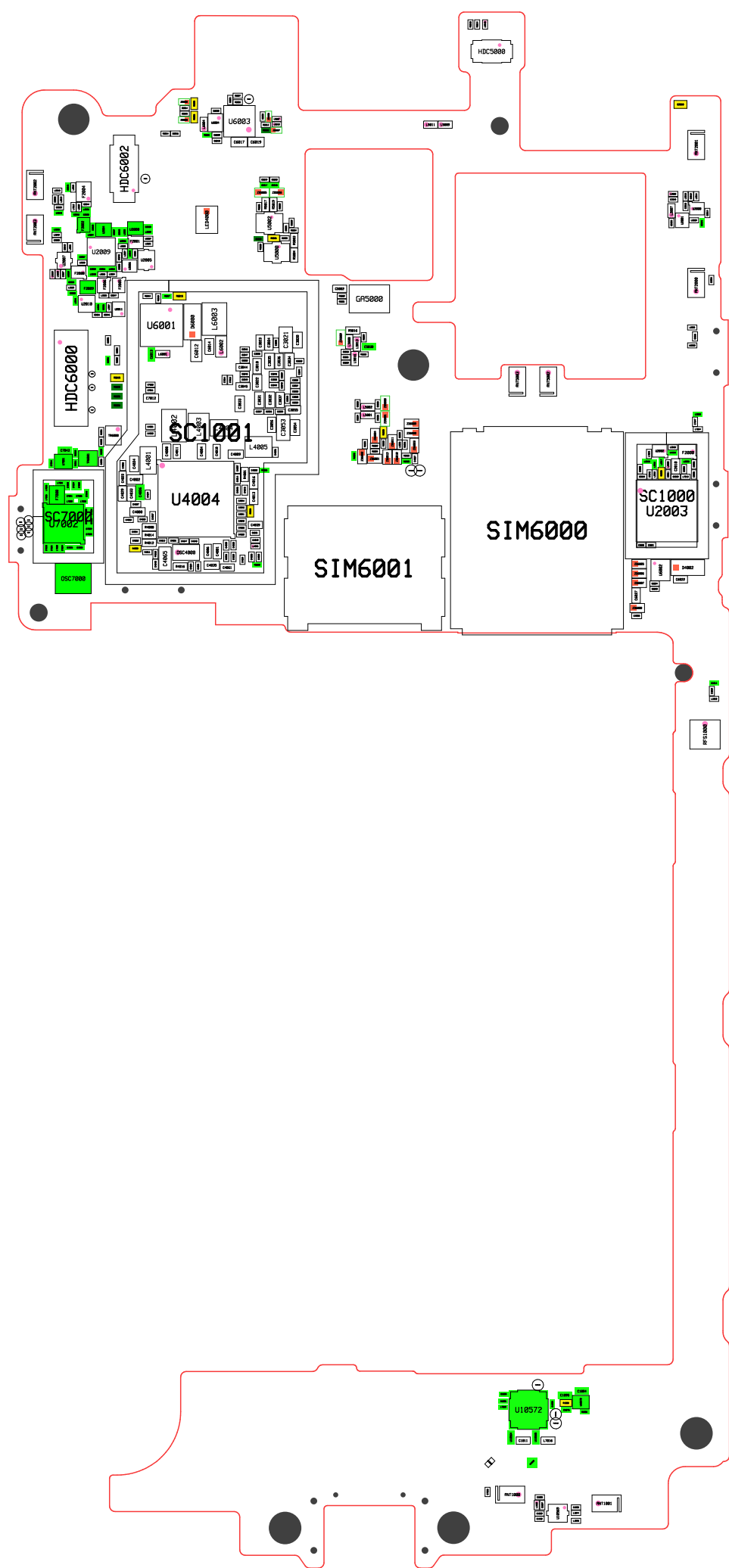
Sheet06 : 5" LCD, CAM PART, SIM_SD PART

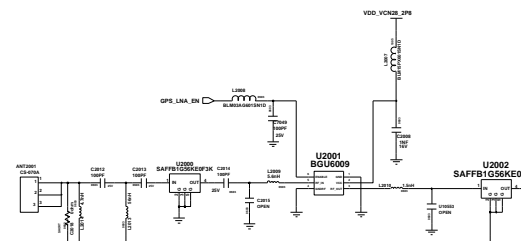
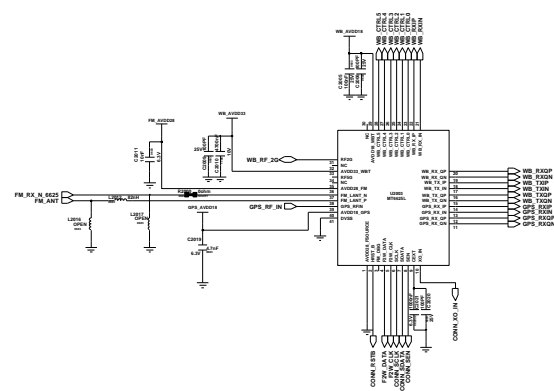
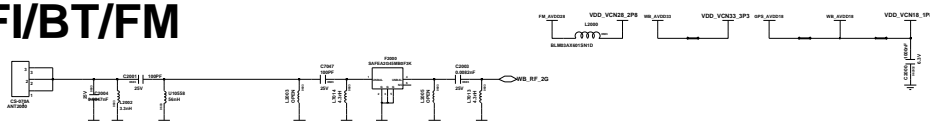
Sheet07 : MEMORY

Engineer			
Drawn by			
FIELD CHK	TITLE		Size A2
DOC CTRL CHK			
WPS ENGR CHK			
QA CHK	REV	Drawing Number	Sheet 0/3

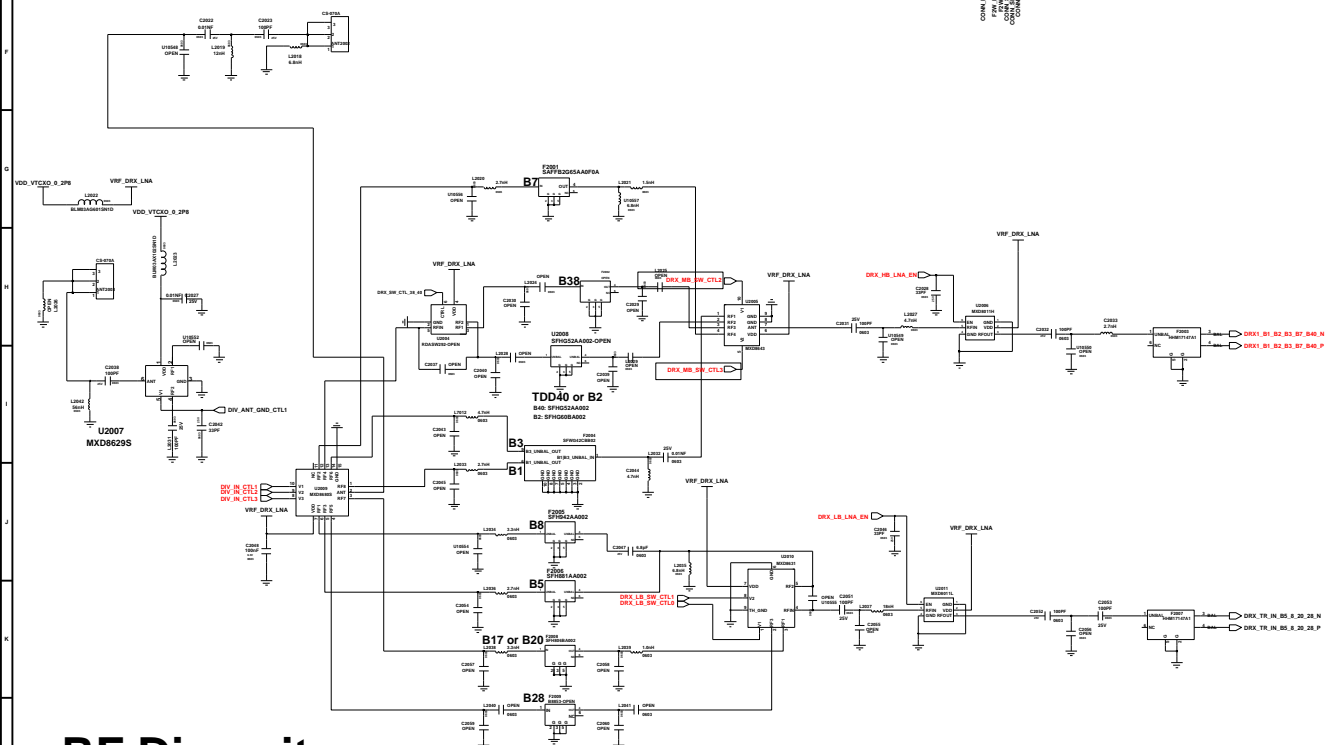
Changed by	Date Changed	
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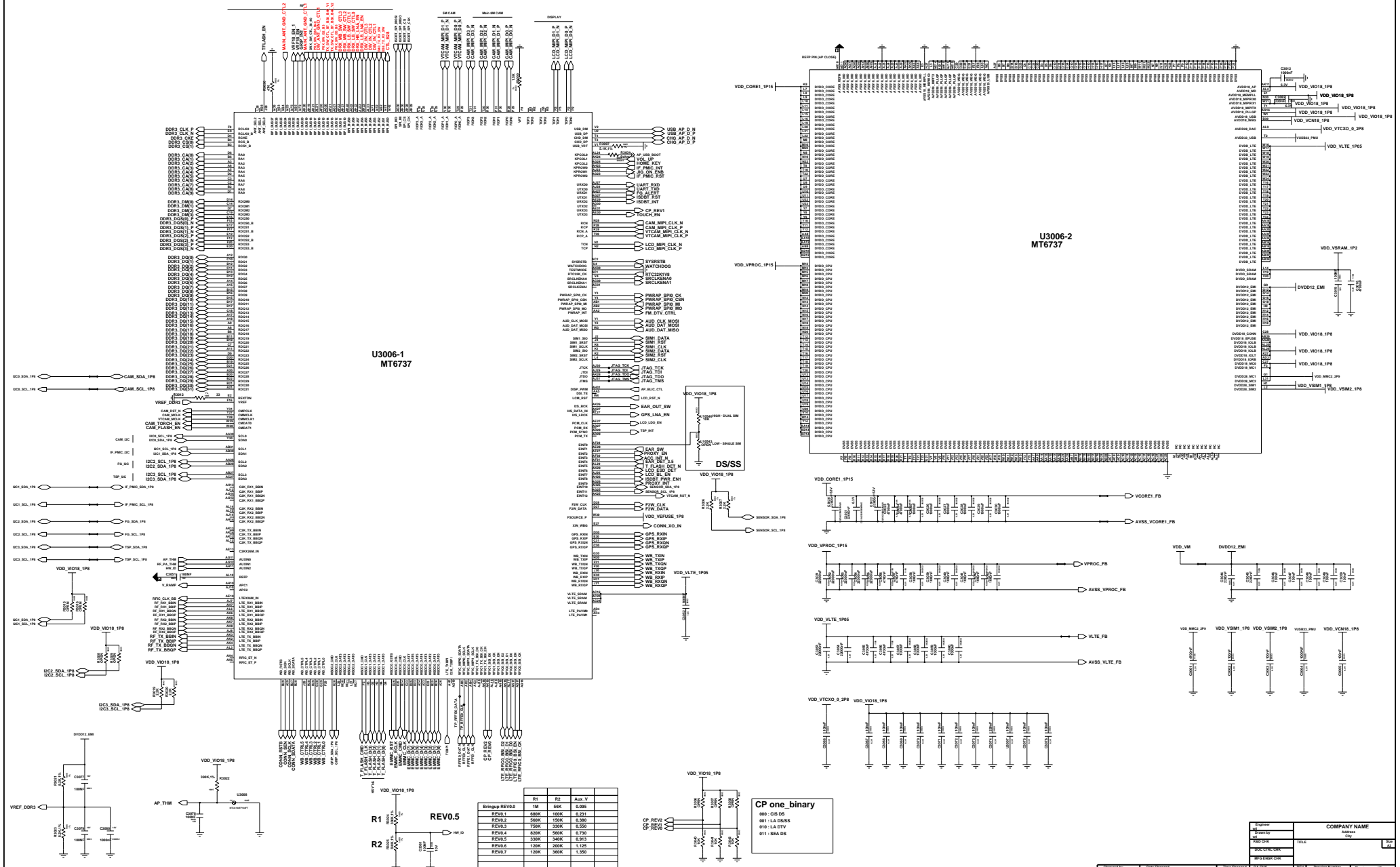


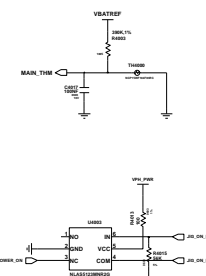
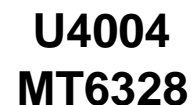
GPS



RF Diversity

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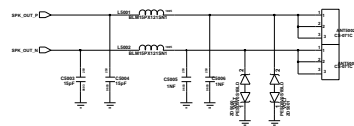


USER INTERFACE PART



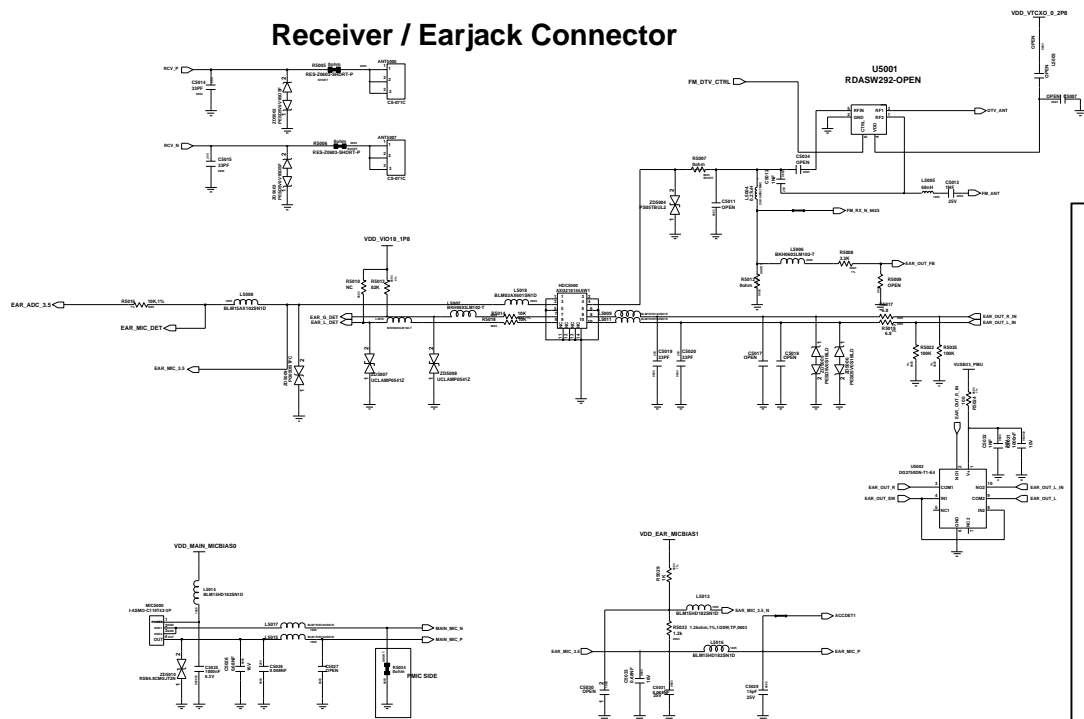
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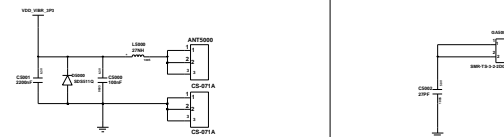
Speaker

Receiver / Earjack Connector

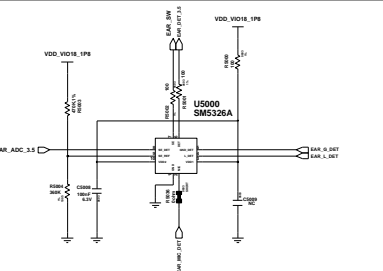


Main MIC

Ear MIC

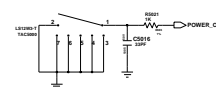


Vibrator PAD

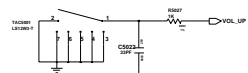


EARPHONE Detect

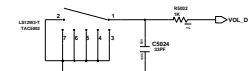
KEY PART



Power_ON

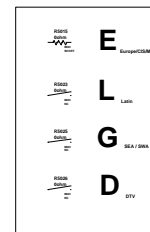


VOL_UP



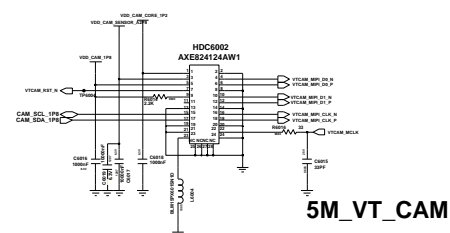
VOL_DOWN

Home Key

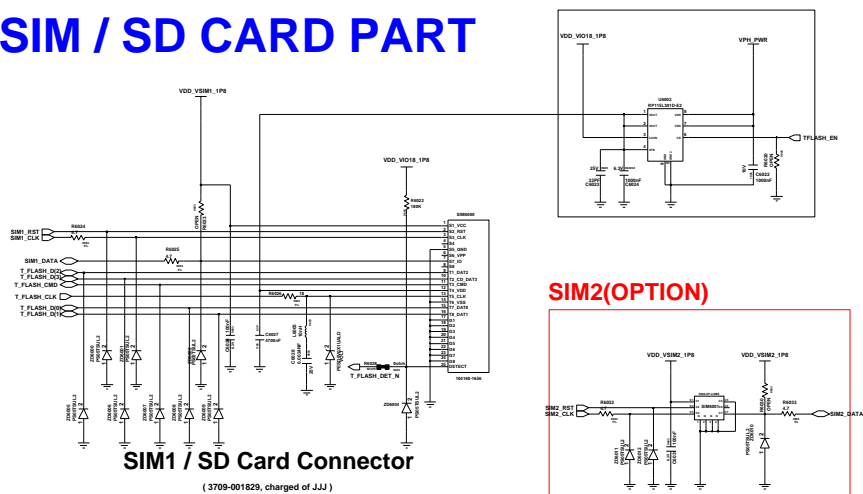


Engineer	AUDIO / MOTOR / KEY
DESIGNER	DATE
CHECKER	DATE
APPROVER	DATE
DATE	DATE

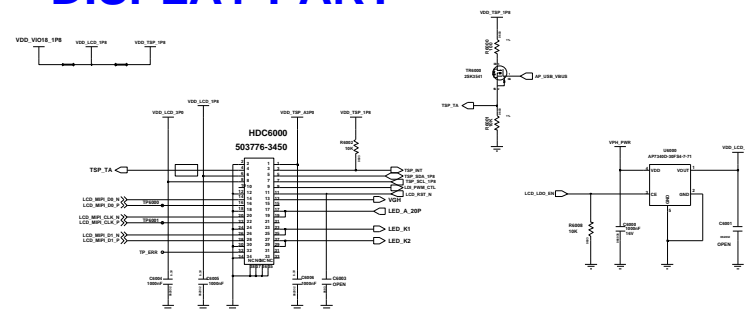
CAMERA PART



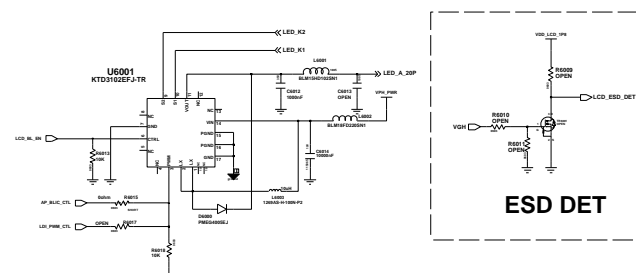
SIM / SD CARD PART



DISPLAY PART

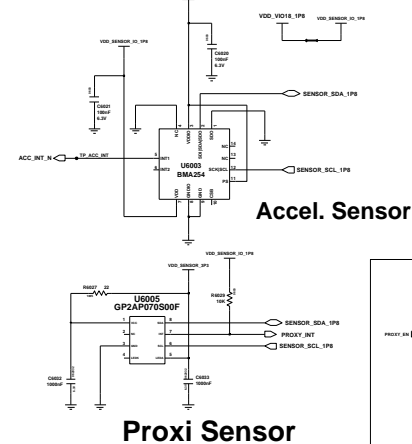


LCD Connector(5")



10 LED LCD DCDC

SENSOR PART

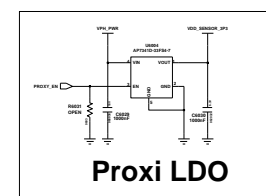


JTAGs

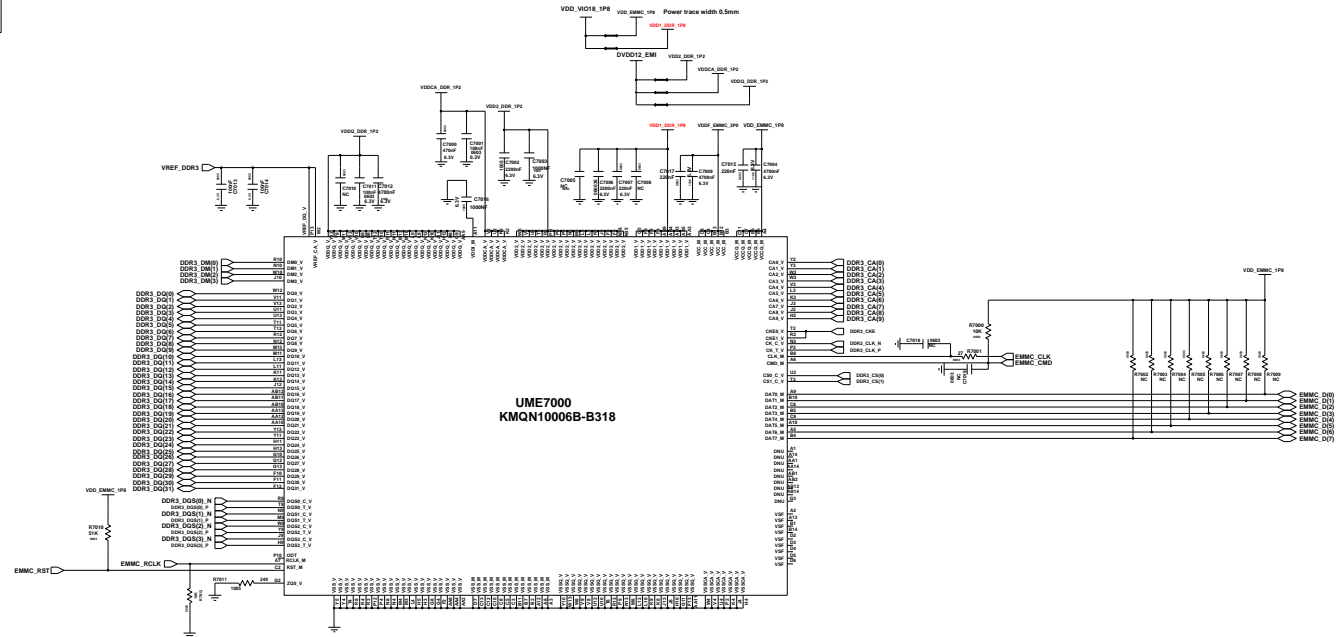
PCB JTAG CON

[illegible]

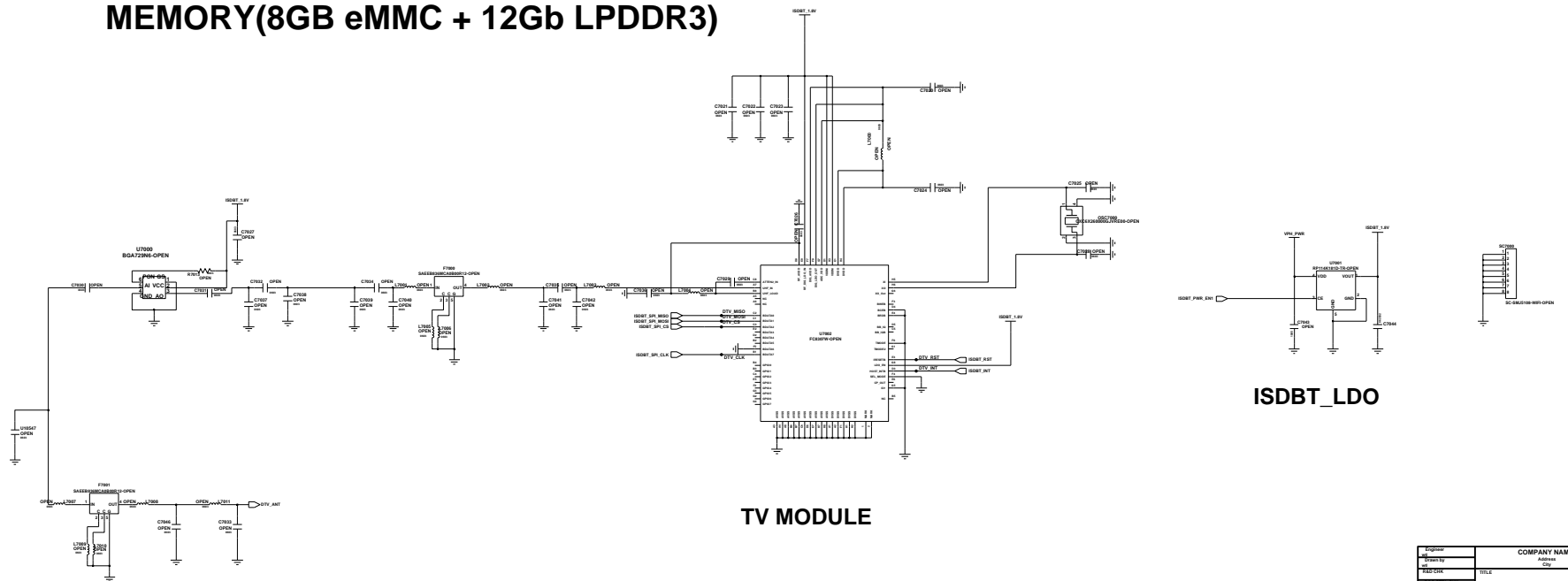
ARRAY JTAG



JTAG-SPI			
Engineer:		CAMERA / LCD / SIM,SD	
Drawn by:			
R&D CTR:			
QC CTR CTR:			
MFG ENG CTR:			
Checked:	QA CTR:	REV	Drawing Number:



MEMORY(8GB eMMC + 12Gb LPDDR3)



TV MODULE

Engineer [Signature]	COMPANY NAME			
Drawn by [Signature]	Address			
READ CHK	TITLE			Size A3
DWG CTRL CHK				
MFG ENG CHK				
QA CHK	REV	Drawing Number	Sheet	4/8