

plan9

plan9 is an implementation of the Cellular Automata Research Platform (CARP) developed at NTNU targeting Convey WX coprocessors. It consists of a hardware component implemented using Chisel, Verilog and VHDL (residing in `/hardware`) and a software API written in C (`/software`). For instructions on synthesizing and deploying the hardware component, see [here](#). Similarly for the API, [here](#).