

# **Fall 2021 CSE 436/536 Digital Integrated Circuits**

## **Assignment 2 Report**

I needed to make an AND gate in my assignment. Since the design and cost of my AND gate would be more affordable, I designed 1 NAND gate and 1 inverter. I took the A B inputs from the NAND and gave the result to the inverter.

To test the results, I gave values to my A and B inputs in the and.spice file.

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