



Course Name: PRIN ELECT ENG I LAB

Course Number and Section: 14:332:223:01

Experiment: Prelab 4

Lab Instructor: ANASTASIOS KLENIATIS

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C.3.1 How many voltage levels will this DAC provide for us at the DAC output?

8 levels

C.3.2 If the input voltage range is $0 \leq v_{\text{bit}} \leq 5V \rightarrow$ what will the voltage step value $\Delta v = ?$

0.625V

C.3.3 For a binary input 001, what will be the associated output voltage $v_{v\ 00} = ?$

0.625V

C.3.4 For a binary input 010, what will be the associated output voltage $v_{v\ 00} = ?$

1.25V

C.3.5 For a binary input 100, what will be the associated output voltage $v_{v\ 00} = ?$

2.5V

C.3.6 For a binary input 110, what will be the associated output voltage $v_{v\ 00} = ?$

3.75V