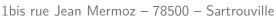
Pierre Avital





French - Swiss

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Professional Experience

Valeo CDA / Centrale-SupElec

Créteil / Gif-sur-Yvettes, France

Research Engineer, Radio Source Localisation in Complex Environments

Nov 2017 - Present

With the endgoal of enabling smartphones to function as passive keys for vehicules, I was tasked with researching localisation methods for current generation smartphones.

The two main constraints of this research were the precision of the localisation, as overly lenient localisation would lead to high insurance costs; and non-cooperativeness, as no alterations were to be made to the smartphones for this function to be provided. Power consumption constraints had already bounded the project into relying solely on Bluetooth LE for both communicating with and locating the smartphone.

Due to my versatility in programming and networking, I was also routinely tasked with providing the means to enable the various sub-systems or the project to interract over a diverse set of mediums under varying constraints. Overall, I was lead to:

- o Study, implement and evaluate techniques for Radio Source Localisation, such as MUSIC and ESPRIT.
- o Define and implement various protocols to provide better and more flexible reliability mechanisms.
- Interface a tacheometer to local network and providing user-friendly calibration procedures to build a Local Positioning System for collection of ground-truth measurements for both training and evaluation of localisation algorithms.
- o Provide software interfacing with various measurement equipement for remote and automated data collection.

Technologies: Bluetooth LE, Embedded Systems, DSP, Antenna Networks

Languages : Python, C, C++, Rust

Pôle Judiciaire de la Gendarmerie Nationale

Pontoise, France

Internship: Connected Vehicules Security

Feb 2017 - Aug 2017

Starting with the Reverse Engineering (RE) of Android applications, the subject evolved towards the use of Software Defined Radios to listen to Bluetooth communications.

During RE phase, I studied closed-source Android applications through the use of commercially available tools, before developing my own plugin to integrate open-source tools into the Atom Text Editor, implementing most of the functions of commercial static RE tools, and integrating them as well as a debugger to Atom's UI.

During BT phase, I modified drivers, firmware and gateware of open source SDR projects to tailor them to the very specific signal processing needs that come with Bluetooth's physical layer and real time computation requirements on wide-band signals.

Technologies: Atom, ADB, JDB, GNU Radio, Quartus Prime, FPGA, Gateware, Firmware, DSP, Wireless Languages: Smali (Java Assembly), JavaScript, Bash, Python, VHDL

Castrol innoVentures Reading, UK

Internship: Business Opportunity Development

Sep 2015 - Feb 2016

Inside a team comprised of 8 interns of diverse origin and background and myself, I was tasked with developping new business opportunities for Castrol, following methods commonly used by start-ups. As the person with the widest scientifical background, I also took the role of scientific consultant for my team, studying the feasability of some of the projects we developed.

Academics

Scholarship

Laboratoire des Signaux et Systèmes, Centrale Supélec

Gif-sur-Yvettes, France

PhD Student

Sep 2018–Present

From September 2018 onward, my work as Research Engineer for Valeo was paired with working as a PhD student at Centrale Supélec's Laboratory of Signals and Systems. The goal for this was to reenforce ongoing studies for the Valeo project with a more scientifically rigorous approach, notably to the optimisation of measurement characteristics.

Université de Technologie de Troyes

Troyes, France

Ingénieur Technologies Mobiles Systèmes Embarqués / Master Sécurité des SI 2011–2017 Building solid foundations in Physics, Chemistry and Mathematics through the preparatory courses, I have taken a particular interest in low-level computer science and signal processing. To complete my training, I decided to take up computer security, and took a liking to cryptography.

Notable Projects.

Zyng testing for computer vision

Our goal through this projet was to study Xilinx's Zynq platform, an ARM Cortex embedded with a FPGA, as a preparation for experimentation courses. Our main focus was the use of the FPGA as a versatile hardware accelerator for image processing.

FSG002 - Fox Music Analysis Library

Developped in C#, the goal for this library was to analyse music in real time in order to tie game mechanics to it. This goal later shifted to the translation of music to MIDI or musical scores.

Skillset

Languages

- French: Mother tongue
- **English**: Fluent, with knowledge of scientific and technological vocabularies. BULATS C1 (2012), Erasmus C2 (2016), 9 months spent in english-speaking countries, including 6 as an intern in Reading, UK.
- **German**: Basics. B1/B2. 1 month spent in german-speaking countries.
- Japanese: Basics. 3 weeks spent in Japan in 2014, renewed in 2017.
- **Programming (favorites) :** Rust, C/C++, Python, Javascript.
- o Programming (occasional use): Bash, Java, Smali, MATLAB, HTML, CSS...
- Surface level: VHDL, Prolog, LISP...

Software

- Operating Systems : Frequent use of MacOS, Windows and Debian-based Linuces.
- Office : Microsoft Office, iWork, Google Docs, LATEX
- Video editing : Adobe Premiere, iMovie
- IDE : Visual Studio, XCode, IntelliJ, Xilinx SDK, Atom+Plugins, GNU Radio Companion

Others

- **Electronics**: Digital and analogical signal processing. Linking microprocessors to sensors and motors.
- **Signal processing**: Image processing, steganography, watermarking. Sound processing and analysis, with basic knowledge of psycho-acoustics. Inverse Problems.
- Reverse Engineering: Improved through the development of a RE-assisting plugin. Android/Java focused.
- Wireless: Information and Communication theory. Understanding of Software-Defined Radios.

Hobbies

- Music: Originally trained in violin and music theory, I currently play the guitar.
- Computers: I program for my own use, and go to great length to tailor my OS to my needs.