CALL FOR PAPERS
IEEE INTERNATIONAL
CONFERENCE
on Microelectronic Devices Circuits and Systems

10th and 12th AUGUST 2017

Technical Area

Process technology,
Device modelling and simulation
Digital Architecture Design
Analog, mixed signal and RF IC Design
Interconnect reliability, integrated circuit manufacturing, ,
On chip Signal integrity, Cross talk, ESD
Low-power design
DSP design
DFT, fault modeling and simulation,
FPGA architecture and FPGA circuit design,
Processor and Memory Design
On-chip communication architectures and networks-on-chip
Hardware platforms for Internet-of-Things (IoT) devices
Sensors and Wireless Network

Conference Venue:
VIT University, Vellore
Tamil Nadu, India

Important Dates

Deadline for Submission:
20th May 2017
Notification of Acceptance:
20th June 2017
Camera Ready Paper Submission:
10th July 2017
Early Bird Registration:
30th June 2017
For Further Details:
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