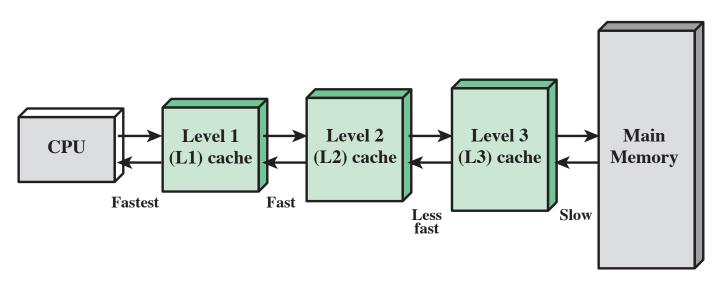


(a) Single cache



(b) Three-level cache organization

Figure 1.16 Cache and Main Memory