

(a) Speedup with 0%, 2%, 5%, and 10% sequential portions

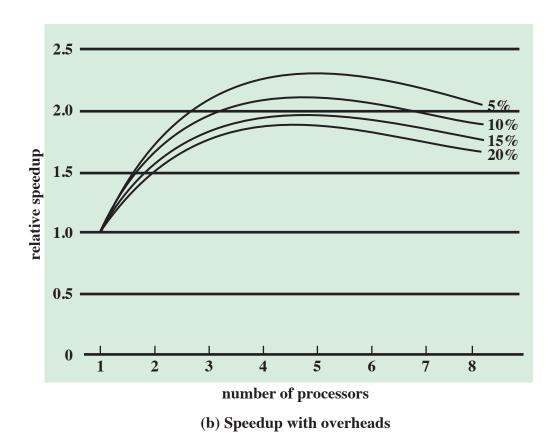


Figure 4.7 Performance Effect of Multiple Cores