OCXO

File: OCXO.kicad_sch

DAC_CPU

File: DAC_CPU.kicad_sch

Block Diagram

Sheet: /
File: Master_OCXO_DAC.kicad_sch

Title: OCXO with DAC control for PI4

| Size: A4 | Date: 2022-08-21 | Rev: V1.0 |
| KiCad E.D.A. kicad (6.0.7-1)-1 | | Id: 1/3 |

AVR-ISP-6

J1

VCC 2

MISO 1 MISO
MOSI 4 MOSI
SCK 3 SCK
RST 5 RST

GND 6

+5V

REF_V

C1
100n

U1
ATtiny85-20S

SCK MISO MOSI RST

VCC 8

AREF/PB0 5
PB1 6
PB2 7
XTAL1/PB3 2
XTAL2/PB4 3
RESET/PB5 1

GND 4

TP1
TestPoint

C2
4u7

C3
100n

R1   R2

4k7   4k7

J2
1   2
3   4
EXT I2C

External Alignment Jig
Connection

U2
MAX5217

SCL 3   VDD 7   REF 1
SDA 4

AUX 5   OUT 6   OCXO_F
ADDR 2

GND 8

PPM sync input

R3   J3
12k   1
      2

RV1
5V

CPU + DAC

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Not Mounted on PCB

V1.2 Errata

Changed Output driver
to NB3L551
R9/R10 Optional

CTI 10Mhz OCXO

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