Master Calculator Architecture Diagram

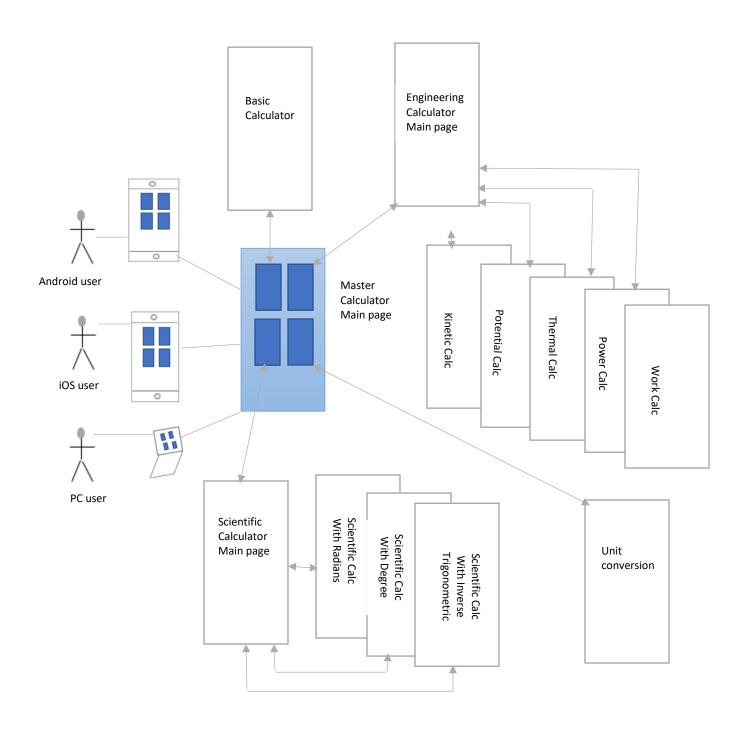


Fig. 1 Physical architecture diagram

Basic Calculator logic diagram

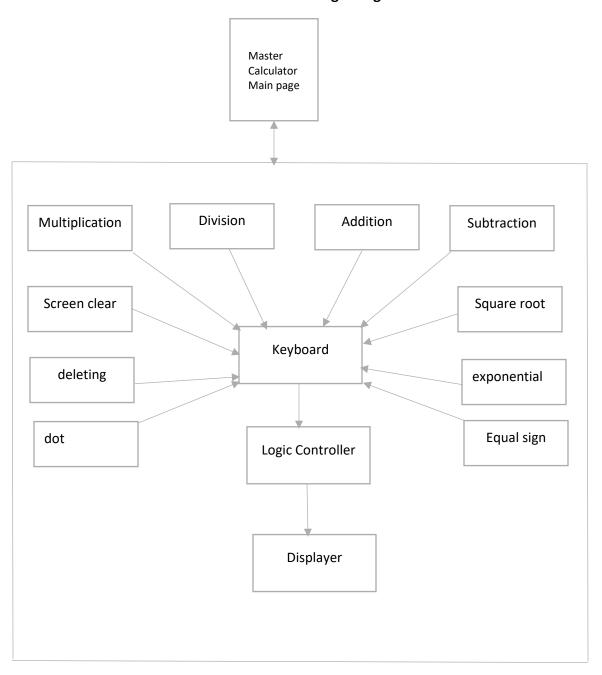


Fig. 2. Basic Calculator Logic Controller Diagram

Engineering Calculator logic diagram

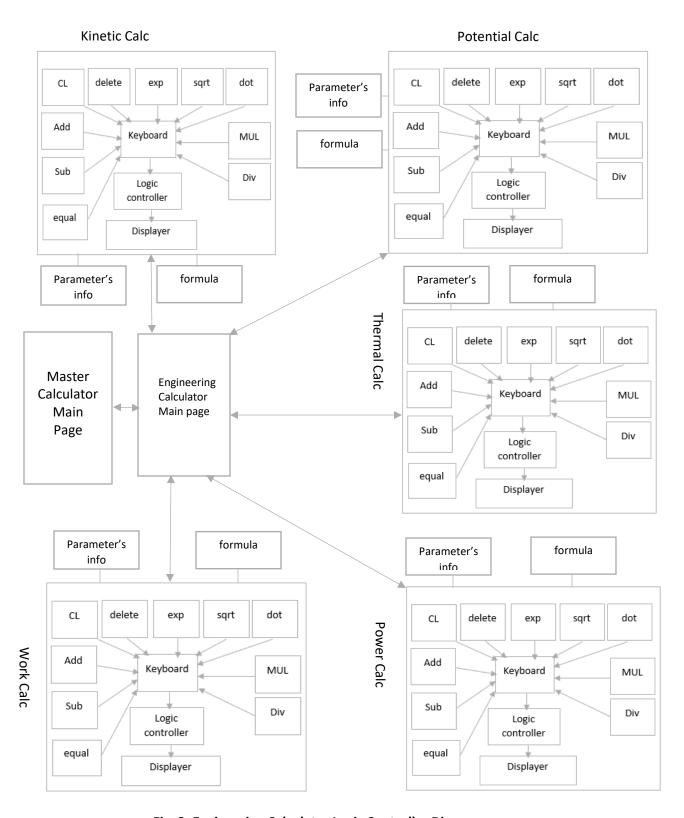


Fig. 3. Engineering Calculator Logic Controller Diagram

Scientific Calculator Logic Diagram

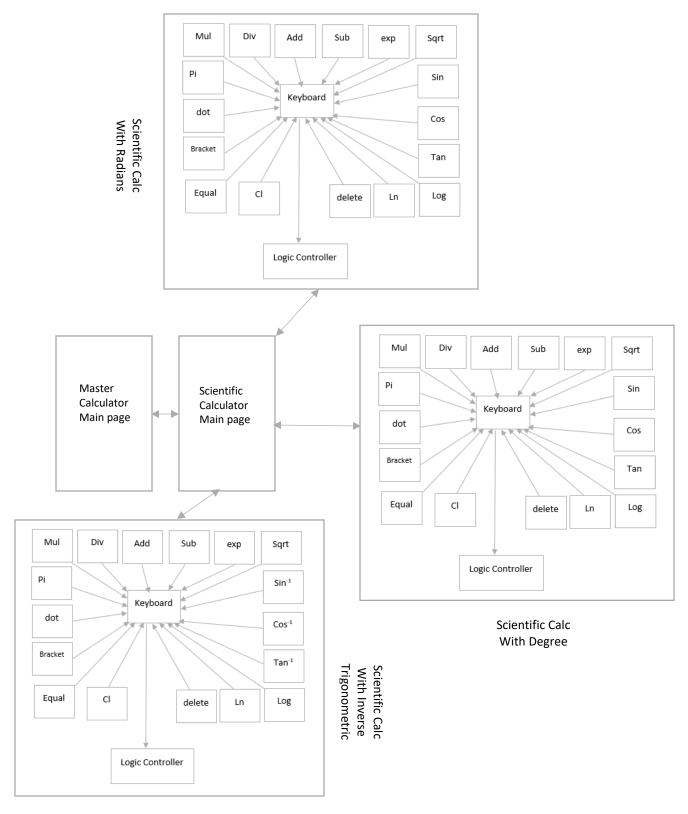


Fig. 4. Scientific Calculator Logic Controller Diagram

Unit Conversion Diagram

This part of the master calculator is not yet developed.

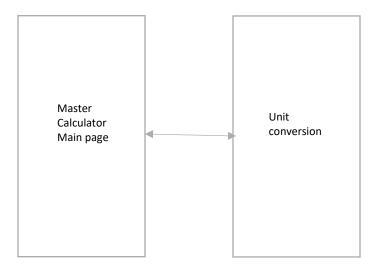


Fig. 5. Unit conversion Diagram