TENAGA NASIONAL			
FORM REF	1725IED-FAT-TR-FTRT		
FORM DESCRIPTION	RTU FAT RECORD – REDUNDANCY TEST	FORM VERSION	02
DD 0 17 07 D 7 7 7	RTU SERIAL NO:	PRODUCT DETAILS	DF1725IED RTU
PROJECT DETAILS	CONTRACT NO:	TEST RECORD SHEET	38 of 39
FAT – TEST RECORD REF:	CHANNEL REDUNDANCY TEST	RECORD SHEET NO.	1 of 1

Channel Redundancy Test

1.0	IEC101

No.	Procedure	OK	NO
1	Start by polling IEC101 through Main Channel (COM2).		
2	Simulate any Digital Input signal and verify ASE2000 received the signal.		
3	Do not stop ASE2000 and move the ASE Serial Cable from COM2 to COM3.		
4	IEC101 communication will resume through COM3 after around 20 – 30 seconds.		
5	Simulate any Digital Input signal and verify ASE2000 receive the data.		

2.0 IEC104

No.	Procedure	OK	NO
1	Start by polling IEC104 through Main Channel (Ethernet1).		
2	Simulate any Digital Input signal and verify ASE2000 received the signal.		
3	Stop ASE2000 and move the LAN Cable from ETH1 to ETH2.		
4	Start polling IEC104 again through ETH2.		
5	Simulate any Digital Input point and make sure ASE2000 receive the data.		

3.0 DNP3

No.	Procedure	OK	NO
1	Start by polling DNP3 through Main Channel (Ethernet1).		
2	Simulate any Digital Input signal and verify ASE2000 received the signal.		
3	Stop ASE2000 and move the LAN Cable from ETH1 to ETH2.		
4	Start polling DNP3 again through ETH2.		
5	Simulate any Digital Input point and make sure ASE2000 receive the data.		

Tested By: (Signature/Official Stamp)	Witnessed By: (Signature/Official Stamp)
Tester's	TNB QAI's
Name:	Name:
Date:	Date: