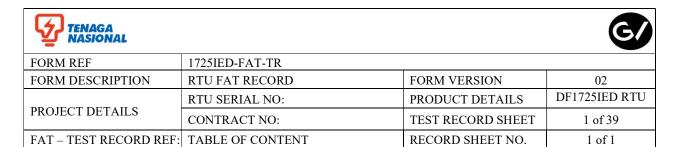
Grid Vision T&D Sdn.Bhd.

Factory Acceptance Test Record DF1725IED RTU

Project Name	
Contract No.	

Approval				
	Name	Company	Date	Signature
Prepared by:	Muhammad Nor Farhan	Grid Vision T&D	17 th December 2024	Jy.
Reviewed by:	Lim Yee Chin	Grid Vision T&D	17 th December 2024	At .
Verified by:	Ir. Ts. Ramlah Binti Awang	Tenaga Nasional Berhad		

Rev 02 / Eff. Date: 17/12/2024 QA05-F06



Change Log

No.	Date	Rev No.	Remark
1	22/12/2022	00	Initial Release
2	24/2/2024	00	• Revise parameter setting for AI, IEC101.
3	16/5/2024	01	Separate Test Procedure and Test Record
4	10/9/2024	01	 Update firmware version and date for MCU-1-A and MCU-4-A. Change Force to 0 setting under AI Card Module.
5	17/12/2024	02	 Add Ecolink LED in accessories list. Add another firmware check under Processor Functional Test. Update IEC104 Channel Redundancy Test Procedure. Combine Physical and Functional result in 1 page. Add checkbox and signature box in FAT flowchart. Add tester's competency verification. Update IOA for I/O Fail. Add COM-6-A Module. Add Appendix for I/O Test Setup.





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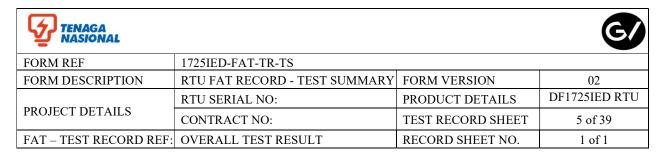
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1.0 Overall Pre-FAT/FAT Result

RTU Serial No.	
PASS	
FAIL	
REMARKS:	
Tested By: (Signature/Official Stamp)	Witnessed By: (Signature/Official Stamp)
17	
Tester's	TNB QAI's
Name: Date:	Name: Date: