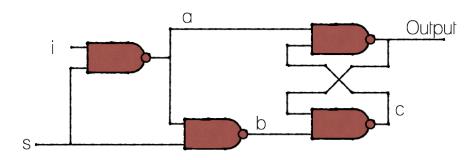
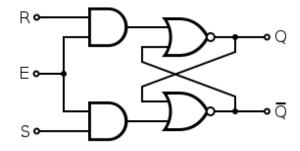
Q1) According to the flip-flop below, fill the empty fields in the table;



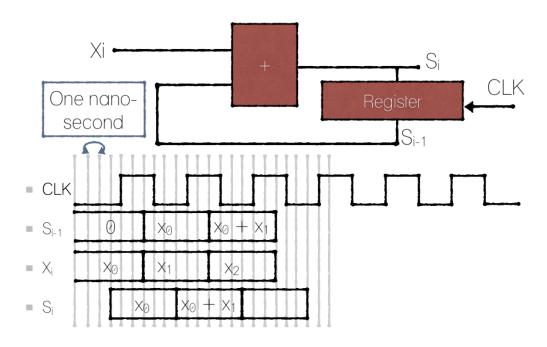
	i	S	Result
First State	1	1	
Second State	1	0	
Third State	0	0	
Fourth State	0	1	
Fifth State	0	0	
Sixth State	1	0	
Seventh State	1	1	

Q2) According to the flip-flop below, fill the empty fields in the table;



	R	E	S	Q
First State	1	1	0	
Second State	0	1	0	
Third State	0	1	1	
Fourth State	0	0	1	
Fifth State	1	0	1	

Q3) What's the <u>combinational logic delay</u> and the <u>CLK-to-Q Delay</u> for the diagram below?



Practice Questions for Tuesday's Quiz

Answers:

Q1)

- /			
	i	S	Result
First State	1	1	1
Second State	1	0	1
Third State	0	0	1
Fourth State	0	1	0
Fifth State	0	0	0
Sixth State	1	0	0
Seventh State	1	1	1

Q2)

	R	E	S	Q
First State	1	1	0	0
Second State	0	1	0	0
Third State	0	1	1	1
Fourth State	0	0	1	1
Fifth State	1	0	1	1

Q3) Combinational logic delay is 3 nano-seconds CLK-to-Q delay is 2 nano-seconds