Design Rules Verification Report

Filename : C:\Users\pablo\Desktop\AnalogIO-CAN-HW\Altium_prj\AnalogIO-CAN-HW-Re

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.381mm) ((OnLayer('GND Layer') OR OnLayer('Power Layer'))),(All)	0
Clearance Constraint (Gap=0.203mm) (InNamedPolygon('GND_L01_P059_OSC_GUARD')),(All)	0
Clearance Constraint (Gap=0.102mm) (All),(All)	0
Clearance Constraint (Gap=0.381mm) (InNamedPolygon('GND_L01_P106')),(All)	0
Clearance Constraint (Gap=0.203mm) (InNamedPolygon('GND_L01_P094')),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.102mm) (Max=1816.048mm) (Preferred=0.102mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.3mm) (Conductor Width=0.102mm) (Air Gap=0.102mm)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Minimum Annular Ring (Minimum=0.076mm) (All)	0
Hole Size Constraint (Min=0.2mm) (Max=6.3mm) (All)	0
Hole To Hole Clearance (Gap=0.25mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.102mm) (IsPad),(All)	0
Silk to Silk (Clearance=0mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Room U_Peripherals (Bounding Region = (59.259mm, 39.208mm, 86.348mm, 85.205mm)	0
Room U_PowerModule (Bounding Region = (30.461mm, 46.429mm, 75.279mm, 104.961mm)	0
Room U_CanTransceiver (Bounding Region = (24.055mm, 39.243mm, 70.673mm, 70.897mm)	0
Room ARM4 (Bounding Region = (88.646mm, 38.862mm, 130.048mm, 56.134mm) (InComponentClass('ARM4')	0
Room U_AnalogueIsolatedReceiver (Bounding Region = (75.131mm, 61.256mm, 87.338mm, 80.984mm)	0
Room U_CPU (Bounding Region = (66.891mm, 38.316mm, 86.475mm, 70.03mm) (InComponentClass('U_CPU')	0
Room ARM1 (Bounding Region = (88.646mm, 90.678mm, 130.048mm, 107.95mm) (InComponentClass('ARM1')	0
Room ARM2 (Bounding Region = (88.646mm, 73.406mm, 130.048mm, 90.678mm) (InComponentClass('ARM2')	0
Room ARM3 (Bounding Region = (88.646mm, 56.134mm, 130.048mm, 73.406mm) (InComponentClass('ARM3')	0
Height Constraint (Min=0mm) (Max=1816.048mm) (Prefered=12.7mm) (All)	0
Total	0