

## Design Rules Verification Report

Filename : C:\Users\pablo\Desktop\AnalogIO-CAN-HW\AnalogIO-CAN-HW-Rev1.PcbDoc

Warnings 0  
Rule Violations 0

Warnings	
Total	0

  

Rule Violations	
Clearance Constraint (Gap=15mil) ((OnLayer('GND Layer') OR OnLayer('Power Layer'))),(All)	0
Clearance Constraint (Gap=8mil) (InNamedPolygon('GND_L01_P059_OSC_GUARD')),(All)	0
Clearance Constraint (Gap=4mil) (All),(All)	0
Clearance Constraint (Gap=15mil) (InNamedPolygon('GND_L01_P106')),(All)	0
Clearance Constraint (Gap=8mil) (InNamedPolygon('GND_L01_P094')),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=4mil) (Max=71497.938mil) (Preferred=4mil) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=11.811mil) (Conductor Width=4mil) (Air Gap=4mil) (Entries=4)	0
Power Plane Connect Rule(Direct Connect )(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Minimum Annular Ring (Minimum=3mil) (All)	0
Hole Size Constraint (Min=7.874mil) (Max=248.031mil) (All)	0
Hole To Hole Clearance (Gap=9.842mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mil) (All),(All)	0
Silk To Solder Mask (Clearance=4mil) (IsPad),(All)	0
Silk to Silk (Clearance=0mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Board Clearance Constraint (Gap=0mil) (All)	0
Room U_Peripherals (Bounding Region = (2333.047mil, 1543.638mil, 3399.512mil, 3354.512mil)	0
Room U_PowerModule (Bounding Region = (1199.236mil, 1827.929mil, 2963.724mil, 4132.307mil)	0
Room U_CanTransceiver (Bounding Region = (947.063mil, 1545.016mil, 2782.386mil, 2791.205mil)	0
Room ARM4 (Bounding Region = (3490mil, 1530mil, 5120mil, 2210mil) (InComponentClass('ARM4'))	0
Room U_AnalogIsolatedReceiver (Bounding Region = (2957.929mil, 2411.669mil, 3438.488mil, 3188.331mil)	0
Room U_CPU (Bounding Region = (2633.5mil, 1508.5mil, 3404.512mil, 2757.071mil) (InComponentClass('U_CPU'))	0
Room ARM1 (Bounding Region = (3490mil, 3570mil, 5120mil, 4250mil) (InComponentClass('ARM1'))	0
Room ARM2 (Bounding Region = (3490mil, 2890mil, 5120mil, 3570mil) (InComponentClass('ARM2'))	0
Room ARM3 (Bounding Region = (3490mil, 2210mil, 5120mil, 2890mil) (InComponentClass('ARM3'))	0
Height Constraint (Min=0mil) (Max=71497.938mil) (Preferred=500mil) (All)	0
Total	0