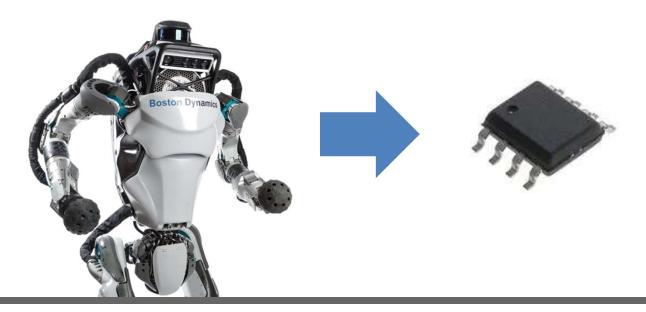


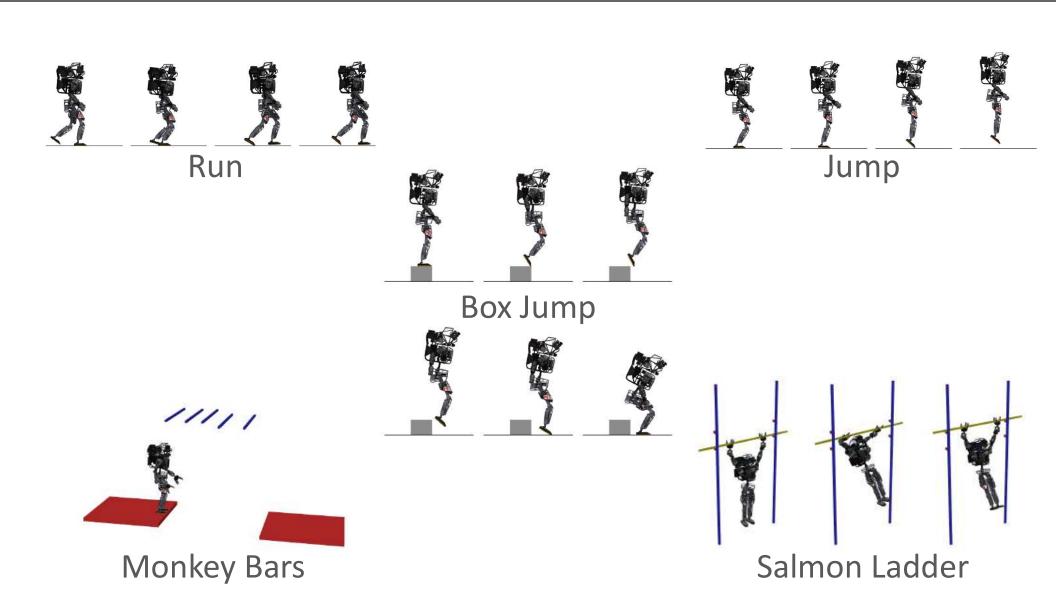
A Design Methodology for Domain-Specific Accelerators
Parameterized by Robot Morphology



Sabrina M. Neuman, Brian Plancher, Thomas Bourgeat (MIT), Thierry Tambe, Srinivas Devadas (MIT), and Vijay Janapa Reddi

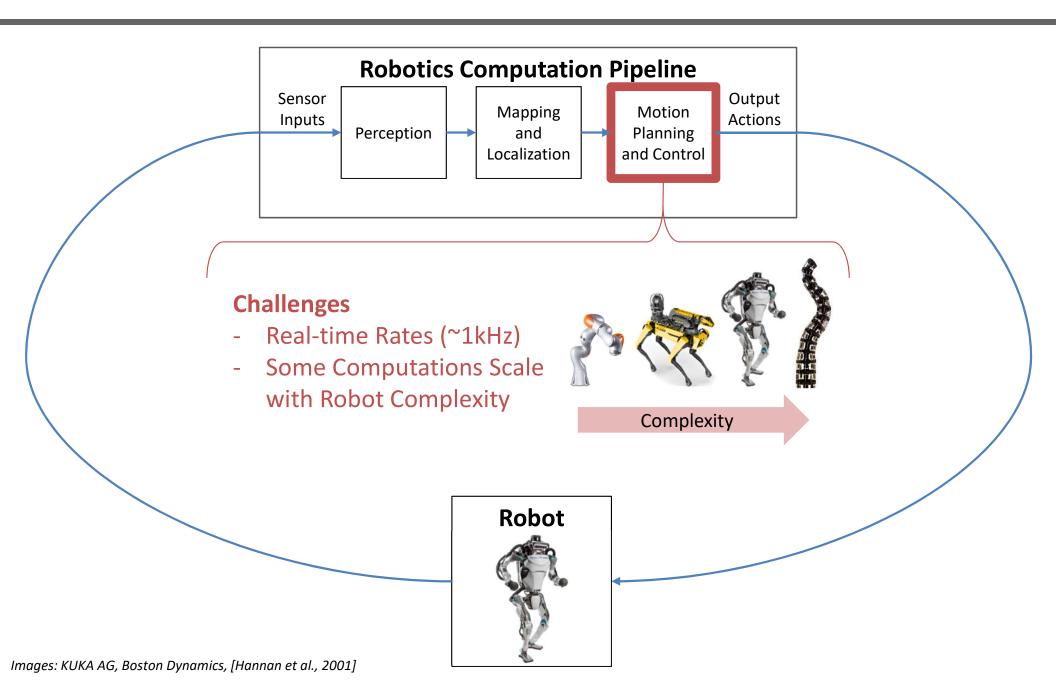


Goal: Agile, Robust, Untethered Motion!



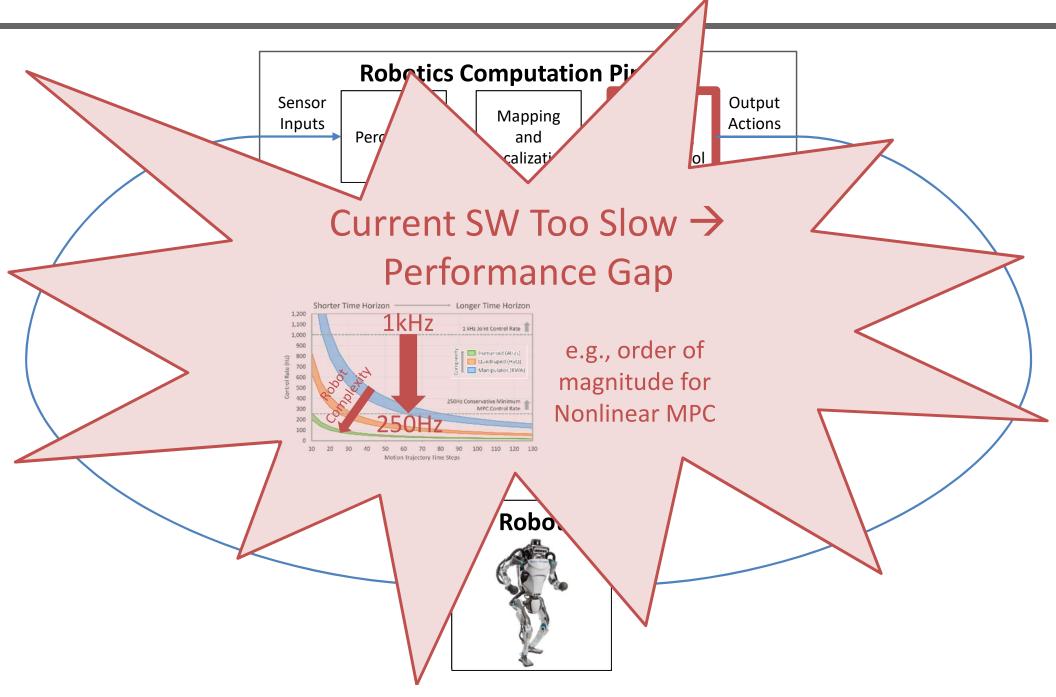


Need Real-time Performance



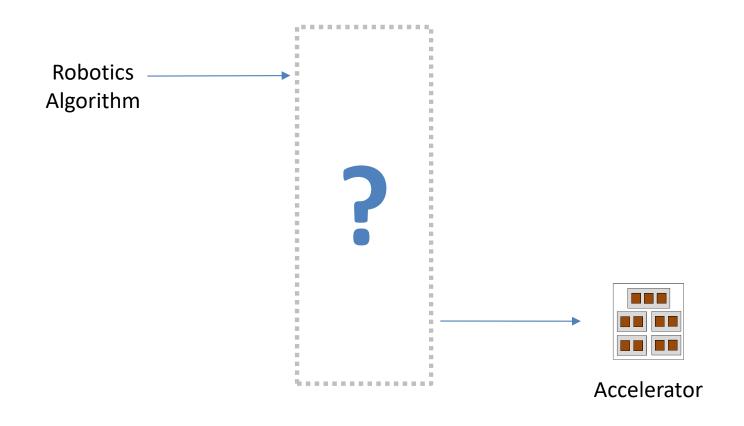


Need Real-time Performance



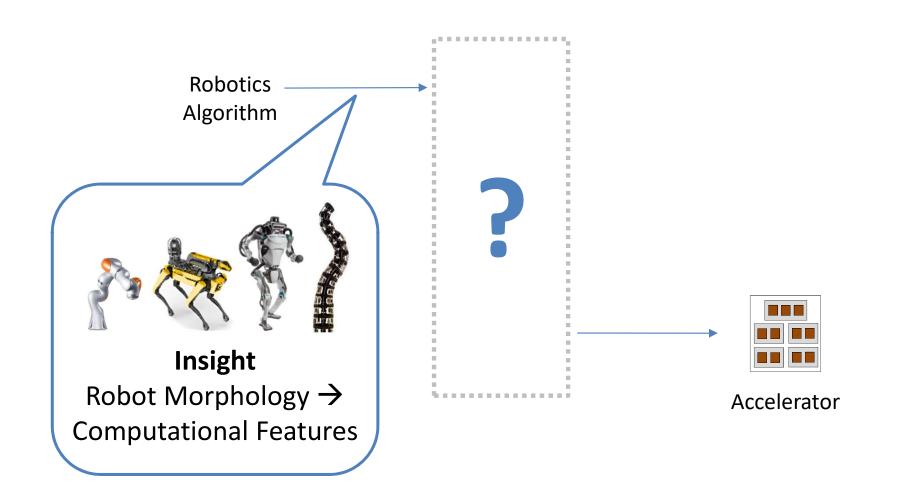


Solution: Hardware Acceleration



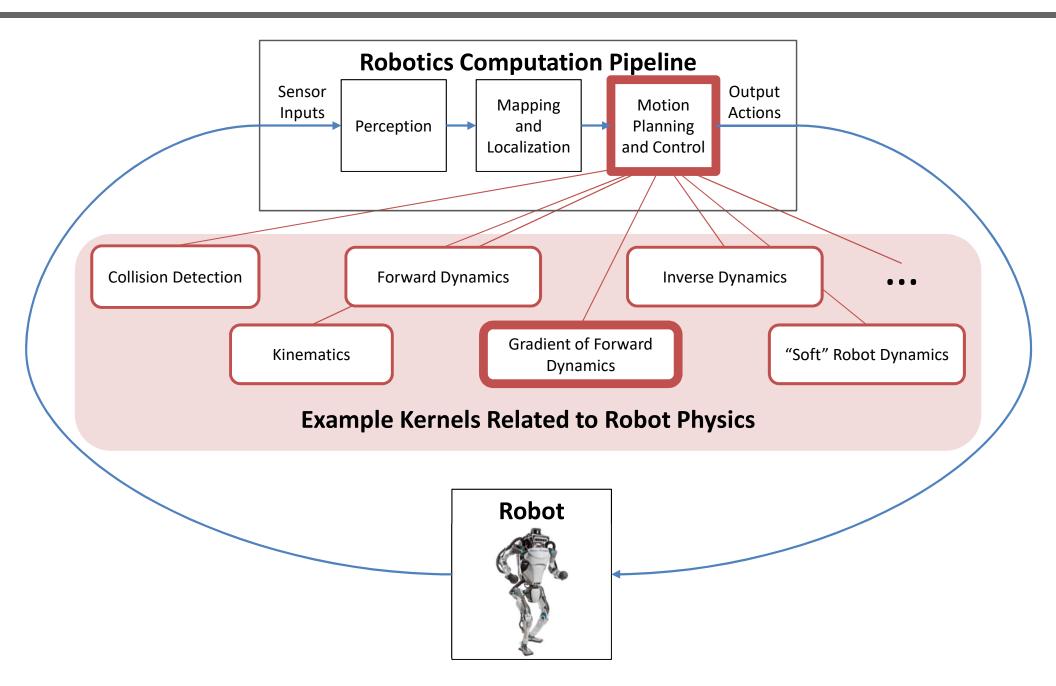


Insight: Robot Morphology

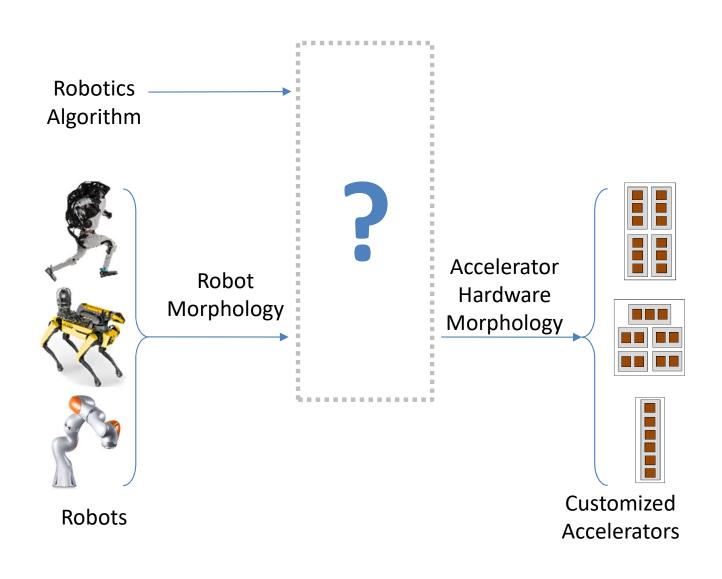




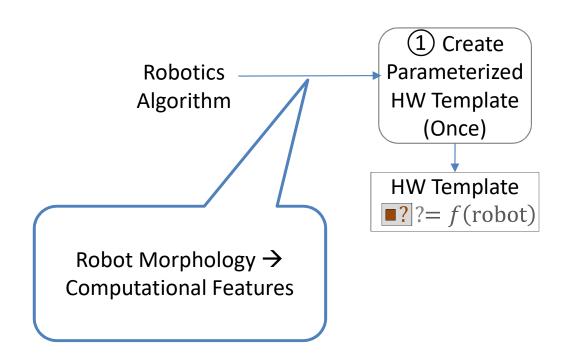
Insight: Robot Morphology



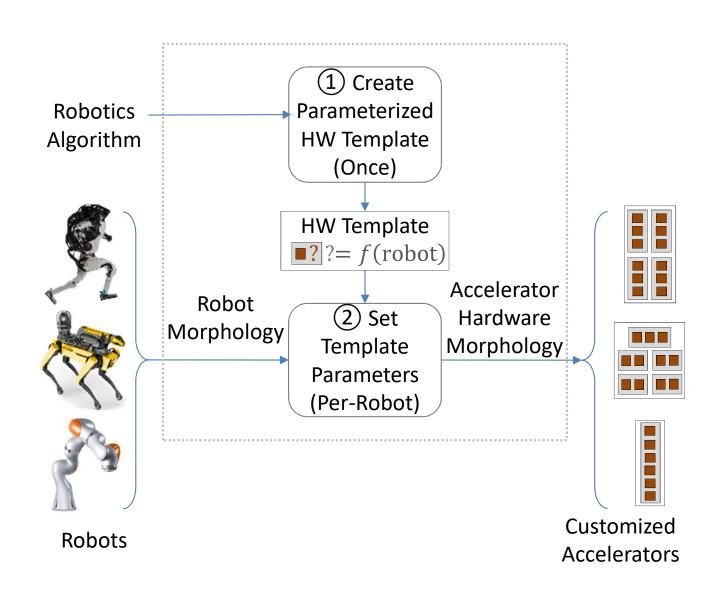






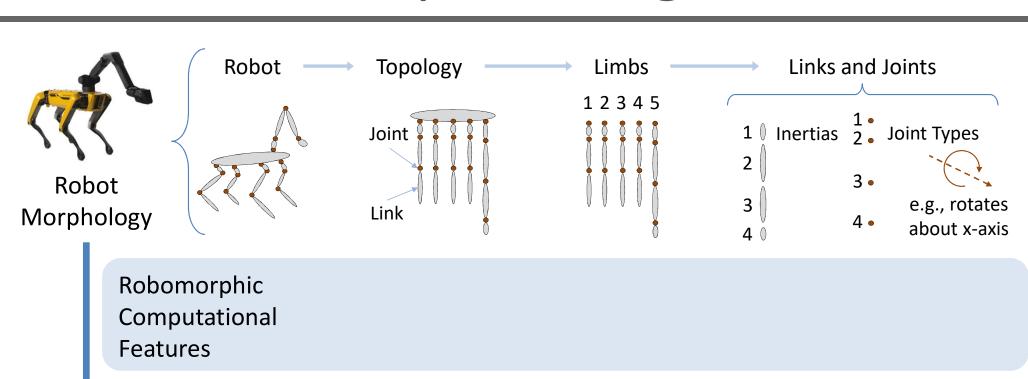








Robomorphic Design Flow

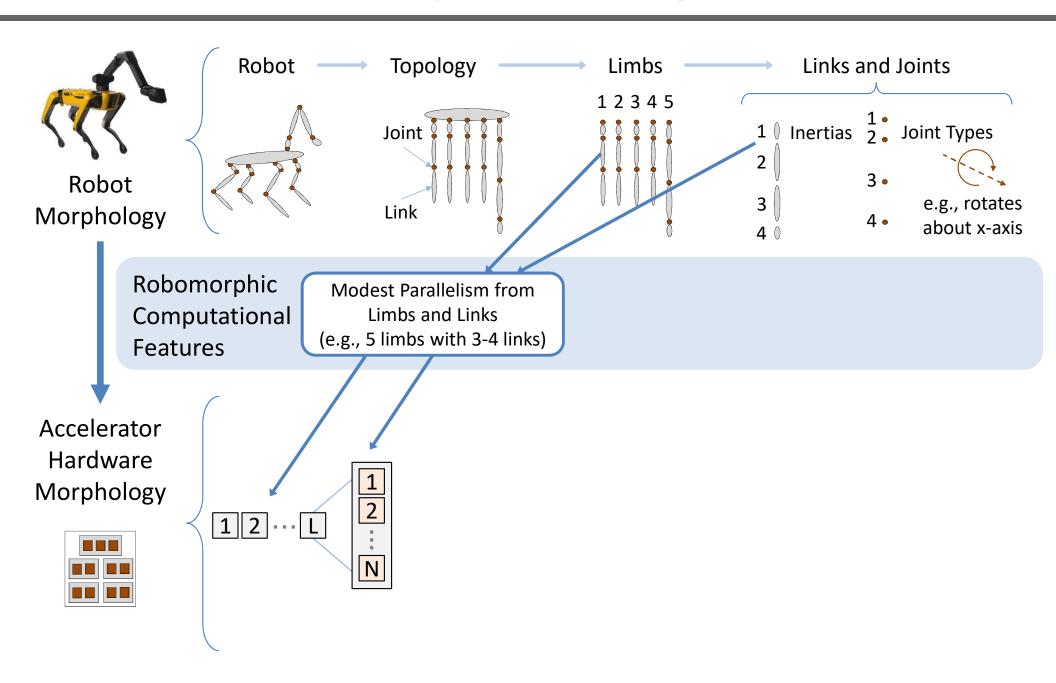


Accelerator Hardware Morphology



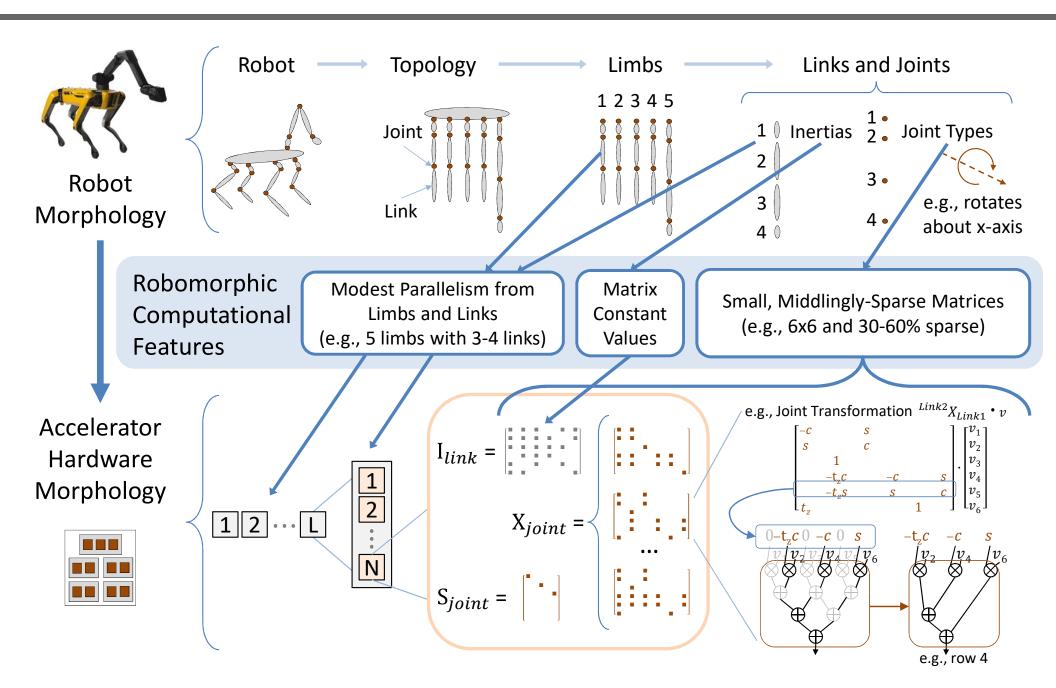


Robomorphic Design Flow



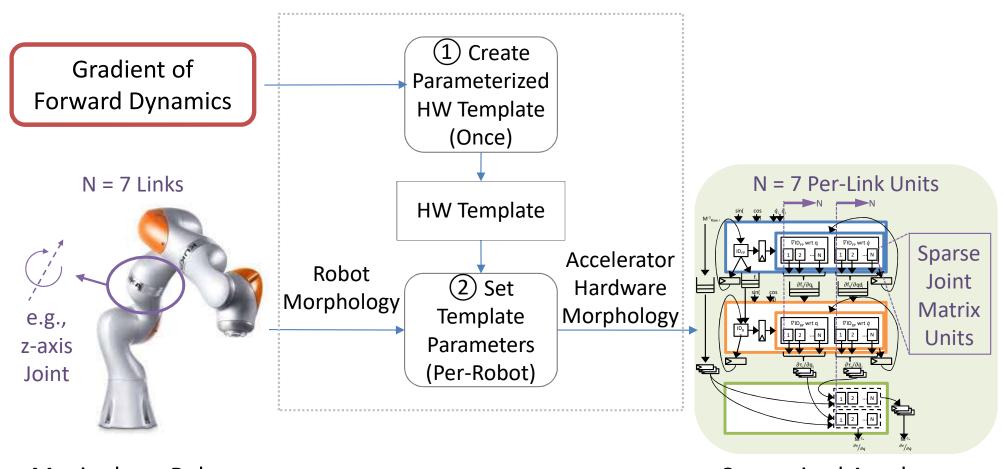


Robomorphic Design Flow





Example: Dynamics Gradient Accelerator

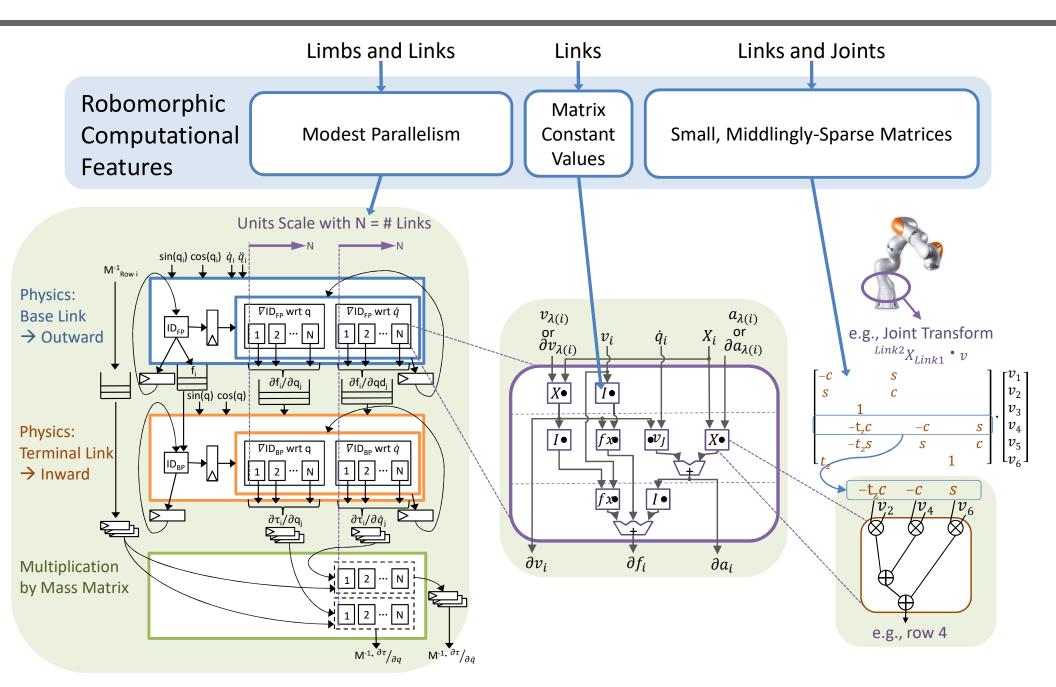


Manipulator Robot

Customized Accelerator



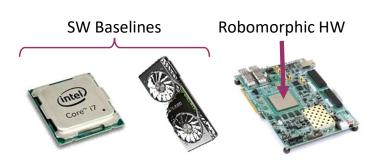
Example: Dynamics Gradient Accelerator



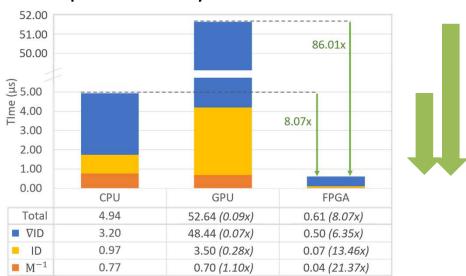


Results Highlights

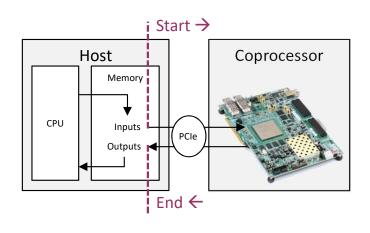
Single Computation: Compute Latency

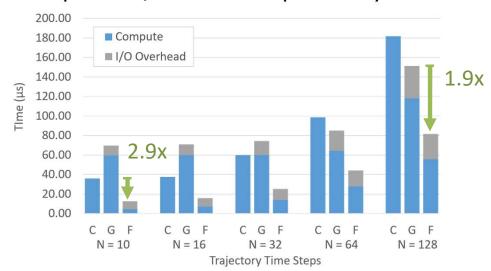


Platform	CPU	GPU	FPGA
Processor	i7-7700	RTX 2080	XCVU9P
# of Cores	4	2944 CUDA (46 SM)	N/A
Max Frequency	3.6 GHz	1.7 GHz	55.6 MHz



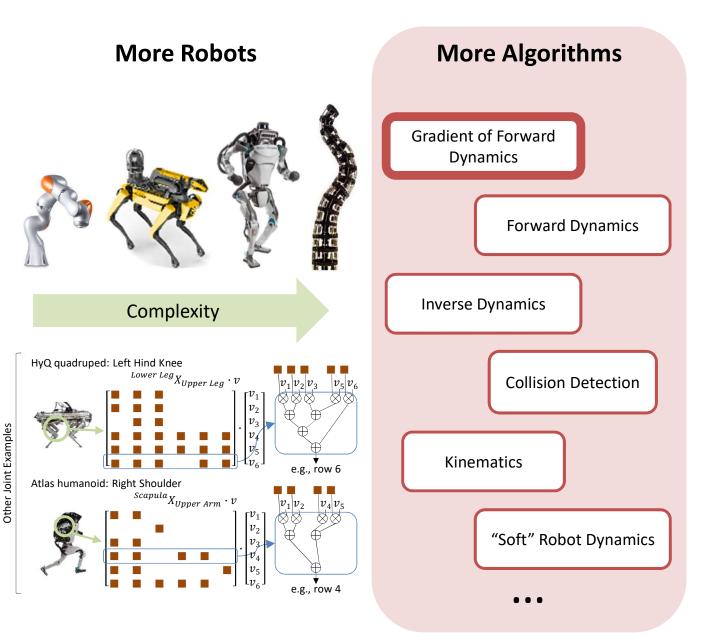
Multiple Computations: Compute + I/O Round-Trip Latency

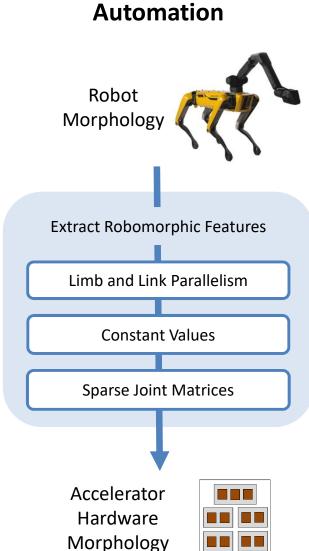






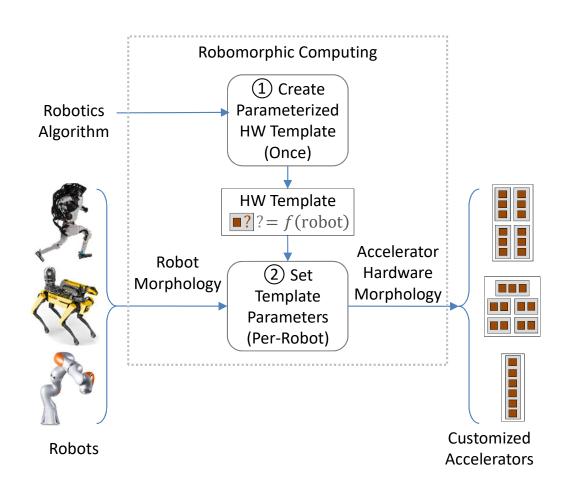
Future Work







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Contributions

- Robomorphic Computing
 - Systematic methodology
 - Robot → Customized Accelerator
- Dynamics Gradient Accelerator
 - Implemented for manipulator
 - FPGA speedups over CPU, GPU
 - ASIC speedup over FPGA

Future Work

- More robots
- More algorithms
- Automation

Goal: Automatically-Optimized Robotics Processors

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