

PRADYUMNA G NADIGER

Vidyamanya Nagar, Bengaluru 560 091

📞 +91 9480741543 📩 pradyumnagnadiger@gmail.com 💬 linkedin.com/in/pradyumna-nadiger
GitHub: github.com/pradyumnagn ⚡ [pradyumna-nadiger](https://pradyumna-nadiger.github.io/)

Experience

InQore Systems

Design engg

Mar 2025 - nov 2025

hybrid

- Currently working in the Raspberry pi pico Board designing team
- Skills Enhanced: DataSheet Design · KiCad

Nandi PowerTronics, Bengaluru

Student Intern

Jan 2024 - April 2024

Onsite

- worked as a PCB testing engineering
- hands on PCB masking, assembling, soldering, coating

Education

Dayanand Sagar College of Engineering, Bengaluru

2024 - 2027

Autonomous, Affiliated to VTU

Aggregate: 7.2 CGPA

Bachelor of Engineering in Electrical and Electronics Engineering

MEI Polytechnic, Bengaluru

2021 - 2023

Aided

Aggregate: 80.4 percent

Diploma in Electrical and Electronics Engineering

Projects

Smart Security System using IoT | Arduino uno

Nov 2025 - Ongoing

- * Smart security using Fingerprint, RFID and passkeys to unlock the door and a display for monitor.
- * about to add the camera module and microphone to analyse the emergency and to detect the irregularities.
- * Implemented in house practically.

GitHub: github.com

Collection of RTL Codes | Verilog · RTL Design

Sep 2025

- * This hobby project includes a collection of RTL codes, featuring components such as flip-flops, adders, latches, multiplexers, encoders, ALUs, counters, RAMs, ROMs, and more.

GitHub: github.com

Monitoring and Controlling the renewable energy using IoT | esp32

March 2025

- * Monitoring the voltage, temp and current of the solar panel using sensors.
- * Controlling the number of panels using relays.
- * Automated the system in dashboard and blynk application.

GitHub: github.com

Flag controlling using Application | esp32

Dec 2024

- * Created own application using mit app inventor for automatic flag hoisting.
- * to lower the flags when there is sudden rain and lowers at dark.

GitHub: github.com

Development of Plasma Source for Seed Germination | Flyback T/F

Dec 2024

- * It is the 4th State of Matter.
- * Done for germinating the seeds which kills the bad bacteria and helps growing faster.

GitHub: github.com

Technical Skills

Languages: Verilog · SystemVerilog · Python · HTML

Hardware Design & Synthesis: Xilinx Vivado/ISE

Software Development: EDA Playground · Simulink · AMD · SciLab · AutoCad · KiCad

Development Boards: ESP32 and cam module · Arduino UNO

Technologies/Frameworks: LT spice · Matlab · Arduino IDE

Major Courses

Power System Protection and Switchgear, IIT Roorkee

Introduction to Robotics Process Automation, Infosys SB

AutoCad Electric, Udemy

Workshops/Conference

VLSI Conference 2026, VLSI Society of India

Electrical Switchgear and their Applications, DB Son

RTL Design and Verification using Verilog HDL, VLSI First

Extra Curricular

Veena · Flute

Swimming · Chess

Shilambum · Karate