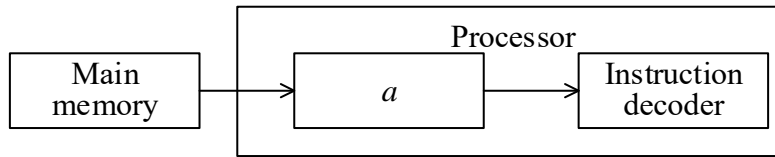**Q12.** The figure shows the storage sequence of instructions fetched by a processor. Which of the following corresponds to a?

a) Accumulator

b) Data cache

c) Instruction register

d) Program register (program counter)