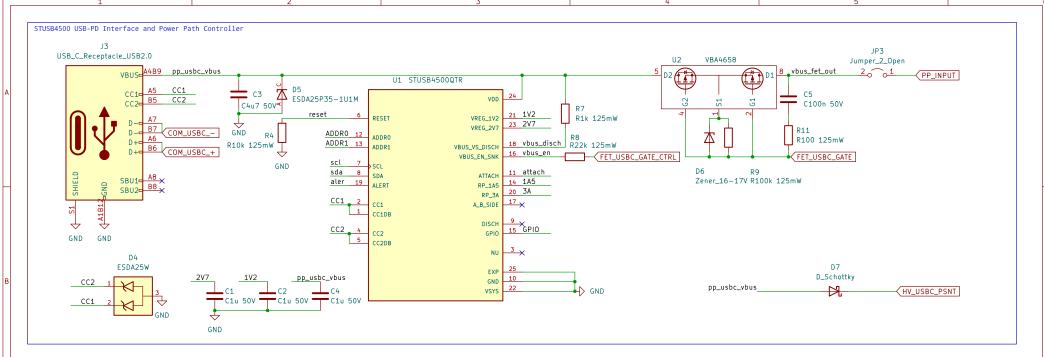


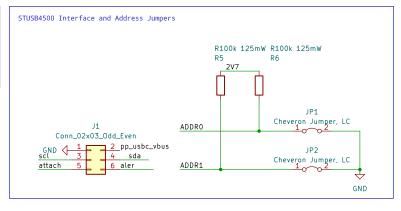
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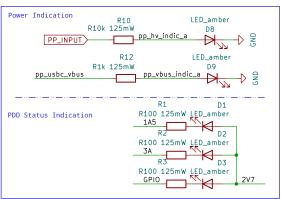
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Sheet: / File: firefly-4.	.kicad_sch			1 '	paisleymicro.com U PAISLEY—FC—4	PAISLEY	PAISLEY	
Title: Root	Layout				y-4	i 心》	/IICROSYSTEMS	
Designed by Matthew Guo <matthew@paisleymicro.com></matthew@paisleymicro.com>				Firefly Aut	omation Controller for RPI CM4			
Approved by M	latthew Guo <matthew@paisleymicro.co< th=""><th>m&gt;</th><th>Rev: 2.0</th><th colspan="3" rowspan="2">© 2025 PAISLEY MICROSYSTEMS INC.</th><th></th></matthew@paisleymicro.co<>	m>	Rev: 2.0	© 2025 PAISLEY MICROSYSTEMS INC.				
Size: A4	Date: 2025-02-17	KiCad E.D.A. 8.0.8	ld: 1/15					
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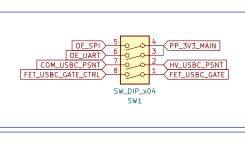




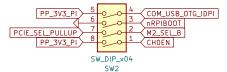
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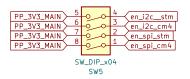
Sheet: /Power File: powerInte	Interfaces/ rfaces.kicad_sch		Order at paisleymicro.com  MFR SKU PAISLEY-FC-4		PAISLEY			
Title: Powe	wer Interfaces				Firefly-4		MICROSYSTEMS	
Designed by Ma	Matthew Guo <matthew@paisleymicro.com></matthew@paisleymicro.com>			Firefly Automation C				
Approved by Ma	atthew Guo <matthew@paisleymicro< th=""><th>.com&gt;</th><th>Rev: 2.0</th><th></th><th colspan="4"></th></matthew@paisleymicro<>	.com>	Rev: 2.0					
Size: A4	Date: 2025-02-17	KiCad E.D.A. 8.0.8	ld: 2/15	© 2025 PAISLEY MICROSYSTEMS I		INC.		
	/ <sub>1</sub>		5					



Enable buffering the SPI channel between SoM and MCU
Enable buffering the UART channel between SoM and MCU
Let USBC\_VBUS trigger the USBC\_PSNT (To direct USB2 to USBC)
Enable USBC-PD power to supply PP\_MAIN



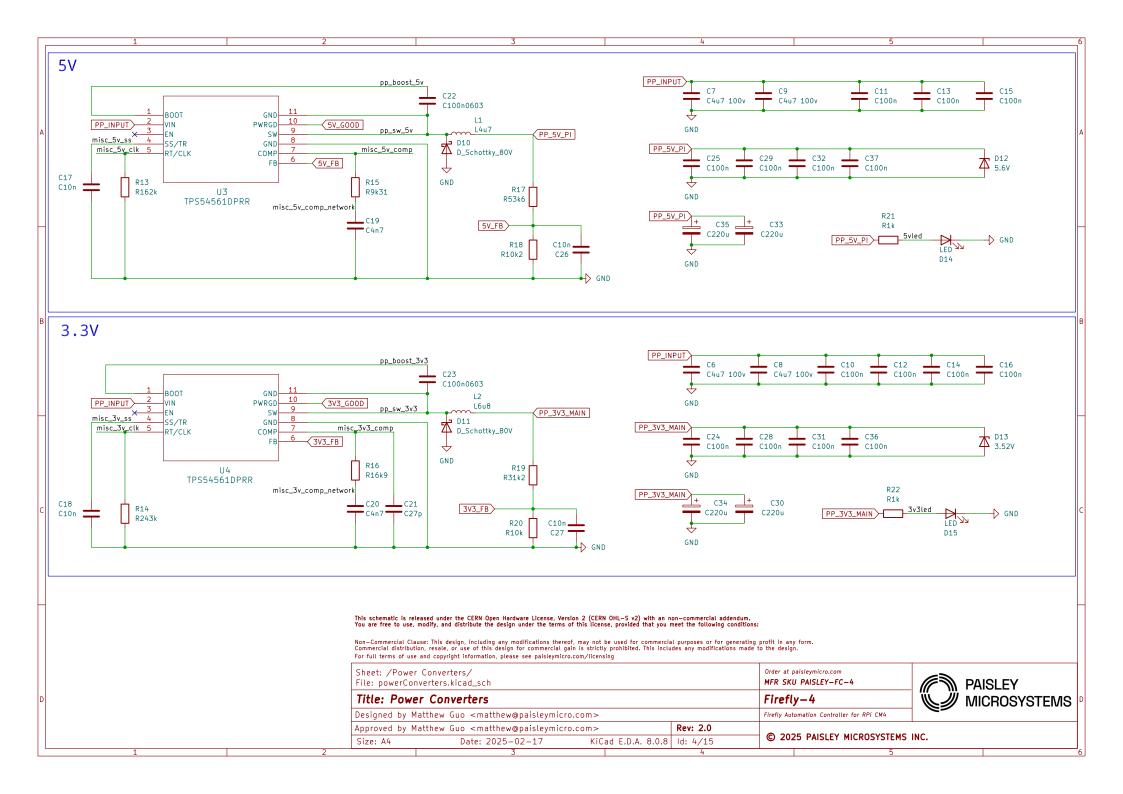
Force USB2 Mode Boot Flash select Select routing PCIe to B-key CHO Interfaces I2C enable

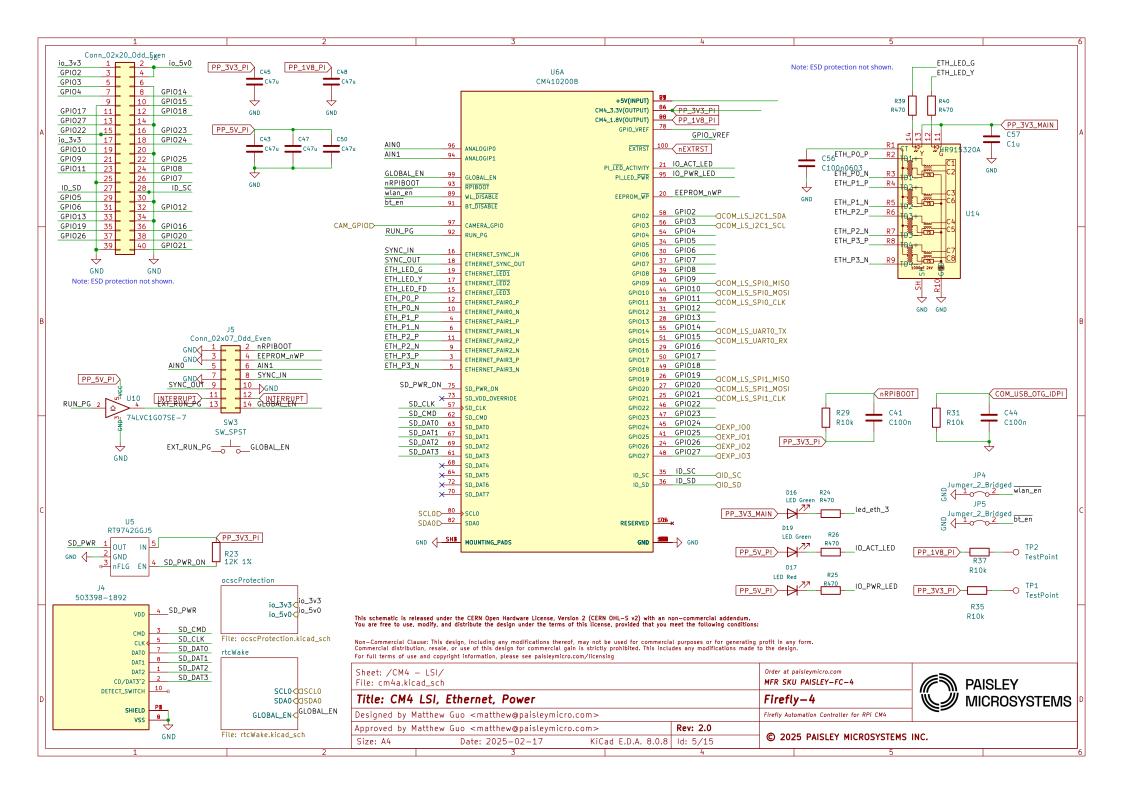


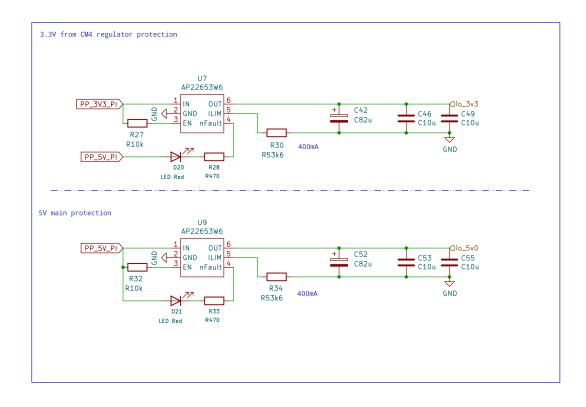
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		cal Configuration Switches/ ation.kicad_sch				aisleymicro.com I PAISLEY—FC—4	PAISLEY		
	Title:				Firefly-4		MICROSYSTEM		
	Designed by M	1atthew Guo <matthew@paisleymicro.∙< th=""><th>com&gt;</th><th colspan="2">Firefly Automation Controller for RPi CM4</th><th colspan="3"></th></matthew@paisleymicro.∙<>	com>	Firefly Automation Controller for RPi CM4					
	Approved by M	1atthew Guo <matthew@paisleymicro.∙< th=""><th>com&gt;</th><th>Rev: 2.0</th><th colspan="5">© 2025 PAISLEY MICROSYSTEMS INC.</th></matthew@paisleymicro.∙<>	com>	Rev: 2.0	© 2025 PAISLEY MICROSYSTEMS INC.				
	Size: A4	Date: 2025-02-17	KiCad E.D.A. 8.0.8	ld: 3/15	C 202				
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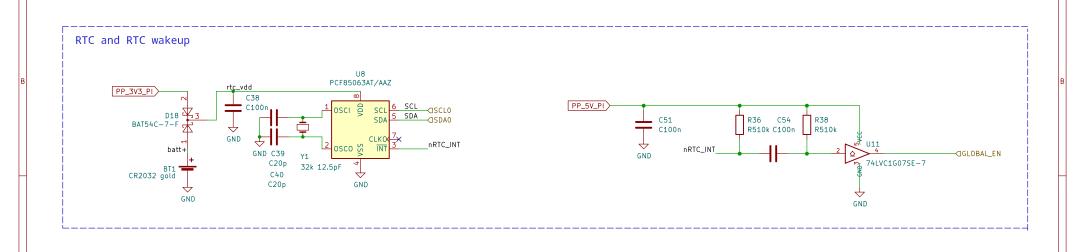




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	<ul><li>LSI/ocscProtection/ tection.kicad_sch</li></ul>		Order at paisleymicro.com MFR SKU PAISLEY-FC-4		€ P	PAISLEY		
Title: Roo	t Layout		Firefly-4		y-4		MICROSYSTEMS	
Designed by I	Matthew Guo <matthew@paisleymicro.< th=""><th>com&gt;</th><th></th><th>Firefly Aut</th><th>omation Controller for RPi CM4</th><th><b>U</b></th><th></th></matthew@paisleymicro.<>	com>		Firefly Aut	omation Controller for RPi CM4	<b>U</b>		
Approved by I	Matthew Guo <matthew@paisleymicro.< th=""><th>com&gt;</th><th>Rev: 2.0</th><th colspan="4">0</th></matthew@paisleymicro.<>	com>	Rev: 2.0	0				
Size: A4	Date: 2025-02-17	KiCad E.D.A. 8.0.8	ld: 6/15	© 2025 PAISLEY MICROSYSTEMS INC.				
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Designed by Matthew Guo <matthew@paisleymicro.com>

Approved by Matthew Guo <matthew@paisleymicro.com>

Date: 2025-02-17

Sheet: /CM4 — LSI/rtcWake/ File: rtcWake.kicad\_sch

Size: A4

Title: Reference RTC Block

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