| | | • | | | 164 | achs for (and | elug inpu | t x | 2) | 1CSP | HEADER |
|----|--------|------------|----------|------|-------------------|---------------|-----------|------|--------|--------|----------|
| | | | | • | | | | | | 1 | MCLR# |
| | | | 1 | _ | | | | | | , 2 | VDD 5V |
| | K | ESET# | ₹10 k | 2 | | 4 7 5/0 | | | | | GND |
| | | MCLR# | 470 | 1 | RE3 | 46071-I/P | RB7 | 40 | | 4 10 | SP DAT |
| | A | Va) C11NO- | ~ | | RAØ | × | R 86 | 39 | | 510 | SPCLK |
| | | | | | RA1 | X | RB5 | 38 | 100 | 6 | 10 |
| | | DACXOUT1 | | | RA2 | | R84 | 37 | 100 | | |
| | . 11, | C11N1+ | | | RA3 | × | RB3 | 36 | 100 | | |
| | . 1. | | | - 1 | RAA | X | RB2 | 35 | 100 | | |
| | | | | | RA5 | V | R81 | 34 | 4.4.4. | 1 62 | IN3- (A) |
| | | LED, PWR | 470 | | REØ | Υ | RBØ | | | C211 | - |
| | | LED. ARMa | 470 | | RE1 | ^ | | 32 | | | DD |
| | | LEDARML | 47.0 | - 14 | | | ► VDD | 31 = | 100n | | SSGND |
| | = | VDD | | | RE2 | // | • V55 | 30 | 100 | 4 | - 1 |
| | | GND, VSS | 100 | 10 | V _{DU} | <i>X</i> | RD7 | 29 | 100 | | |
| | | A145, 175 | | - 1 | V ₅₅ • | Х | RD6 | | 100 | | |
| | | | | - 4 | RA7 | X | RD5 | 28 | 100 | | |
| | | T\/A | | - 1 | RA6 | X | RDA | 27 | | RX | 0 10 |
| | | TX1 | | | RCØ | × | RC7 | 24 | | | |
| | ital | RTS1 | 100 | - 1 | RC1 | Х | RC6 | 25 | 106 | CTS | >7 |
| 00 | tpct o | | | - 1 | RC2 | X | RC5 | 24 | -WV | | |
| | , | | 100 | 18 | Rc3 | Х | RC4 | 23 | 100 | | |
| ĺ | | | 100 | 19 | RDØ | × | RD3 | 22 | 100 | | • |
| 1 | | | 100 | 20 | RD1 | х | RD2 | 21 | 100 | | 2 |
| | 3.4 | | | | | | | | | 7. | TL-232- |
| | | | | | | | | | | | GND |
| | | | }1k | | | | 242 | | | 45-0 | . HOST_ |
| | | | | | | | | | | 1NAcol | +51 |
| | | | | | | | | | | 1k § | . HOST_T |
| | | W | 1k | | | | | | | | 1+057- |
| | | | | | | | | | | ₹1k | HOST-RT |

Pin allocations for PIC18F46Q71-I/P x2-timer-ng-build-3 PJ 2024-07-01

| PIC18F46Q71-I/P | | | | | | | | | | | | |
|-----------------|----|-----|------------|-----------|-------------|--|--|--|--|--|--|--|
| MCLR# | 1 | RE3 | v RB7 | ⊦ I 40 | ICSPDAT | | | | | | | |
| C1INO-(ANa) | 2 | RA0 | RB6 | 39 | ICSPCLK | | | | | | | |
| (NC) | 3 | RA1 | RB5 | 38 | 0UT7b | | | | | | | |
| (NC) | 4 | RA2 | RB4 | 37 | 0UT7a | | | | | | | |
| (NC) | 5 | RA3 | RB3 | 36 | 0UT6b | | | | | | | |
| (NC) | 6 | RA4 | RB2 | 35 | 0UT6a | | | | | | | |
| (NC) | 7 | RA5 | RB1 | 34 | C2IN3-(ANb) | | | | | | | |
| LED_PWR | 8 | RE0 | RB0 | 33 | (NC) | | | | | | | |
| LED_ARMa | 9 | RE1 | VDD | 32 | +5V | | | | | | | |
| LED_ARMb | 10 | RE2 | RB6 | 31 | GND | | | | | | | |
| +5V | 11 | VDD | RD7 | 30 | 0UT5b | | | | | | | |
| GND | 12 | VSS | RD6 | 29 | 0UT5a | | | | | | | |
| (NC) | 13 | RA7 | RD5 | 28 | 0UT4b | | | | | | | |
| (NC) | 14 | RA6 | RD4 | 27 | 0UT4a | | | | | | | |
| TX1 | 15 | RC0 | RC7 | 26 | RX1 | | | | | | | |
| RTS1 | 16 | RC1 | RC6 | 25 | CTS1 | | | | | | | |
| 0UT0a | 17 | RC2 | RC5 | 24 | 0UT3b | | | | | | | |
| 0UT0b | 18 | RC3 | RC4 | 23 | 0UT3a | | | | | | | |
| 0UT1a | 19 | RD0 | RD3 | 22 | 0UT2b | | | | | | | |
| 0UT1b | 20 | RD1 | RD2 | 21 | 0UT2a | | | | | | | |
| ++ | | | | | | | | | | | | |

Notes:

x2 timer ng build 3

- analog input arrangement; want two of these above the PIC18 on the strip board (from page 23 28 Dec 2018 notebook)



