EMBEDDED SYSTEMS DIGITAL ASSIGNMENT 2

COURSE CODE: BCSE305L

SLOT: A1 + TA1

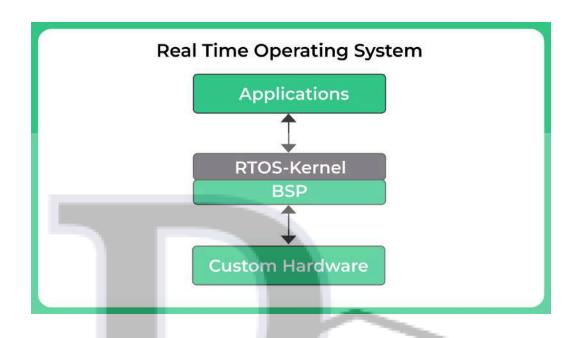
Design the embedded systems for two hard real time systems and 2 soft real time systems and justify your aspects.

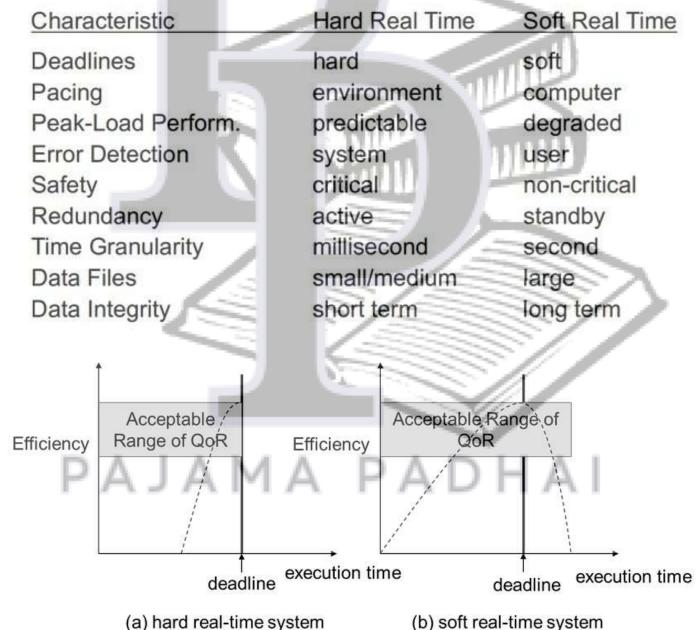
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7	Design the embedded systems for two
žs.	hand real time systems and two soft
	real time systems. Justify your aspects.
-	
- 1	Mard Real time Systems
	1 Aiscraft Flight Control System
-	
4	-> Real-time appointing system (RTOS):
-16	Chaose a highly deterministic RTOS with
	minimal interrupt latency, such as Vocallares
	or FreeRTOS. This ensures that critical glight
12	control tasks are executed within trict
58	timing constoraints.
204	-> Handware Redundancy: Employ redundant
- Light	sensors and control actuators along with a
	fault - tolerant architecture to consure system
25/15	aeliability and fault recovery in case of
	hardware failures.
	PAJAMA PADHAI
S	-> Scheduling: Utiliza static paisaity - based
	schoduling algorithms to guarantee that
	caitical tasks such as altitude and heading
	contral are given highest priority, ensuring
	timely execution and response to
	external stimuli.

-> Validation and verification: Extensive testing and simulation are coucial to verify the correctness and safety of the system. This includes both functional testing and analysis of warst-case execution times to ensure compliance with and time arguinaments. @ Medical Life Suppost System - Fault Tolerance: Implement redundancy in etheraques executions has executored attack to mitigate sisks associated with failures, vi neve voitoreaglo aucunituos grincuane the presence of faults. -> sagety Cartification: Comply with derebuste Lie ensitelingere esset lasiber such as ISO 18485 and IEC GOGOI to ensure the safety and officacy of the life support system. -> Mountaing and Alaxming: Incorparate angie latin truitag for princatinam emit lasse and extern parameters, with immediate alarming and intervention capabilities in case of abnormal conditions.

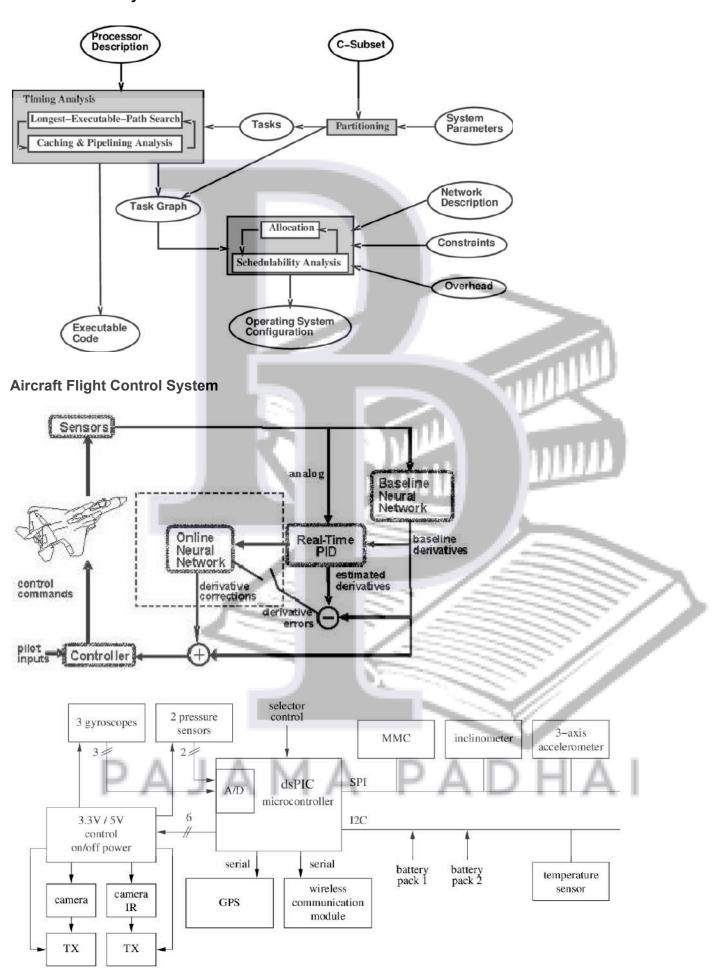
-> Battery Backup: Include battery backup on eruane at (290) sailaplue racuag alditquerestrinu continuous apposition during pouser outages, critical for life support systems where downtime can be life - threatning. Soft Real-Time Systems 1 Online Video Storeaming Source -> Quality of Sourice (QOS) Management: Employ adaptive bitrate staraning algorithms to dynamically adjust video quality based on primary stoom a quience conditions securing experience for users. -> Buffering: Implement buffering mechanisms to compensate for network fluctuations and latency, minimizing interruptions and buffering delays during playback -> Content Delivery Natural (CDN) Integration: Utilize CON sorvices to distribute content closer to end-users, acquiring latency enanceted friends extracte Moreve grivarqui bus -> User Analytics: Collect and analyze user of emit-less in stab transaggue personalize content recommendations and esneways grimasete Marero art escong mi

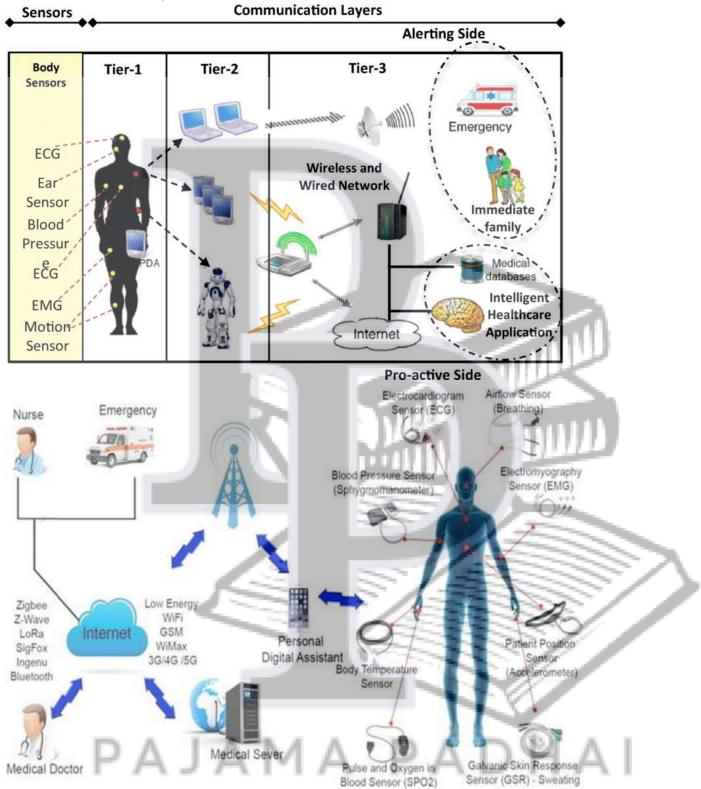
2) Automated Home Security System source avaisor storegater: nations sources such as motion detectors, door vindow sources, and cameras to provide comparehensive converage and accurate detection of security threats. - Mabile Alexts: Implement real-time puch notifications to homeowness mobile devices in case of security breaches or anomalies detected by the system. -> Remote Mantoring and Control: Enable were to remotely manitor and control the security system via mobile appear or use interfaces, providing flexibility and convenience -> Integration with Emergency Sources: Integrate with emergency sorvices such aldone at strantaged only so exilor automated dispatch in case of emergencies Mareno privaragni bris conit ecnoquere prisuber econorate offeriouses. These designs prioritize aspects such as daterminism, fault talexance, safety and user experience based on the specific near to etniantanas and constraints of each system, whather they are hard or soft real-time.





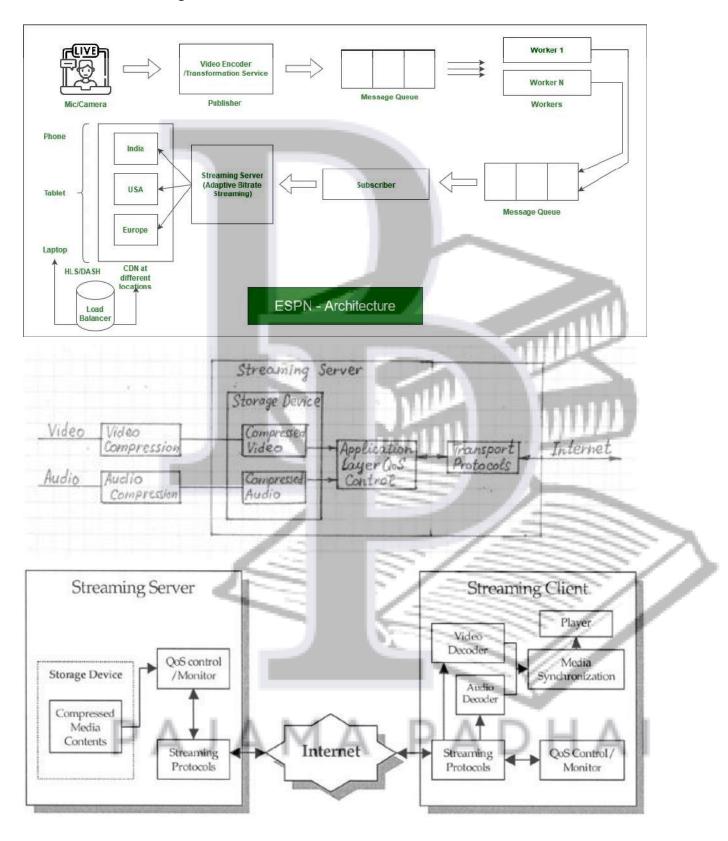
Hard Real-Time Systems:



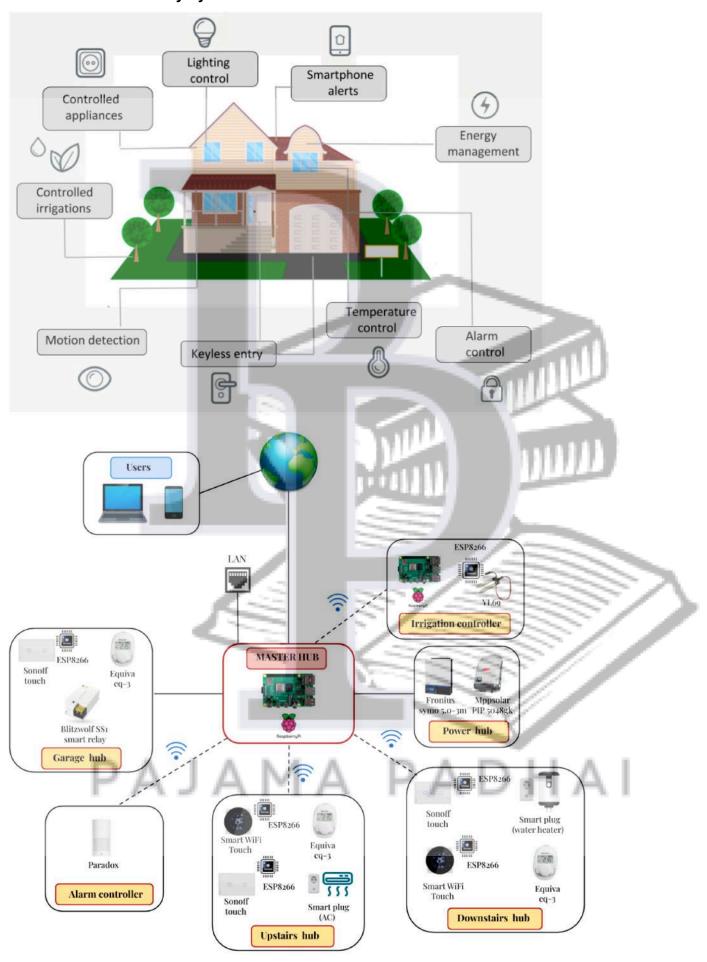


Soft Real-Time Systems:

Online Video Streaming Service



Automated Home Security System



Design an embedded system that requires functionality correctness and timing correctness and write down the solution for their real time systems.

2)	Dosign on Enbodded System that requires
1 Selfern	functionality consectness and timing convectness
Mas	and usuite down the solution for their
	real time systems.
Sup	Embedded System: Real-Time Teaffic Light Controller
- NO	the state of the s
	Functionality Correctness:
	-> E was that the tousing light contexplien
	-> Ensure that the traffic light controller
	interactions according to proffedelined, rules
	and regulations.
	and regulations.
-1510	-> Accurately detects rehicles and pedestrians to
Laves	determine the timing and sequence of traffic
10	light changes.
7-15-2	March and the second se
	-> Implaments fault detection and recovery
	mechanisms to handle sensor failures or
low 1	system malfunctions without compromising
- Charles	safety or functionality.
	Timing Convectness : A DHA
	Turking Coststatives .
- T-	-> Guarantees that the timing of traffic
	light changes is precise and adheres to
THE PARTY	specified intervals to optimize traffic
	glaw and minimize congestion.
	7

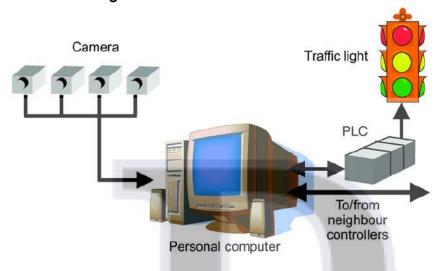
-> Responds to real-time events such as vehicle detection and pedestrian crossings Atomis everes at yendel larvivin Aticu and efficient traffic maragement: Treasparatos deterministic scheduling algorithme to prioritize critical tasks and maintain timing convectuess even under varying load conditions. Solution for Real-Time Systems 1. Hardware Soloction 1. -> Choose a missocontabler or FPGA with laradgina bus recused pricessored theisiffue reading entit last albust of respective ethic signate role alongie lantinos des traffic lights Sensua that the selected hardware platform supports deterministic appointment and parouides mechanisms for precise timing control 2. Roal-Time Operating System (RTOS) → select a lightweight RTOS with deterministic as scheduling capabilities such as FORERTOS ON RTEMS.

-> Otiliza features like priority-based betaler exect lastice exuans of guillubards to traffic light control are executed in a timely manner. 3. Souson Integration -> Integrato sensors such as rehicle detectors (inductive loops, ingrared sensors) and pedestrian detectors (infrared sensors, pressure pads) to accurately detect taaffic conditions at intersections. for guilbroad nevirob - topurcostric translagra sensor inputs to suspond pramptly to changes in traffic flow. 4. Contact Algorithm metiragle laxinas tandore a galeral bus grimit landing after converted timing and sequence of traffic light charges based on real - time inputs from sources. -> Employ a givita state machine (FSM) art lebans at auspiredat ralinic so behaviour of the traffic light controller noitoresque for contractor of operation.

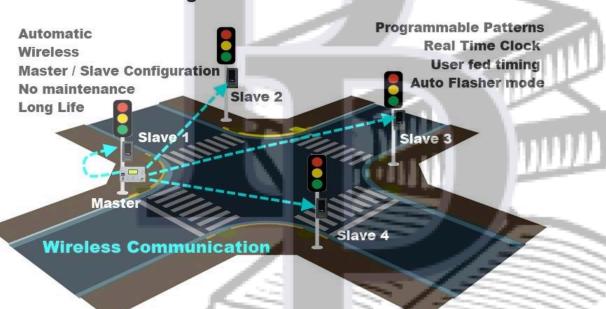
	5. Fault Detection and Recovery
9	-> Implement built -in self-test (BIST)
	mechanisms to posicially check the
	integrity of hardware components and sensor
	inputs.
	-> Incorporate redundancy and failouer
-	
	mechanisms to handle sensor failures or
	communication exerces without dissempting
	taaffic flow.
	6. Validation and Testing
	-> conduct thorough validation and testing
	of the embodded system to really
	Sunctionality consections and timing consections
	under various traffic scenarios and
- 20	envisionmental conditions.
010	
1	- in - exemples of alast maitalunic ac U <-
	the - loop (MIL) testing to assess the
	system's performance and validate
Me	real-time behaviour.
	DATABAA DADILAL
1-0-	By integrating these components and
	ensuring functionality correctness and
	timing contrectness, the oreal-time
	traffic light controller can effectively
	manage traffic flow at intersections,
	enhancing sagety and efficiency on the
	gand:

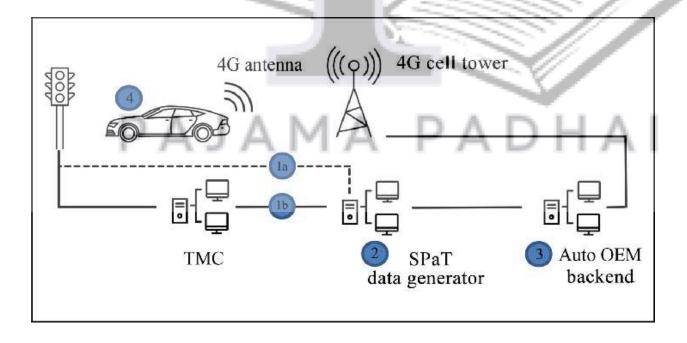
TOTAL PROPERTY.

Real-Time Traffic Light Controller



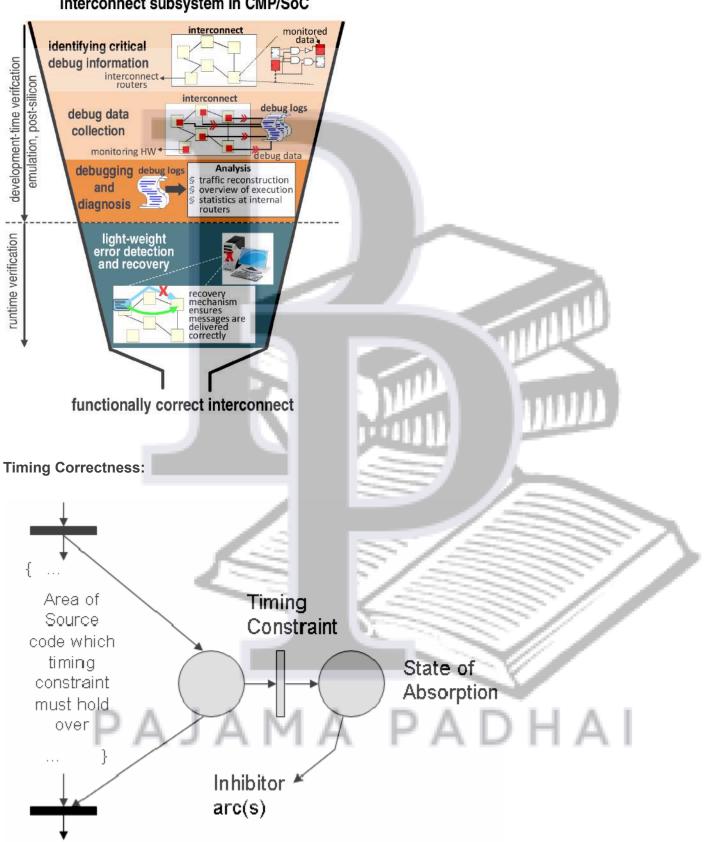
Wireless Traffic Light Controller





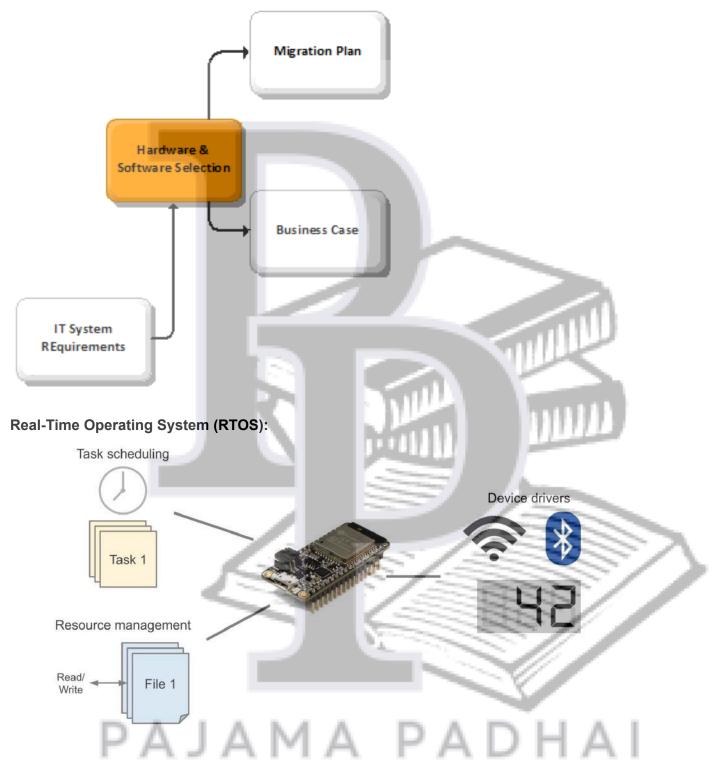
Functionality Correctness:

interconnect subsystem in CMP/SoC

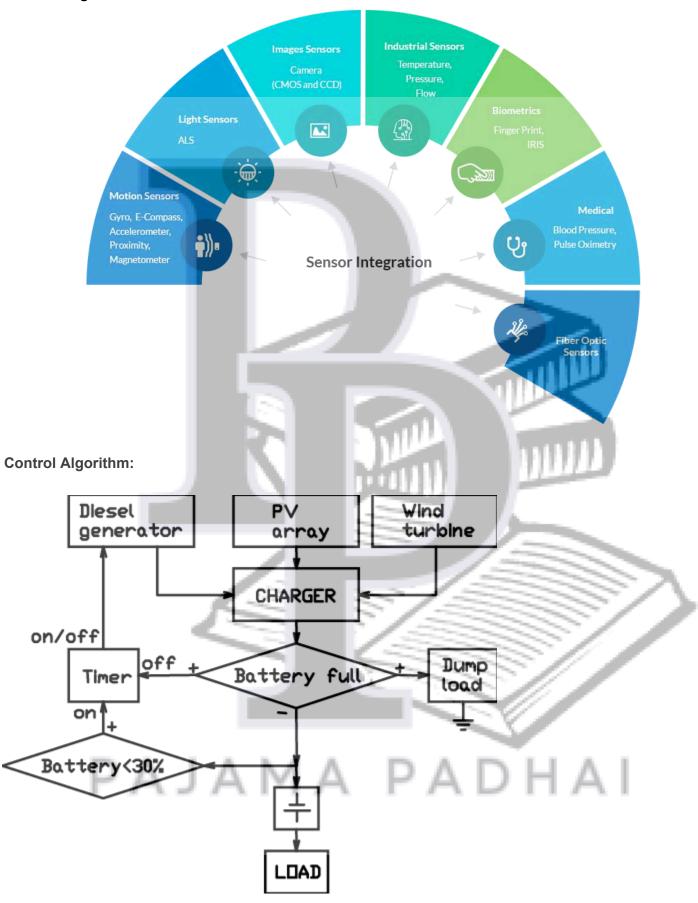


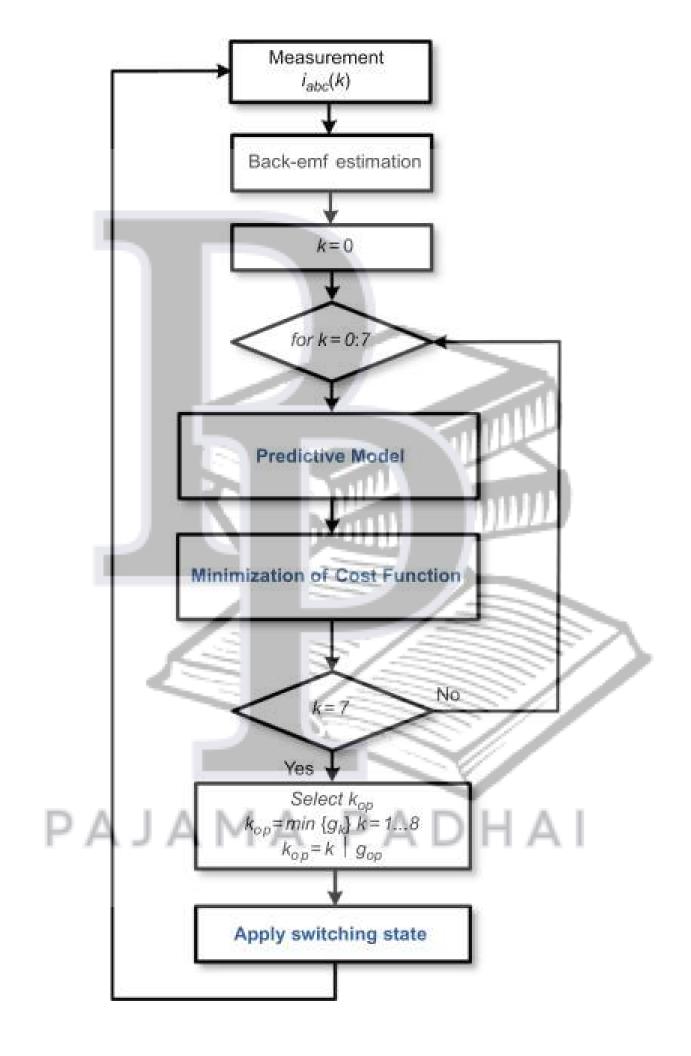
Solution for Real-Time Systems:

Hardware Selection:

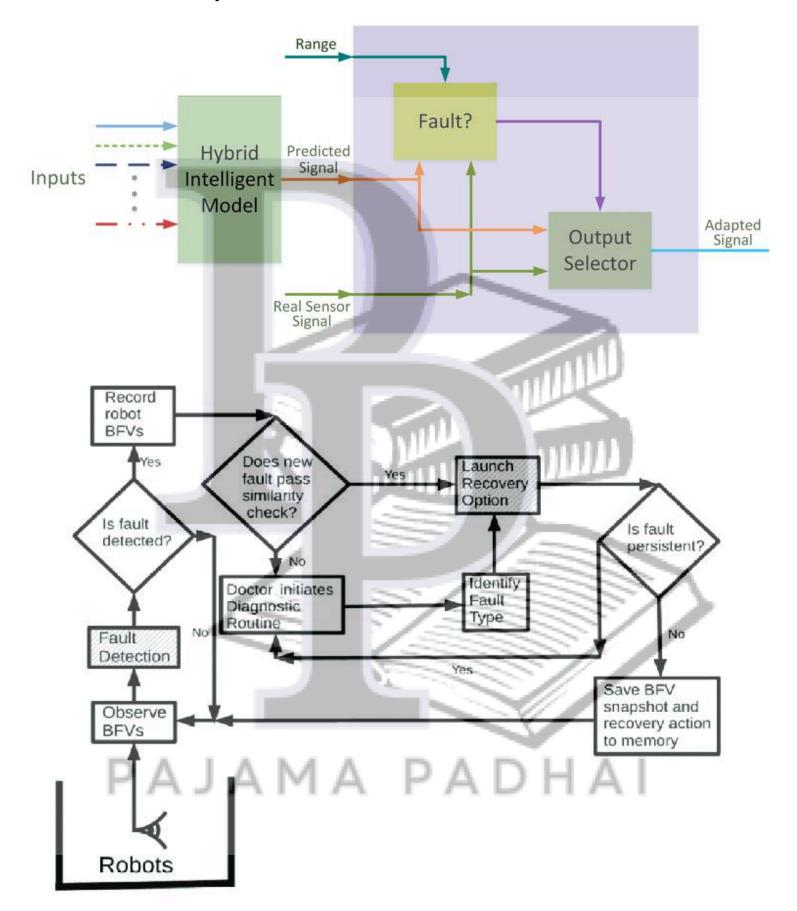


Sensor Integration:





Fault Detection and Recovery:



Validation and Testing:

