* NOT Gate

|  |  |
| --- | --- |
| A | NA |
| 1 | 0 |
| 0 | 1 |

* NAND Gate

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| A | B | NA | NB | NAND |
| 1 | 0 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 0 |

* NOR Gate

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| A | B | NA | NB | NOR |
| 0 | 0 | 1 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 |
| 1 | 0 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 | 0 |

* AND AND Gate

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| A | B | A and B | A and b AND C and D | C and D | C | D |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| 1 | 0 | 0 | 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 0 | 0 | 0 | 1 |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 |