

# RVT70AQFNWC00

# LCD TFT Datasheet

Rev.2.0 2016-10-13

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	/
Size	7.0	Inch
Viewing Direction	12:00 (without image inversion)	O' Clock
Gray Scale Inversion Direction	6:00	O' Clock
LCM (W × H × D)	164.80 ×99.80 × 11.45	mm³
Active Area (W × H)	154.08 × 85.92	mm <sup>2</sup>
Dot Pitch (W × H)	0.1926 × 0.179	mm <sup>2</sup>
Number of Dots	800 (RGB) × 480	/
Driver IC	FT813	/
Backlight Type	21 LEDs	/
Surface Luminance	350	cd/m <sup>2</sup>
Interface Type	SPI/QSPI	/
Color Depth	16.7M	/
Pixel Arrangement	RGB Vertical Stripe	/
Surface Treatment	Clear	
Input Voltage	3.3	V
With/Without TSP	Projected Capacitive Touch Panel	/
Weight	209.9	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.



# **REVISION RECORD**

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2015-05-12	Initial Release	
1.1	2015-09-21	Update total thickness, color depth and weight information	
1.2	2016-01-12	Initial Input Voltage for LED Inverter	
1.3	2016-06-06	Update Interface description (pin 4 and pin 5)	
1.4	2016-10-13	Added Inspection Standards	
2.0	2016-10-13	New PCT Controller	

# CONTENTS

RI	EVISI	ON RECORD	2
C	ONTE	ENTS	2
1	M	10DULE CLASSIFICATION INFORMATION	3
2	Ν	MODULE DRAWING	4
3	Al	BSOLUTE MAXIMUM RATINGS	5
4	El	LECTRICAL CHARACTERISTICS	5
5	El	LECTRO-OPTICAL CHARACTERISTICS	5
6	IN	NTERFACE DESCRIPTION	7
7	FT	T813 CONTROLLER SPECIFICATIONS	8
	7.1	Serial host interface	8
	7.2	Block Diagram	9
	7.3	Host interface SPI mode 0	9
	7.4	Backlight driver block diagram	9
8	LC	CD TIMING CHARACTERISTICS	10
	8.1	Clock and data input time diagram	10
	8.2	Parallel RGB input timing table	10
9	C	APACITIVE TOUCH SCREEN PANEL SPECIFICATIONS	
	9.1	Mechanical characteristics	1
	9.2	Electrical characteristics1	
10	)	INSPECTION	
	10.1	·	
	10.2		
1:	1	RELIABILITY TEST	١6
12	2	LEGAL INFORMATION	17

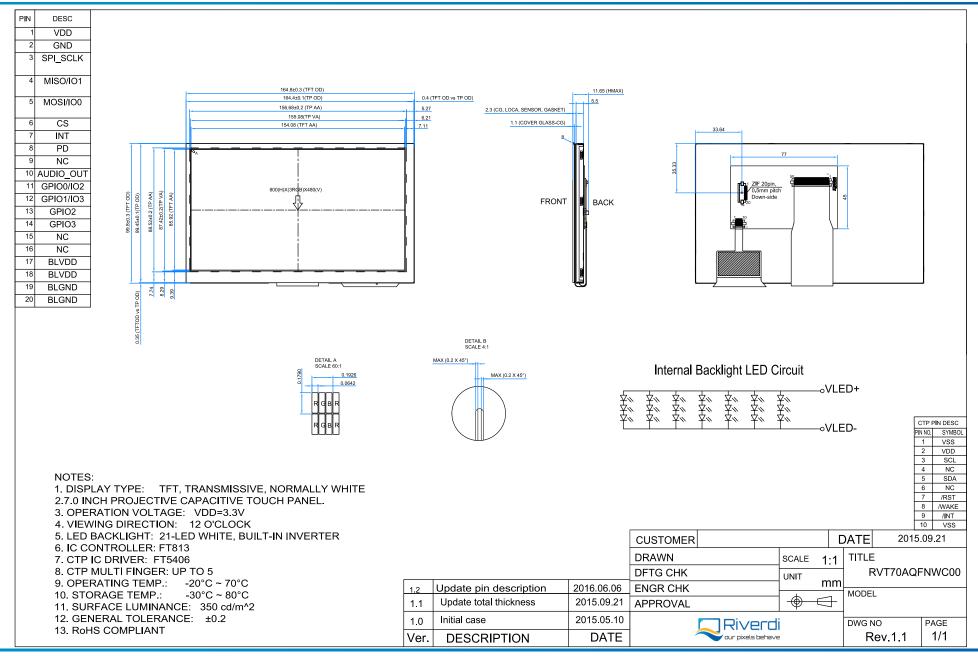


# 1 MODULE CLASSIFICATION INFORMATION

RV	Т	70	А	П	F	Ν	W		
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard F – TFT Custom
3.	DISPLAY SIZE	35 – 3.5" 43 – 4.3" 50– 5.0" 70 – 7.0"
4.	MODEL SERIAL NO.	<b>A</b> (A-Z)
5.	RESOLUTION	Q – 800x480 px
6.	INTERFACE	T – TFT LCD, RGB L – TFT LCD, LVDS S – TFT + Controller SSD1963 F – TFT + Controller FT813
7.	FRAME	N – No Frame F – Mounting Frame
8.	BACKLIGHT TYPE	<b>W</b> – LED White
9.	TOUCH PANEL	N – No Touch Panel R – Resistive Touch Panel C – Capacitive Touch Panel
10.	VERSION	<b>00</b> (00-99)







## 3 ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage For Logic	VDD	-0.3	3.6	V
Input Voltage For Logic	VIN	-0.3	VDD	V
Input Voltage For LED Inverter	BLVDD	-0.3	7.0	V
LED forward voltage (each LED)	IF	-	30	mA
Operating Temperature	T <sub>OP</sub>	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

## 4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage For Module	VDD	3.0	3.3	3.6	V
Input Voltage for LED Inverter	BLVDD	2.8	5	5.5	V
Input Current (Exclude LED Backlight)	IDD	-	TBD	-	mA
LED Backlight Current	IDD <sub>backlight</sub> (@ 5V)	-	450	540	mA
Input Voltage ' H ' level	ViH	0.7VDD	-	VDD	V
Input Voltage ' L ' level	V <sub>IL</sub>	0	-	0.2VDD	V
LED Life Time	-	30000	50000	-	Hrs

**Note:** The LED life time is defined as the module brightness decrease to 50% original brightness at  $Ta=25^{\circ}C$ 

## 5 ELECTRO-OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Tim	e	Tr+Tf	θ=0°	-	20	35	ms	FIG 1.	4
Contrast Ratio	•	Cr	Ø=0°	400	500	-		FIG 2.	1
Luminance		δ	Ta=25	70	75	-	%	FIG 2.	3
Uniformity		WHITE							
Surface Lumin	ance	Lv		-	350	-	cd/m <sup>2</sup>	FIG 2.	2
			Ø = 90°	40	50	-	deg	FIG 3.	
			Ø = 270°	60	70	-	deg	FIG 3.	
Viewing Angle	Range	θ	Ø = 0°	60	70	-	deg	FIG 3.	6
			Ø = 180°	60	70	-	deg	FIG 3.	
	Red	x		-	-	-			
		У		-	-	-			
CIE (x, y)	Green	x	θ=0°	-	-	-			
Chromaticity		У	Ø=0°	-	-	-		FIG 2.	5
Blue	Blue	x	Ta=25	-	-	-			
		У		-	-	-			
	White	x		-	0.280	-			
		У		-	0.310	-			



Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

Contrast Ratio = 
$$\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

**Note 3.** The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \, WHITE \, = \, \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5)}$$

**Note 4.** Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 1. The test equipment is Autronic-Melchers's ConoScope series.

**Note 5.** CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 3.

**Note 7.** For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

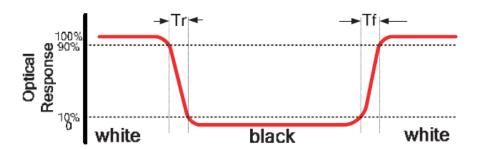


Figure 1. The definition of response time



Figure 2.Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A:5 mm B:5 mm

H,V: Active Area

Light spot size ∅=5mm, 500mm distance from the LCD surface to detector lens

measurement instrument is TOPCON's luminance meter BM-5

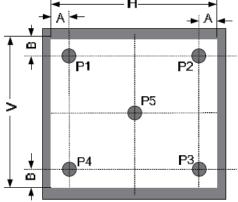
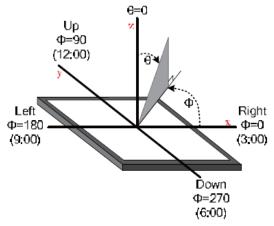


Figure 3.The definition of viewing angle



### **6** INTERFACE DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	SPI_SCLK	SPI SCK Signal, Internally 47k Pull UP
4	MISO/ IO1	SPI MISO Signal / SPI Quad mode: SPI data line 1
5	MOSI/ IO0	SPI MOSI Signal / SPI Quad mode: SPI data line 0
6	CS	SPI Chip Select Signal , Internally 47k Pull UP
7	INT	Interrupt Signal, Active Low, Internally 47k Pull UP
8	PD	Power Down Signal, Active Low, Internally 47k Pull UP
9	NC	Not Connected
10	AUDIO_OUT	Audio Out Signal
11	GPIO0/IO2	SPI Single mode: General purpose IOO/ SPI Quad mode: SPI data line 2
12	GPIO1/IO3	SPI Single mode: General purpose IO1/ SPI Quad mode: SPI data line 3
13	GPIO2	General purpose IO2
14	GPIO3	General purpose IO3 or analog input for ADC
15	NC	Not Connected
16	NC	Not Connected
17	BLVDD	Backlight Power Supply, Can Be Connected to VDD
18	BLVDD	Backlight Power Supply, Can Be Connected to VDD
19	BLGND	Backlight Ground, Internally connected to GND
20	BLGND	Backlight Ground, Internally connected to GND



#### 7 FT813 CONTROLLER SPECIFICATIONS

FT813 or EVE (Embedded Video Engine) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object oriented architecture approach that extends from display creation to the rendering of the graphics.

#### 7.1 Serial host interface

Figure 4.SPI interface connection

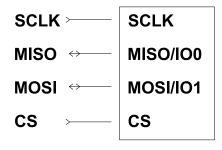


Figure 5. QSPI interface connection

SCL	< >	SPI_SCLK
IO0	$\leftrightarrow$	MISO/IO0
IO1	$\leftrightarrow$	MOSI/IO1
102	$\leftrightarrow$	IO2
<b>IO</b> 3	$\leftrightarrow$	IO3
CS	>	CS

**SPI Interface** – the SPI slave interface operates up to 30MHz.

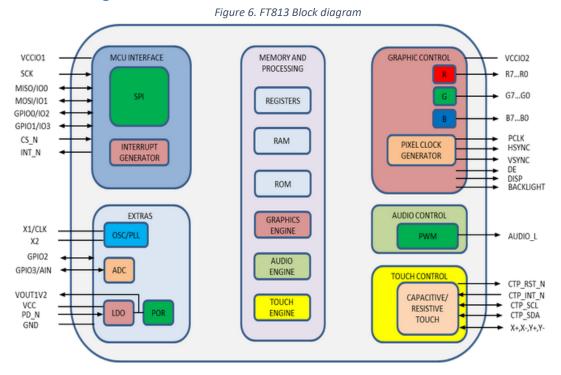
Only SPI mode 0 is supported. The SPI interface is selected by default (MODE pin is internally pulled low by 47k resistor).

**QSPI Interface** – the QSPI slave interface operates up to 30MHz. Only SPI mode 0 is supported. The QSPI can be configured as a SPI slave in SINGLE, DUAL or QUAD data bus modes.

By default the SPI slave operates in the SINGLE channel mode with MOSI as input from the master and MISO as output to the master. DUAL and QUAD channel modes can be configured through the SPI slave itself. To change the channel modes, write to register REG\_SPI\_WIDTH.



#### 7.2 Block Diagram



#### 7.3 Host interface SPI mode 0

CSN Tsac Tsclkh Tsclkl

SCLK
MOSI
MISO
Tzo
Tzo
Tod
Tod
Toz

Figure 7. SPI timing diagram

For more information about FT813 controller please go to official FT81x website <a href="http://www.ftdichip.com/Products/ICs/FT81X.html">http://www.ftdichip.com/Products/ICs/FT81X.html</a>

### 7.4 Backlight driver block diagram

Backlight enable signal is internally connected to FT813 Backlight control pin. This pin is controlled by two FT813's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to FT812 datasheet for more information.

FT813

PWM dimming signal driver

Figure 8. Backlight driver block diagram

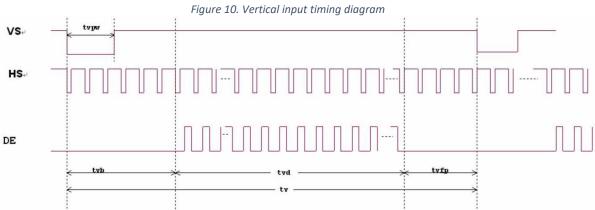


#### 8 LCD TIMING CHARACTERISTICS

#### 8.1 Clock and data input time diagram

Figure 9. Horizontal input timing diagram





## 8.2 Parallel RGB input timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz
VSD Period Time	tv	510	525	650	TH
VSD Display Area	tvd		480		TH
VSD Blanking	tvb		23		TH
VSD Front Porch	tvfp	7	22	147	TH
VSD Pulse Width	tvpw	1	-	20	TH
HSD Pulse Width	thpw	1	-	40	DCLK
HSD Period Time	th	862	1056	1200	DCLK
HSD Display Area	thd		800		DCLK
HSD Blanking	thb		46		DCLK
HSD Front Porch	thfp	16	210	354	DCLK



### 9 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

The Capacitive Touch Panel is directly connected to FT813 module. Therefore communication with Capacitive Touch Panel is simplified to read registers of FT813.

FT813 Capacitive touch panel

Figure 11.Capacitive Touch Panel Connection

#### 9.1 Mechanical characteristics

DESCRIPTION	INL SPECIFICATION	REMARK
Touch Panel Size	7 inch	
Outline Dimension (OD)	164.4mm x 99.45mm	Cover Lens Outline
Product Thickness	2.3mm	
Glass Thickness	1.1mm	
Ink View Area	155.08mm x 87.42mm	
Sensor Active Area	156.68mm x 88.52mm	
Input Method	5 Finger	
Activation Force	Touch	
Surface Hardness	≥7H	

#### 9.2 Electrical characteristics

DESCRIPTION		SPECIFICATION
Operating Voltage		DC 2.8~3.3V
Power Consumption (IDD)	Active Mode	10~18mA
	Sleep Mode	30~50μA
Interface		I <sup>2</sup> C
Linearity		<1.5%
Controller		FT5426
I2C address		0x38 (7-bit address)
Resolution		1792*1024



### 10 INSPECTION

Standard acceptance/rejection criteria for TFT module.

### 10.1 Inspection condition

Ambient conditions:

Temperature: 25±°CHumidity: (60±10) %RH

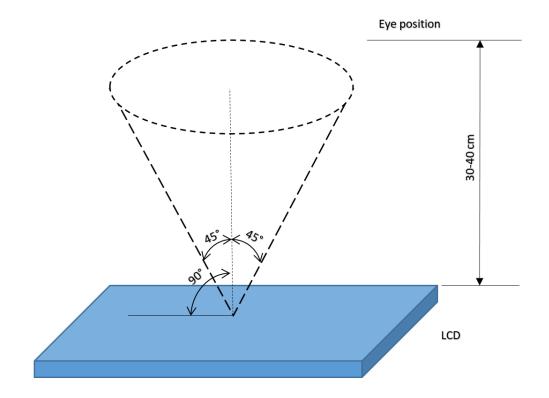
• Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

#### Viewing distance:

35±5cm between inspector bare eye and LCD.

#### Viewing Angle:

U/D: 45°/45°, L/R 45°/45°

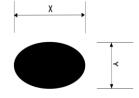




## 10.2 Inspection standard

Item
Black spots, white
spots, light leakage,
Foreign Particle
(round Type)





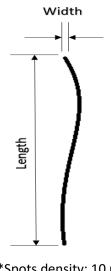
$$D = \frac{(x+y)}{2}$$

\*Spots density: 10 mm

Size < 5"			
Average Diameter	Qualified Qty		
D < 0.2 mm	Ignored		
0.2 mm < D < 0.3 mm	3		
0.3 mm < D < 0.5 mm	2		
0.5 mm < D	0		

Size >= 5"		
Average Diameter	Qualified Qty	
D<0.2 mm	Ignored	
0.2 mm < D < 0.3 mm	4	
0.3 mm < D < 0.5 mm	2	
0.5 mm < D	0	

### LCD black spots, white spots, light leakage (line Type)



*Spots	density:	10	mm
Opoto			

Size < 5"			
Length	Width	Qualified	
		Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	2	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	

Size >= 5"			
Length	Width	Qualified Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	4	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	



Item	Criterion		
Clear spots		Size < 5"	
	Average Diameter	0.120 + 0	Qualified Qty
	D < 0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		3
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
		Size >= 5"	
	Average Diameter		Qualified Qty
	D<0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		4
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
	*Spots density: 10 mm		
Polarizer bubbles			
		Size < 5"	
	Average Diameter		Qualified Qty
	D < 0.2 mm		Ignored
	0.2 mm < D < 0.5 mm		3
	0.5 mm < D < 1 mm		2
	1 mm < D		0
	Total Q'ty		3
		Size >= 5"	
	Average Diameter	3126 >= 3	Qualified Qty
	D<0.25 mm		Ignored
	0.25 mm < D < 0.5 mm		3
	0.25 mm < D		0
	0.5 11111 \ D		U
Electrical Dot			
Defect		Size < 5"	
	item		Qualified Qty
	Black do defect		4
	Bright dot defect		2
	Total Dot		5
		Size >= 5"	
	item		Qualified Qty
	Black do defect		5
	Bright dot defect		2
	Total Dot		5
	Total Dot		5



tem Criter	on	
Touch panel spot		
		e < 5"
	age Diameter	Qualified Qty
	0.2 mm	Ignored
	nm < D < 0.4 mm	5
	nm < D < 0.5 mm	2
0.5 r	nm < D	0
		2 >= 5"
	age Diameter	Qualified Qty
	25 mm	Ignored 4
	0.25 mm < D < 0.5 mm	
0.5 r	0.5 mm < D	
Fouch panel \M/hita		
Touch panel White Line Scratch	Size < 5"	
		Qualified Oty
Leng	W< 0.02	Qualified Qty
-		Ignored 2
L < 3		
L <z< td=""><td>0.05 &lt; W &lt; 0.08 0.08 &lt; W</td><td></td></z<>	0.05 < W < 0.08 0.08 < W	
-	0.08 < W	0
	Size >= 5"	
Leng		Qualified Qty
Leng	W< 0.03	Ignored
L < 5	.0	



# 11 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80±2°C/240hours
2	Low Temperature Storage	-30±2°C/240hours
3	High Temperature Operating	70±2°C/240hours
4	Low Temperature Operating	-20±2°C/240hours
5	Temperature Cycle	-30±2°C~25~80±2°C × 20 cycles (30min.) (5min.) (30min.)
6	Damp Proof Test	60°C ±5°C × 90%RH/240hours
7	Vibration Test	Frequency 10Hz~55Hz Amplitude of vibration: 1.5mm Sweep: 10Hz~55Hz~10Hz X, Y, Z 2 hours for each direction.
8	Package Vibration Test	Random vibration :0.15G*G/HZ from 5-200HZ, -6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)
9	Package Drop Test	Height:60 cm 1 corner,3 edges,6 surfaces
10	ESD Test	$\pm$ 2KV, Human body mode,100pF/1500 $\Omega$
11	Mechanical Shock	100G 6ms, X, Y, Z 3 times for each direction



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