## LIS3MDL 3-Axis Magnetometer Carrier with Voltage Regulator



This module is a carrier/breakout board for the ST LIS3MDL three-axis magnetometer. The sensor provides magnetic field strength measurements with a configurable range of ±4 gauss to ±16 gauss that can be read through a digital I²C or SPI interface. The board includes a 3.3 V voltage regulator and integrated level shifters that allow operation from 2.5 to 5.5 V, and the 0.1" pin spacing makes it easy to use with standard solderless breadboards and 0.1" perfboards.

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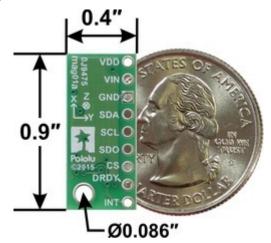


### Overview

This board is a compact (0.4" × 0.9") breakout board for ST's LIS3MDL three-axis digital magnetic sensor; we therefore recommend careful reading of the **LIS3MDL datasheet** (2MB pdf) before using this product. The LIS3MDL is a great IC, but its small package makes it difficult for the typical student or hobbyist to use. It also operates at voltages below 3.6 V, which can make interfacing difficult

for microcontrollers operating at 5 V. This carrier board addresses these issues by incorporating additional electronics, including a 3.3 V voltage regulator and level-shifting circuits, while keeping the overall size as compact as possible. The board ships fully populated with its SMD components, including the LIS3MDL, as shown in the product picture.

The LIS3MDL has many configurable options, including four selectable sensitivity (gain) settings, a wide choice of output data rates, and a programmable external interrupt signal. The three magnetic field strength readings are available through a digital interface, which can be configured to operate in either I<sup>2</sup>C (TWI) or SPI mode.



The carrier board includes a low-dropout linear voltage regulator that provides the 3.3 V required by the LIS3MDL, which allows the sensor to be powered from a 2.5-5.5 V supply. The regulator output is available on the VDD pin and can supply almost 150 mA to external devices. The breakout board also includes a circuit that shifts the I<sup>2</sup>C clock and data lines to the same logic voltage level as the supplied VIN, making it simple to interface the board with 5 V systems, and the board's 0.1" pin spacing makes it easy to use with standard <u>solderless breadboards</u> and 0.1" perfboards.

This LIS3MDL carrier is pin-compatible with many of our previous ST I<sup>2</sup>C/SPI sensor carriers, including our **LSM303D compass and accelerometer carrier**, and the orientation of the sensor axes is the same. However, since the ICs contain different sensors with different I<sup>2</sup>C addresses and configuration registers, code written to interface with another chip will need to be modified to work with an LIS3MDL.

For sensor fusion applications, our <u>MinIMU-9 v5</u> and <u>AltIMU-10 v5</u> inertial measurement units combine this LIS3MDL with an <u>LSM6DS33</u> 3-axis accelerometer and 3-axis gyro on a single board, providing nine independent readings that can be used to calculate an absolute orientation. The AltIMU-10 v5 also includes an <u>LPS25H</u> pressure sensor that can be used to calculate altitude.

## **Specifications**

• Dimensions: 0.4" × 0.9" × 0.1" (10 × 23 × 3 mm)

Weight without header pins: 0.6 g (0.02 oz)

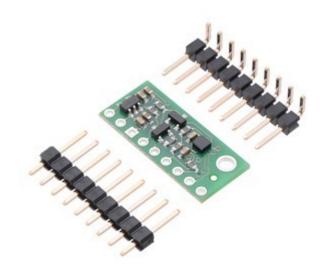
Operating voltage: 2.5 V to 5.5 V

Supply current: 3 mA

- Output format (I2C/SPI): one 16-bit reading per axis
- Sensitivity range (configurable): ±4, ±8, ±12, or ±16 gauss

### **Included components**

A 1×9 strip of <u>0.1" header pins</u> and a 1×9 strip of <u>0.1" right-angle header pins</u> are included, as shown in the picture below. You can solder the header strip of your choice to the board for use with <u>custom cables</u> or <u>solderless breadboards</u>, or you can solder wires directly to the board itself for more compact installations.



The board has one mounting hole that works with #2 and M2 **screws** (not included).

### Using the LIS3MDL

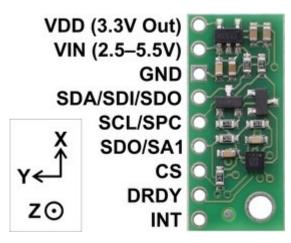
### **Connections**

Regardless of the interface being used to communicate with the LIS3MDL, its VIN pin should be connected to a 2.5 V to 5.5 V source, and GND should be connected to 0 volts. (Alternatively, if you are using the board with a 3.3 V system, you can leave VIN disconnected and bypass the built-in regulator by connecting 3.3 V directly to VDD.)

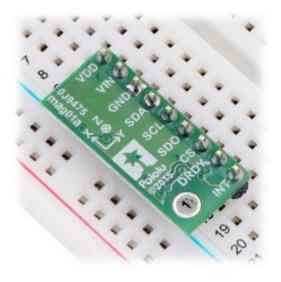
A minimum of two logic connections are necessary to use the LIS3MDL in I<sup>2</sup>C mode (this is the default mode): SCL and SDA. These pins are connected to built-in level-shifters that make them safe to use at voltages over 3.3 V; they should be connected to an I<sup>2</sup>C bus

operating at the same logic level as VIN. The remaining pins are <u>not</u> connected to level-shifters on the board and are <u>not</u> 5V-tolerant, but our **4-channel bidirectional logic level shifter** can be used externally with those pins to achieve the same effect.

To use the LIS3MDL in SPI mode, four logic connections are typically used: SPC, SDI, SDO, and CS. These should be connected to an SPI bus operating at the same logic level as VIN. The SPI interface operates in 4-wire mode by default, with SDI and SDO on separate pins, but it can be configured to use 3-wire mode so that SDO shares a pin with SDI.



LIS3MDL 3-Axis Magnetometer Carrier with Voltage Regulator, labeled top view.



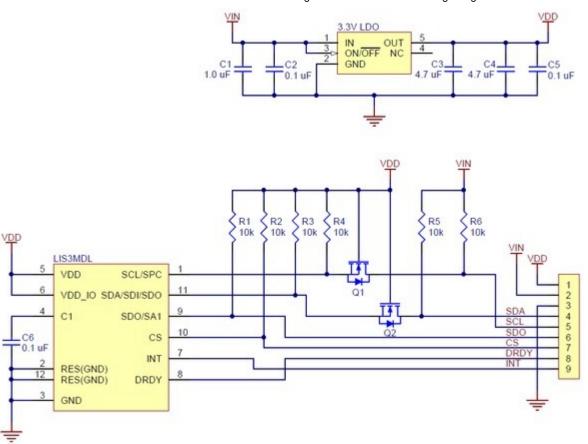
LIS3MDL 3-Axis Magnetometer Carrier with Voltage Regulator in a breadboard.

#### **Pinout**

PIN	Description
VDD	Regulated 3.3 V <b>output</b> . Almost 150 mA is available to power external components. (If you want to bypass the internal regulator, you can instead use this pin as a 3.3 V input with VIN disconnected.)
VIN	This is the main 2.5 V to 5.5 V power supply connection. The SCL/SPC and SDA/SDI level shifters pull the I²C and SPI bus high bits up to this level.
GND	The ground (0 V) connection for your power supply. Your I <sup>2</sup> C or SPI control source must also share a common

	ground with this board.
SDA/SDI/SDO	Level-shifted $I^2C$ data line and SPI data in line (also doubles as SDO in 3-wire mode): HIGH is VIN, LOW is 0 V
SCL/SPC	Level-shifted I <sup>2</sup> C/SPI clock line: HIGH is VIN, LOW is 0 V
SDO/SA1	SPI data out line in 4-wire mode: HIGH is VDD, LOW is 0 V. <i>This output is not level-shifted.</i> Also used as an input to determine I <sup>2</sup> C slave address (see below).
CS	SPI enable (chip select). Pulled up to VDD to enable I <sup>2</sup> C communication by default; drive low to begin SPI communication.
DRDY	Data ready indicator, a 3.3-V-logic-level output. HIGH (3.3 V) indicates magnetic data can be read. <i>This output is not level-shifted.</i>
INT	Programmable interrupt, a 3.3-V-logic-level output. This output is not level-shifted.

# Schematic diagram



The above schematic shows the additional components the carrier board incorporates to make the LIS3MDL easier to use, including the voltage regulator that allows the board to be powered from a single 2.5-5.5 V supply and the level-shifter circuit that allows for I<sup>2</sup>C and SPI communication at the same logic voltage level as VIN. This schematic is also available as a **downloadable PDF** (90k pdf).

### I<sup>2</sup>C communication

With the CS pin in its default state (pulled up to VDD), the LIS3MDL can be configured and its readings can be queried through the I<sup>2</sup>C bus. Level shifters on the I<sup>2</sup>C clock (SCL) and data lines (SDA) enable I<sup>2</sup>C communication with microcontrollers operating at the same voltage as VIN (2.5-5.5V). A detailed explanation of the protocol can be found in the **LIS3MDL datasheet** (2MB pdf), and more detailed information about I<sup>2</sup>C in general can be found in **NXP's I<sup>2</sup>C-bus specification** (1MB pdf).

In I<sup>2</sup>C mode, the sensor's 7-bit slave address has its *second-least* significant bit determined by the voltage on the SA1 pin. The carrier board pulls SA1 to VDD through a 10 k $\Omega$  resistor, making the second-least significant bit 1 and setting the slave address to 0011110b by default. If the selected slave address happens to conflict with some other device on your I<sup>2</sup>C bus, or if you want to use two LIS3MDL sensors on the same bus, you can drive SA1 low to set the second-least significant bit to 0 (which sets the slave address to 0011100b).

The I<sup>2</sup>C interface on the LIS3MDL is compliant with the I<sup>2</sup>C fast mode (400 kHz) standard. In our tests of the board, we were able to communicate with the chip at clock frequencies up to 400 kHz; higher frequencies might work but were not tested.

### **SPI** communication

To communicate with the LIS3MDL in SPI mode, the CS pin (which the board pulls to VDD through a 10 k $\Omega$  resistor) must be driven low before the start of an SPI command and allowed to return high after the end of the command. Level shifters on the SPI clock (SPC) and data in (SDI) lines enable SPI communication with microcontrollers operating at the same voltage as VIN (2.5 V to 5.5 V).

In the default 4-wire mode, the sensor transmits data to the SPI master on a dedicated data out (SDO) line that is <u>not</u> level-shifted. If the SPI interface is configured to use 3-wire mode instead, the SDI line doubles as SDO and is driven by the LIS3MDL when it transmits data to the master. A detailed explanation of the SPI interface on the LIS3MDL can be found in its <u>datasheet</u> (2MB pdf).

### **Sample Code**

We have written a basic <u>Arduino library for the LIS3MDL</u> that makes it easy to interface this sensor with an <u>Arduino</u> or Arduino-compatible board like an <u>A-Star</u>. The library makes it simple to configure the LIS3MDL and read the raw magnetometer data through I<sup>2</sup>C.

### **Protocol hints**

The datasheet provides all the information you need to use this sensor, but picking out the important details can take some time. Here are some pointers for communicating with and configuring the LIS3MDL that we hope will get you up and running a little bit faster:

• The magnetometer is in power-down mode by default. You have to turn it on by writing the appropriate value to the CTRL\_REG3 register.

- You can read or write multiple registers in a single I<sup>2</sup>C command by asserting the most significant bit of the register address to enable address auto-increment.
- You can enable the same auto-increment feature in SPI mode by asserting the second bit (bit 1, called the MS bit in the datasheet) of an SPI command.
- In addition to the datasheet, ST provides an <u>application note</u> (598k pdf) containing additional information and hints about using the LIS3MDL.

### People often buy this product together with:



Accelerometer and
Gyro Carrier with
Voltage Regulator



MinIMU-9 v5 Gyro,
Accelerometer, and
Compass
(LSM6DS33 and
LIS3MDL Carrier)