

TI81xx-HDVPSS-01.00.01.24 ReleaseNotes

Introduction

This release notes provides important information that will assist you in using the HDVPSS software package. This document provides the product information and know issues that are specific to the HDVPSS software package.

New in this Release

TI816x

- Bug fixes. This is a patch release on top of 01.00.01.22 release.
- Tested on DDR3 as well as DDR2 EVM

TI814x

- None

What is Supported

Common

- Supports HDVPSS drivers for TI814x/TI816x EVMs
- Supports FVID2 interfaces for all the supported drivers
- Package includes HDVPSS DSP/BIOS driver sources, sample applications sources on HDVPSS DSP/BIOS drivers, sample applications executable
- Supports TI816x Silicon ES 1.0 and ES 1.1

Display Drivers

- Supports Display Controller driver
- Supports Bypass Path Display driver
- Supports Secondary Path Display driver
- Supports Graphics Display driver

Capture Drivers

- Supports VIP capture driver (For TI814x only VIP 0 port A and port B are supported)
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Memory to Memory Drivers

- Supports Scalar driver through Secondary Path 0-SC5
- Supports Scalar driver through Bypass Path 0/1-SC5
- Supports SC3/4 Scalar driver through Secondary Path 0/1-SC3/4- VIP0/1.
- Supports Noise filter driver
- Supports DEI driver (For TI814x only DEI bypass is supported)

What is not supported

TI816x

- Runtime parameters and subframe processing support in SC3/4 Scalar M2M drivers
- Triple display sample application is not supported

TI814x

- On chip HDMI is not supported
- DEI in De-interlacing mode is not supported
- SD Display is not supported
- Triple display sample application is not supported

Driver Maturity

Driver Maturity

Driver	TI816x	TI814x
VIP 0/1 Capture	Beta	EA
Display Controller	Beta	EA
Bypass Path 0/1 Display	Beta	EA
Secondary Path 1 SD Display	EA	Not supported
GRPX Path 0/1/2 Display	Beta	EA
NSF M2M	Beta	EA
DEIH-WB0 M2M	Beta	NA
DEIH-VIP0 SC3 M2M	Beta	NA
DEIH-WB0-VIP0 SC3 M2M	Beta	NA
DEI-WB1 M2M	Beta	EA
DEI-VIP1 SC4 M2M	Beta	EA
DEI-WB1-VIP1 SC4 M2M	Beta	EA
Secondary Path 0 - SC5 M2M	Beta	EA
Bypass Path 0/1 - SC5 M2M	EA	Not supported
Secondary Path 0/1 - VIP SC3/4 M2M	EA	Not supported
Proxy Server	Beta	Not supported

Fixed in this release

Common

- SDOCM00076074 - [Build] config_proxy_ti816x.bld should be renamed as config_proxy_C6A8.bld

Display

- None

Capture

- None

M2M

- SDOCM00075699 - WBSC Rt param update results random artifacts
- SDOCM00075405 - Un-initialized variable in vpsMdrvDeiCfgDeiMqCore ()

Issues and Limitations

Issues and limitations for all the supported HDVPSS drivers could be found at the respective driver section at HDVPSS User Guide

Installation and Usage

Installation and Usage of the HDVPSS package could be found at HDVPSS User Guide

Upgrade and Compatibility information

- Proxy Server: By default the proxy server application is now built for TI816x and the corresponding memory map.
- On Chip HDMI Pixel Clock:
 - ES 1.0 - Pixel clock requires to 2X, However HD VENC D requires to be clocked at 1X. (e.g. To display 1080P60 the required pixel clock is 148.5 MHz, we would require to configure video PLL to clock pixel clock at 297 MHz.) This could achieved by configuring the video PLL to 2X clock (hd_venc_d_clk) and update "CLKC Video Encoder Clock Select" to hd_venc_d_clk1x_select should be set
 - ES 1.1 - Doubling of pixel clock is not required.

Dependencies

This release requires following tools/packages to be installed.

- Code Composer Studio Version: 4.2.0.09000 or 5.0.0.000xx
- XDC Tools Version: 3.20.05.69_eng
- BIOS Version: 6.31.00.06_eng
- CG Tool (TMS470) Version: 4.6.3
- IPC: ipc.1.22.00.13_eng
- Syslink: 02.00.00.65_alpha2

Devices Supported

- TI814x EVM
- TI816x EVM

Validation Information

- This release is validated on TI814x/TI816x EVMs for the above mentioned components.

Technical Support and Product Updates

For further information or to report any problems, contact <http://e2e.ti.com> or <http://community.ti.com> or <http://support.ti.com>.
