A	Introduction to Digital Logic						
A1	Information Representation, Number Conversion, Binary Addition						
A2	Introduction to Logic Operations						
A3	Introduction to Boolean Algebra						
A4	Example Logic Functions						
A5	Karnaugh Maps						
A6	Introduction to FPGAs and Verilog						
A7	Timing Analysis (covered at end of term) 1						
B	Digital Storage Elements						
B1	Latches						
B2	Flip-Flops						
B3	Counters and registers						
B4	Resets and Enables						
C	Finite State Machines (FSMs)						
C1	Intro to FSMs						
C2	State assignment						
C3	Verilog for FSMs						

D	Introduction to Computer Organization and Assembly Language						
D1	Intro to Processors						
D2	Signed Numbers						
D3	Instruction Set Architecture						
D4	Basic Instruction Execution						
E	Advanced Assembly Language						
E1	Subroutines						
E2	I/O Devices						
E3	Interrupts						