



The screenshot displays the Cadence Virtuoso schematic editor interface. The main workspace shows a schematic diagram of a 2-to-1 multiplexer (MUX21) circuit. The circuit consists of two input buffers (V0 and V1) connected to the inputs of the MUX21 block. The output of the MUX21 is connected to an output buffer (V2). The inputs are connected to a common source (VDD) and a common sink (GND). The output is connected to a common source (VDD) and a common sink (GND). The circuit is simulated at 27°C.

The interface includes a Navigator pane on the left showing the project structure, a Schematic Editor pane in the center, and a Property Editor pane on the right. The Property Editor shows the attributes of the selected component, including its name (MUX21), library (CMOC_vikram), and various simulation parameters.