Equivalence class manager

- maintains candidate equivalence
- substitute when proved
- refine when disproved



SAT-sweeping manager

- initialize equivalence classes
- define resources of SAT solver and simulator
- iterate over nodes in a topological order





STP-based Circuit simulator

- reverse simulation & cut algorithm
- disprove candidate equivalence



SAT solver

- (dis)prove candidate equivalence
- generate CEs for simulation





Transitive fanin manager

maximum number of nodes in the TFI to be compared