Rayat Shikshan Sanstha's

Name of the college: Sadguru Gadage Maharaj college, karad

Subject: Electronics

Expt. No. 5

Date 9/10/24

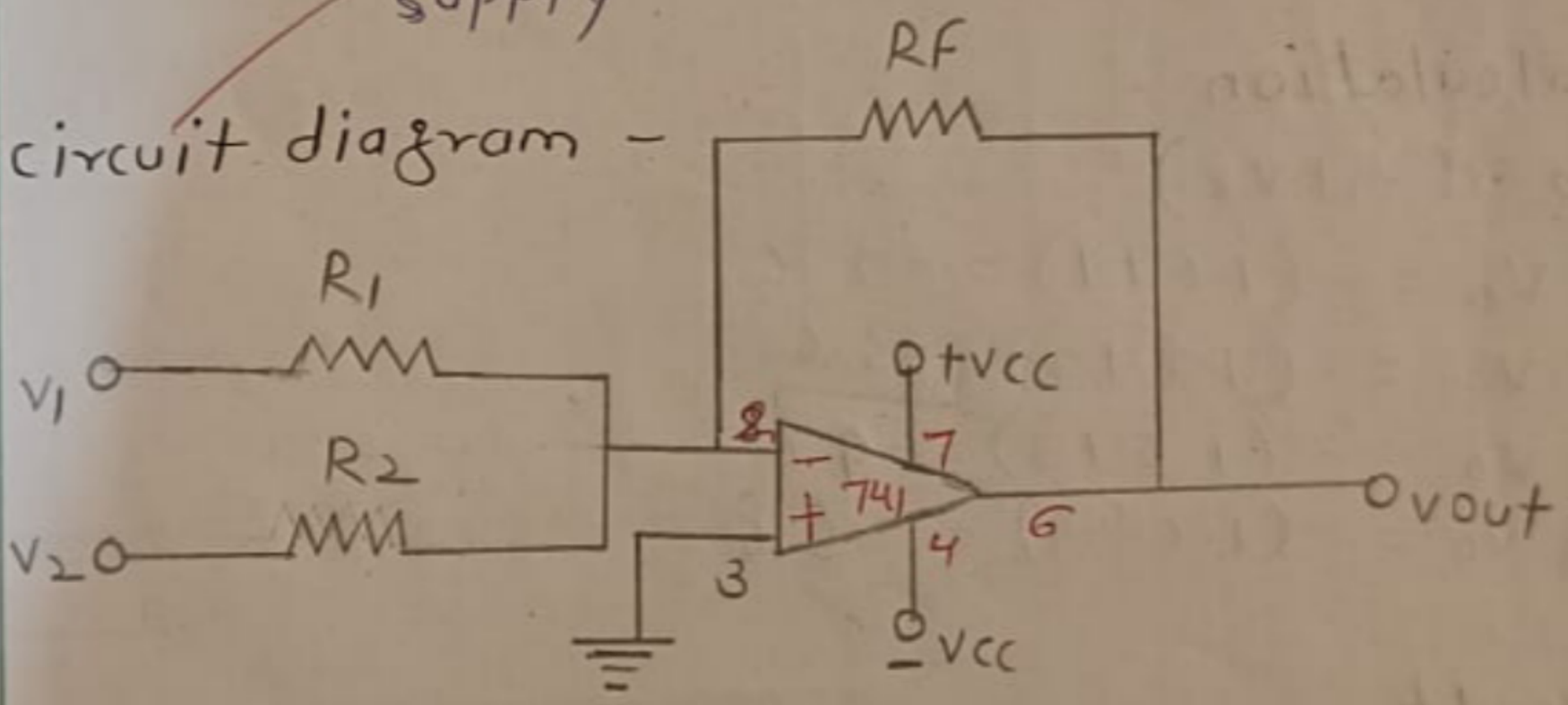
Name: [redacted] Class: BCS-I Batch: Roll No.: [redacted]

Title of Expt.: OP-amp as adder

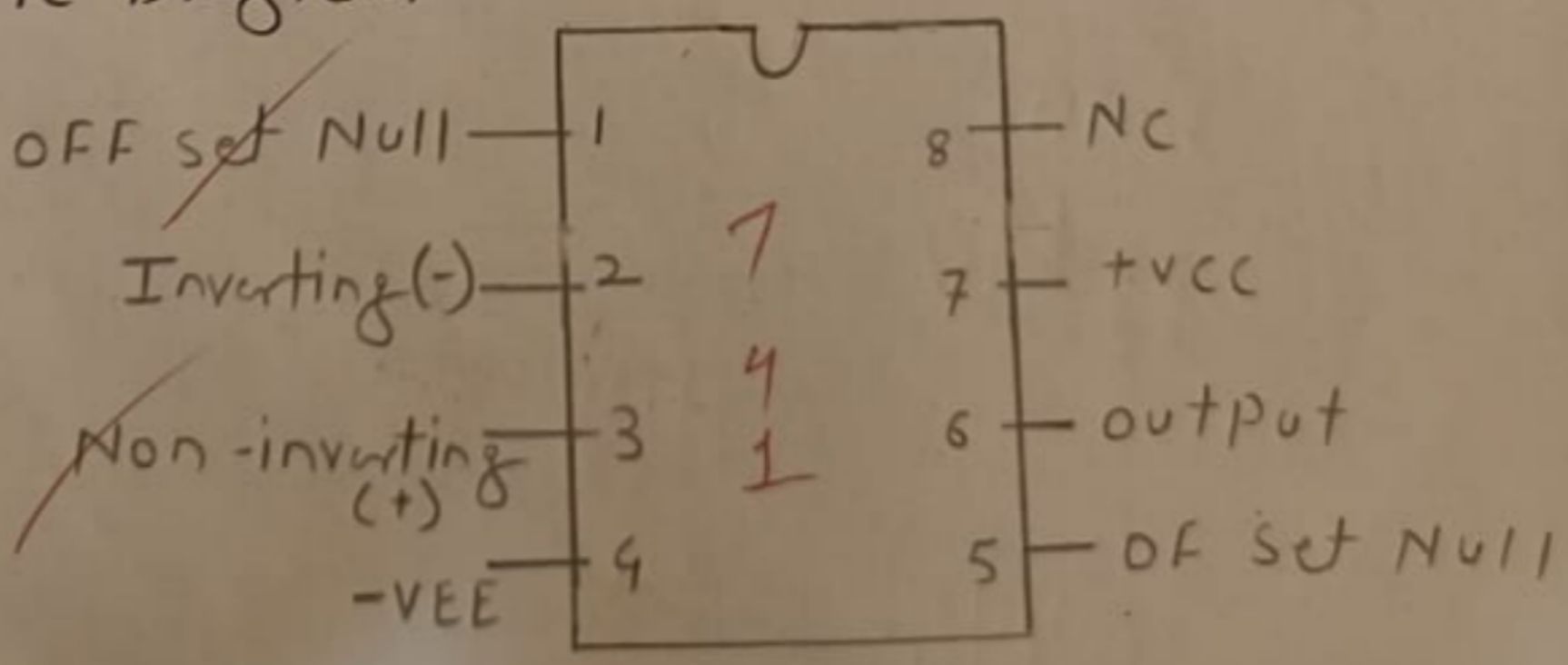
Aim - To study the application of OP-AMP as adder.

Apparatus - op-amp 741, resister, dual Power supply

circuit diagram -



Pic Diagram -



Remarks

9-10-2024

Signature

Observation Table -

Obs No	V_1	V_2	Output Voltage $V_o = (V_1 + V_2)$	
			calculated	observed
1.	1.6	1	2.6	-2.61
2.	1.6	2	3.6	-3.67
3.	1.6	3	4.6	-4.58
4.	1.6	4	5.6	-5.70

Calculation -

$V_o = (V_1 + V_2)$

1] $V_o = -(1.6 + 1) = -2.6$

2] $V_o = -(1.6 + 2) = -3.6$

3] $V_o = -(1.6 + 3) = -4.6$

4] $V_o = -(1.6 + 4) = -5.6$

Result -

Studied OP-AMP as adder circuit