Name of the college: Sadguru Gadage Maharaj college, Karad

Subject: Electronics

Expt. No. 6

Name: ███████████

Class: BCS-I   Batch: _____   Roll No.: ███████

Date 28/8/24   Title of Expt.: Full Adder

Aim - To study of the full Adder

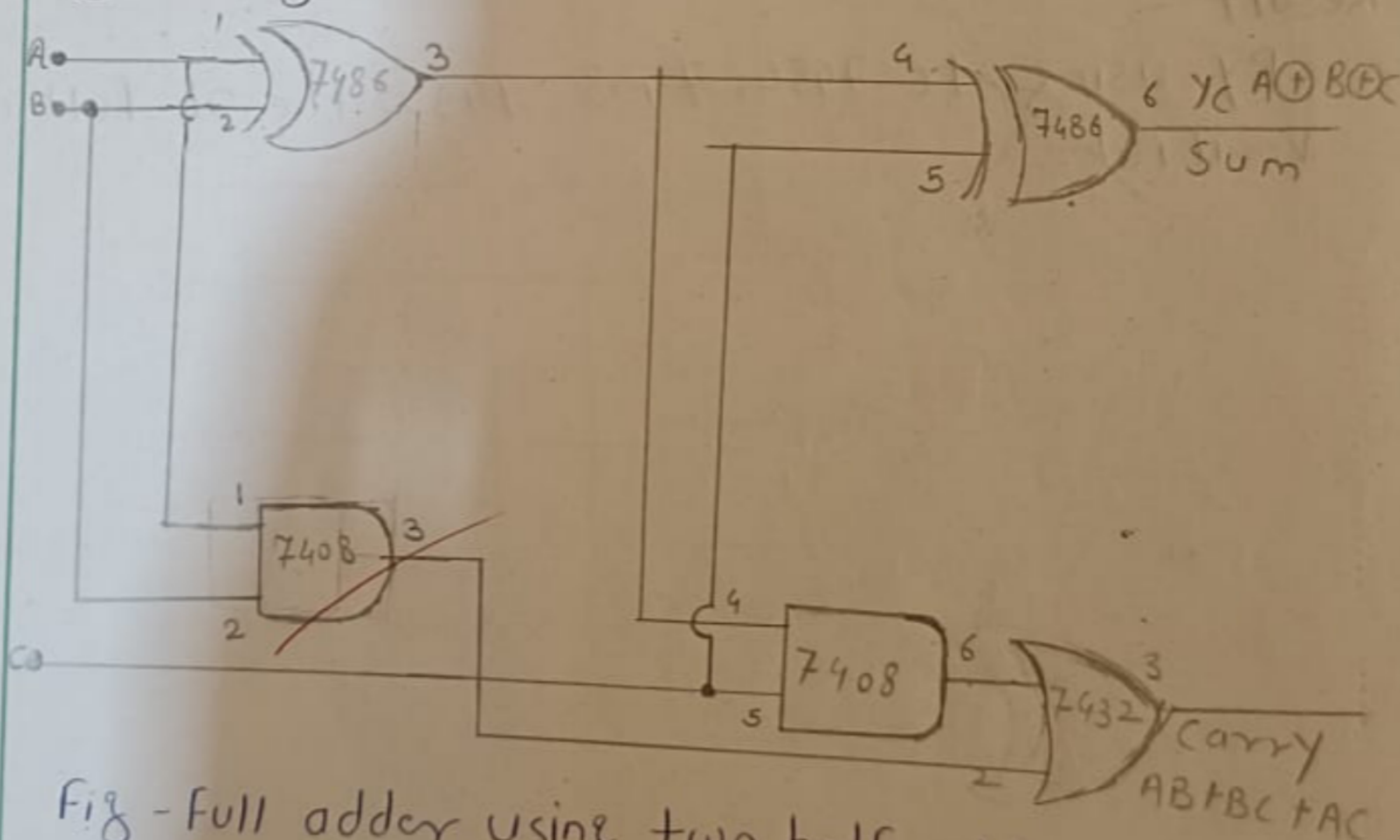Equipment - Ic 7486, 7408, 7432. Power supply etc

Logic Diagram -

Fig - Full adder using two half adder

Truth Table -

Input			Output	
A	B	C	carry	sum
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

Result -

By using Ic 7486, 7408, 7432 the OF Full adder is verified.