



Name of the college: Sadguru Gadage Maharaj college, Korad  
Subject: Electronics

Expt. No. 3

Name: [Redacted] Class: BCS-I Batch: [Redacted] Roll No.: [Redacted]

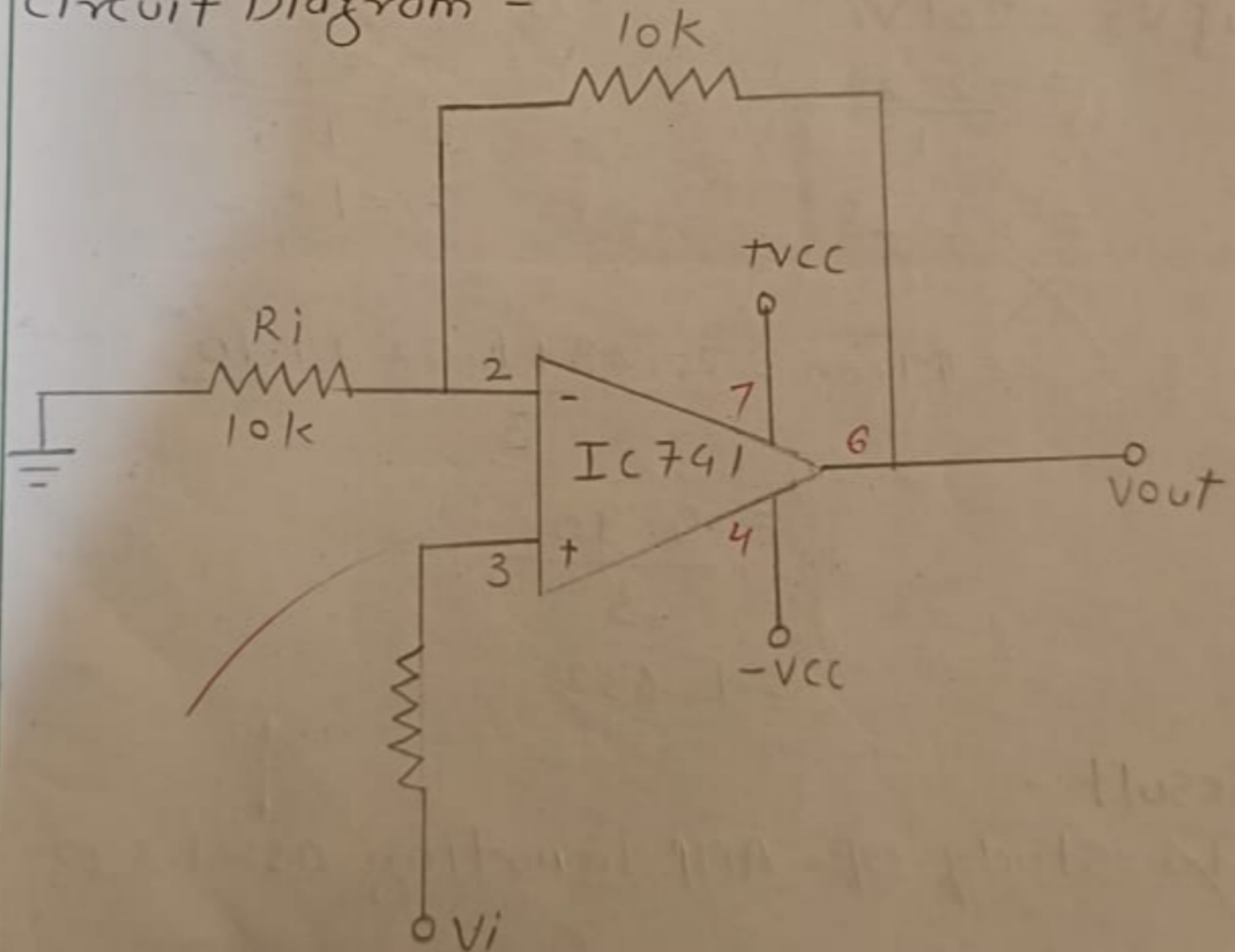
Date 13/10/24

Title of Expt.: O-P-AMP as a Inverting Amplifier

Aim - To study OP-AMP as Inverting

Apparatus - OP-AMP IC 741, Resistors, dual power supply etc.

Circuit Diagram -



Remarks

*[Signature]*  
8-10-2024  
Signature



observation table -

obs No	Input	Output	Gain	Mean Gain
1	1	-2.28	-2.28	
2	1.5	-2.25	-1.52	1.633
3	2	-2.20	-1.10	

Calculation -

$$\begin{aligned}
 1] V_2 &= V_o / V_i \\
 &= \frac{-2.28}{1} \\
 &= -2.28
 \end{aligned}$$

$$\begin{aligned}
 2] V_2 &= V_o / V_i \\
 &= \frac{-2.25}{1.5} \\
 &= -1.52
 \end{aligned}$$

$$\begin{aligned}
 3] V_2 &= V_o / V_i \\
 &= \frac{-2.20}{2} \\
 &= -1.10
 \end{aligned}$$

$$\begin{aligned}
 \therefore \text{Mean} &= \frac{-2.28 + (-1.52) + (-1.10)}{3} \\
 &= \frac{-4.90}{3} \\
 &= -1.633
 \end{aligned}$$

Result -

To study op-AMP inverting os - 1.633