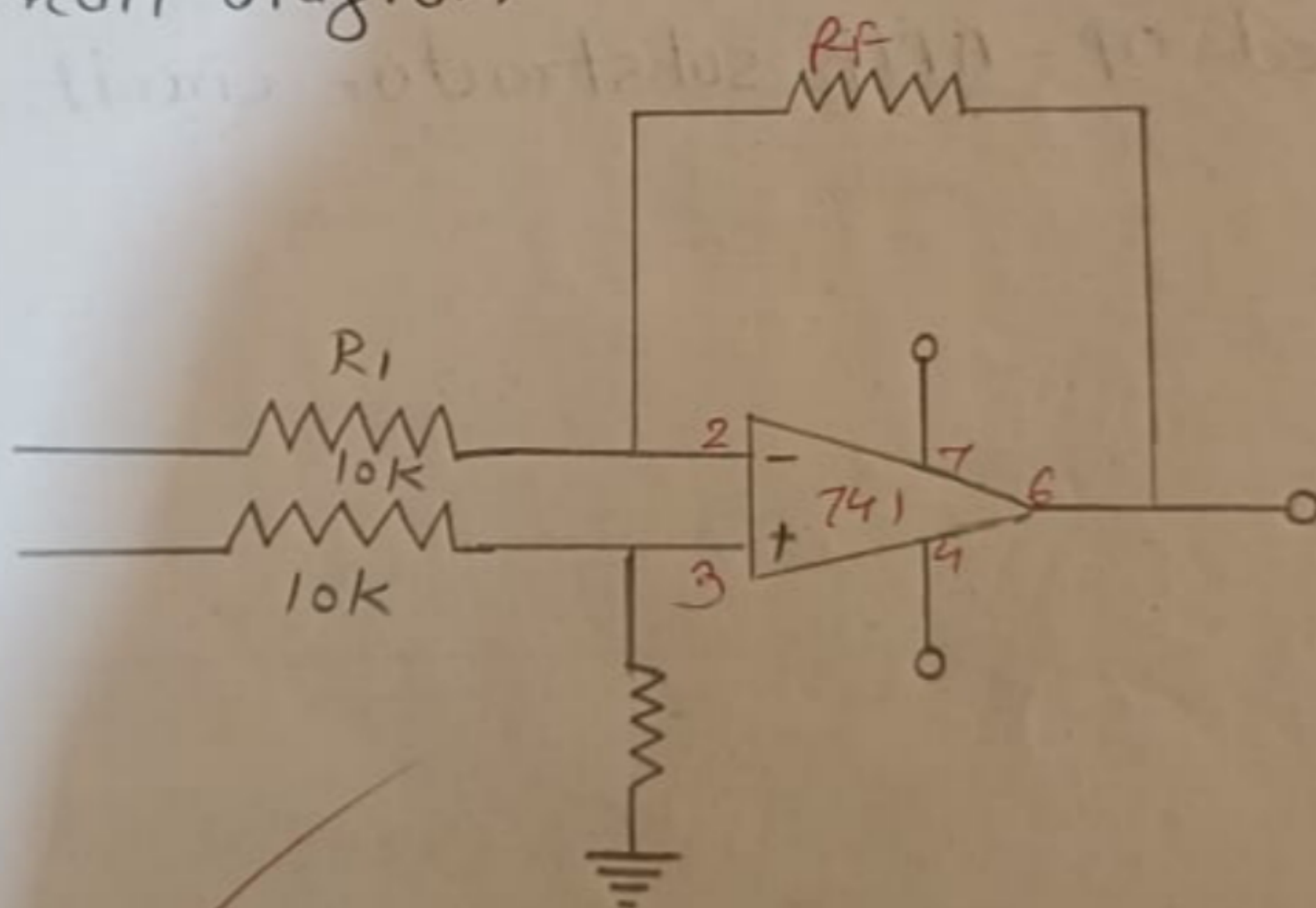


Name of the college: Sadguru Gadage Maharaaj college, KaradSubject: ElectronicsExpt. No. 6Name: [Redacted]Class: BCS-I Batch: [Redacted]Roll No.: [Redacted]Date 9/10/29Title of Expt.: OP-AMP as an subtractor

Aim - To study the Application of op - AMP as subtractor

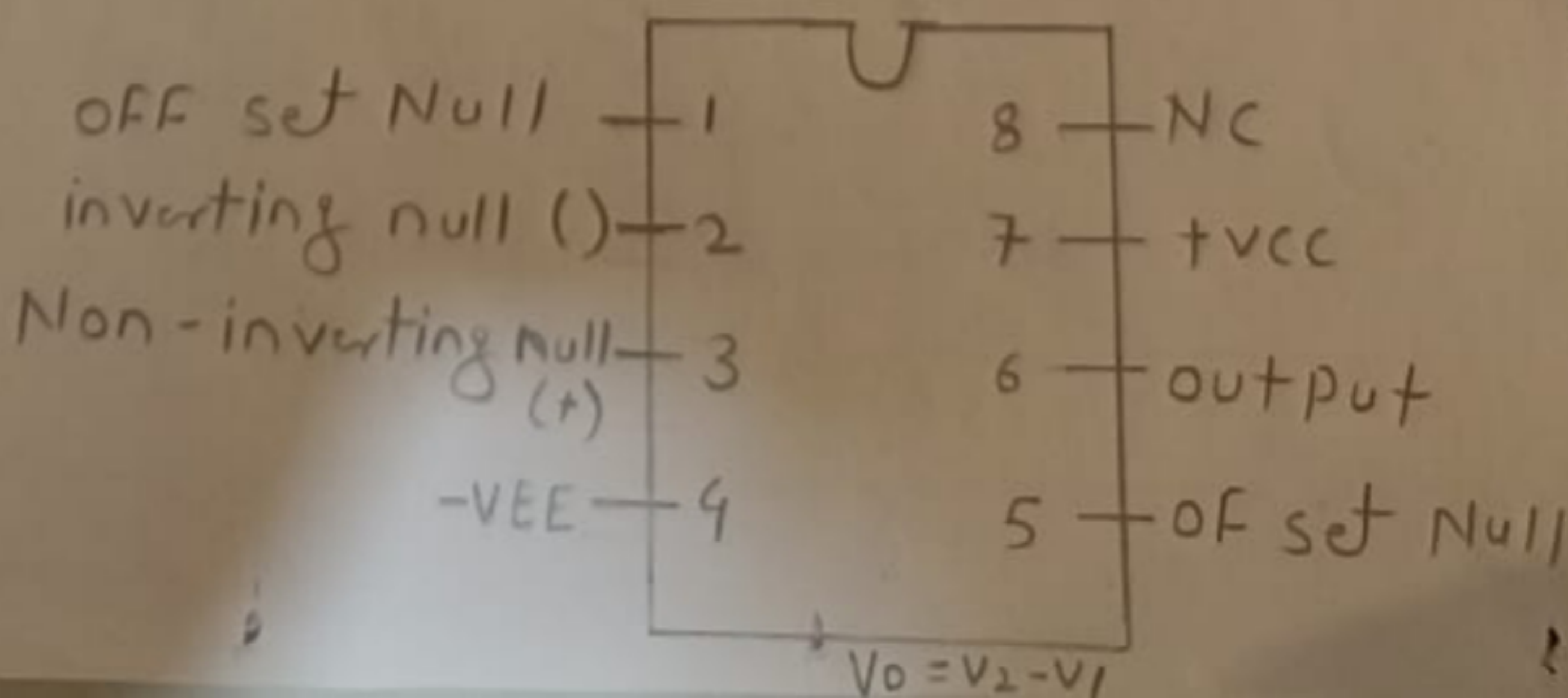
Apparatus - OP-AMP 741, Resistor, dual Power, Supply

circuit diagram -



Pin - Diagram

Remarks



[Signature]
9-10-2024
Signature

Observation table -

| obs. No. | Input | output | output voltage | |
|----------|-------|--------|---------------------------------|----------|
| | | | calculated $V_0 = V_2 - V_1$ | observed |
| 1 | 1.6 | 2 | 0.4 | 0.5 |
| 2 | 1.6 | 3 | 1.4 | 1.6 |
| 3 | 1.6 | 4 | 2.4 | 2.5 |
| 4 | 1.6 | 5 | 3.4 | 3.6 |

Result -

studied OP-AMP subtractor circuit