



Rayat Shikshan Sanstha's

Name of the college: Sadguru Gadge Maharaj college, Kurad

Subject: Electronics

Expt. No. 5

Name: [REDACTED]

Class: BCS-I Batch: [REDACTED]

Roll No.: [REDACTED]

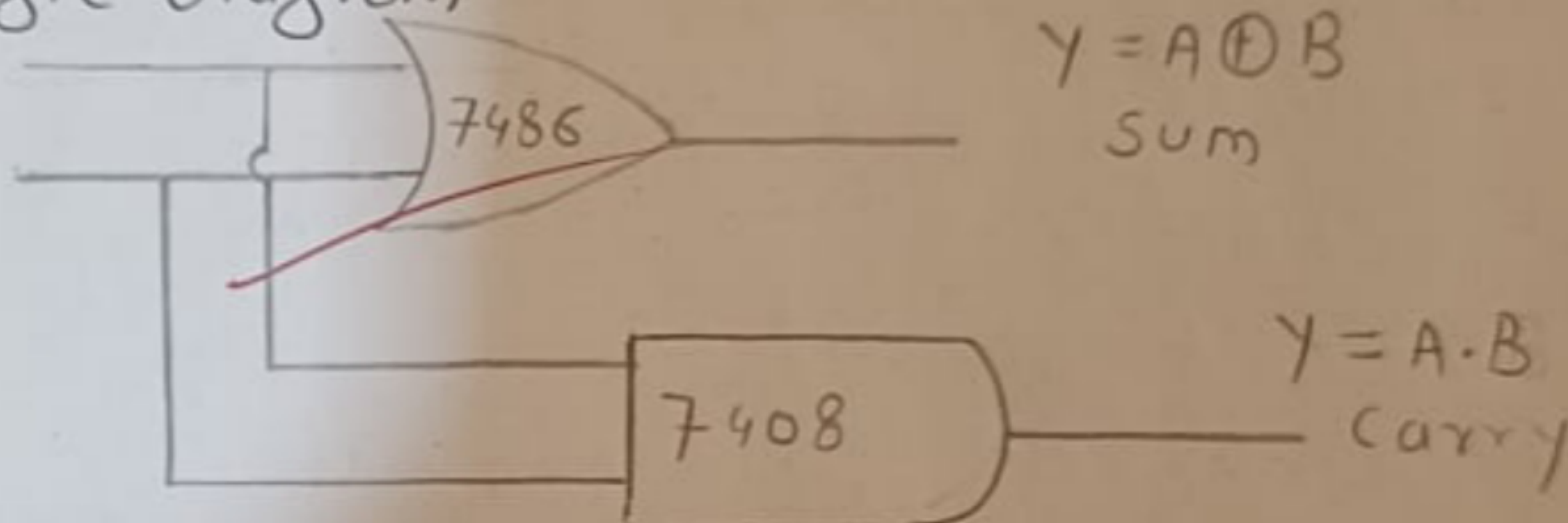
Date 4/9/24

Title of Expt.: Study of half adder

Aim - To study of half adder

Equipment - Ic 7486, Ic 7408, LED and power supply etc.

Logic diagram -



| A | B | Sum | Carry |
|---|---|-----|-------|
| 0 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |

Remarks

Result - By using Ic 7486, Ic 7403 the half adder truth table is verified.

Signature
18/09/2024