1's complement of 8-bit

Aim: Write an ALP program to study 1's complement of an 8-bit data.

Flow chart :-

```
        ( START )
            |
            v
  ┌─────────────────────────────┐
  │ load the data in accumulator │
  │   at the address of 6200H    │
  └─────────────────────────────┘
            |
            v
  ┌─────────────────────────────────────────┐
  │ Taking compliment at the accumulator     │
  └─────────────────────────────────────────┘
            |
            v
  ┌─────────────────────────────────────┐
  │ Store the result at the address      │
  │            6201H                     │
  └─────────────────────────────────────┘
            |
            v
        ( STOP )
```
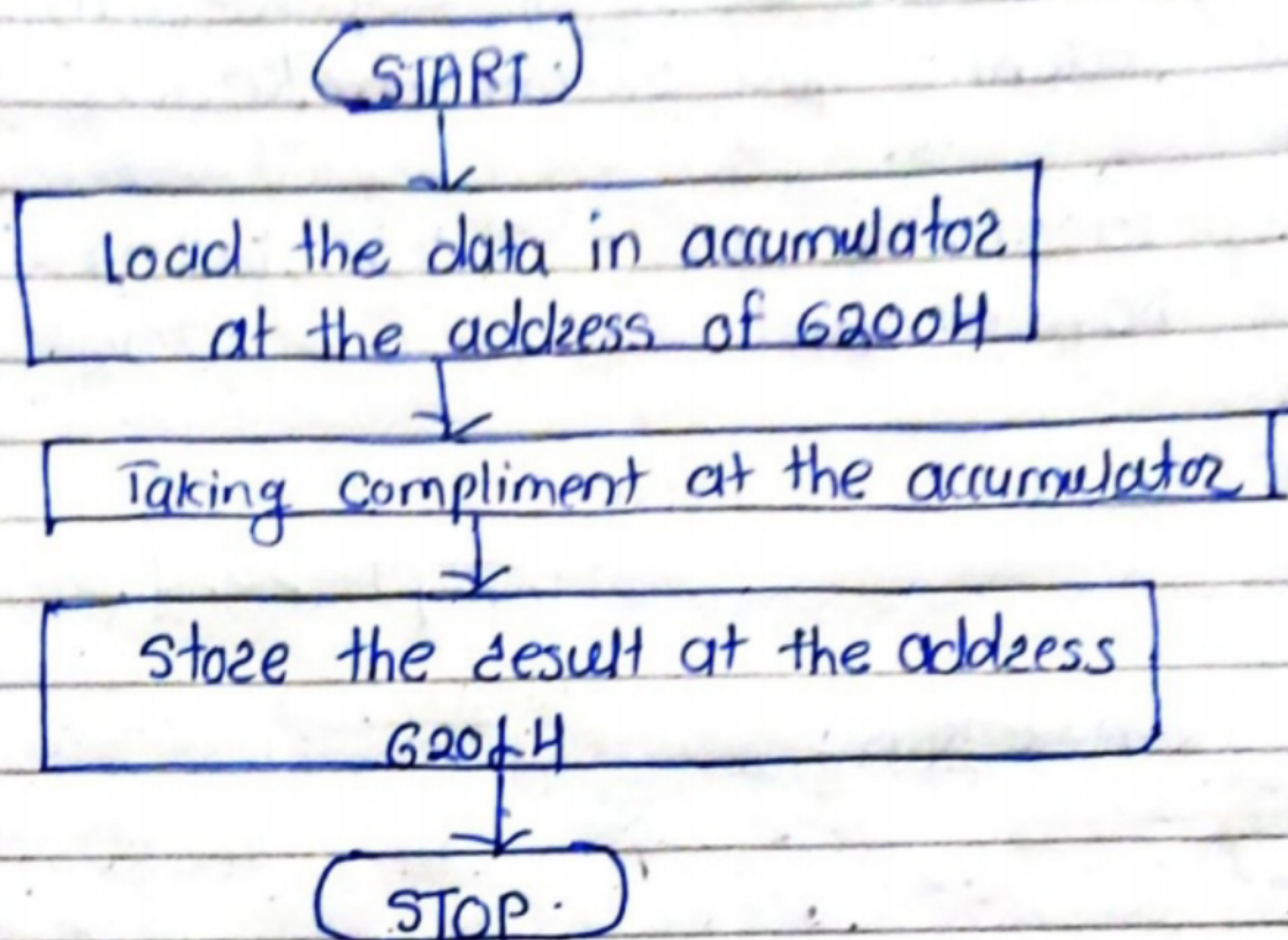
Memory location	Mnemonics.	machine code	comment
6000H	LDA 6200H	3A	load data
6001H		00	at address
6002H		62	6200H
6003H	CMA.	2F.	Taking complimnt
6004H	STA 6201H	32	Store the
6005H		01	result at
6006H		62	6201H
6007H	RST1	CF	stop.

Memory location:

6200H	
6201H	