

# B. Tech Electrical Engineering Indian Institute of Technology, Bombay

09001009

**UG Third Year (B.Tech.)** 

Male

DOB: 12/08/1991

Examination	University	Institute	Year	CPI / %
Graduation	IIT Bombay	IIT Bombay	2012	6.43
Intermediate/+2	Maharashtra State Board	Dr. Babasaheb Ambedkar College, Nagpur	2009	89.33
Matriculation	Maharashtra State Board	S.F.L. Highschool, Dhamangaon(Rly)	2007	91.69

# **AWARDS AND ACHIEVEMENTS**

• Got a **Rajarshi Shahu Maharaj Scholarship** for being **1st** among SC students in **Amravati Division** in 10th

Jul '07

• Excelled in **Intermediate Drawing Examination** conducted by Government of Maharashtra

• Got a consolation prize in Maharashtra Talent Search Exam

'06 '05

# SCHOLASTIC ACHIEVEMENTS

- Secured a rank among top 5% students in Joint Entrance Exam
- Secured a rank among top 0.01% students in All India Engineering Entrance Exam

# **KEY ACADEMIC PROJECTS**

**UID** (*Unique Identity Detection*)

Sep-Nov '09

Guide: Prof. Phatak (Computer Science Engg. Dept.)

- Developed an in-house code to prepare a daily database which will be further used to detect and identify the fingerprints of personnel to manage and monitor entry and exit at various locations
- It is currently being used in labs on trial basis

RC-Plane Mar '10

Guide: Prof. Sudhakar and Prof. Arya (Aerospace Engg. Dept.)

• Made an **RC-plane** to study and analyze the different control parts and motions of flight **Superheterodyne FM Receiver** 

Oct'11

Guide: Prof. Merchant (Electrical Engg. Dept.)

• Designed a superheterodyne FM receiver system which can be tuned to receive any local FM radio stations

#### **TECHNICAL PROJECTS**

• Made a wireless remote controlled F1 car

'09

• Implemented line following algorithm in AVR Microcontroller

'09

• Made an attempt to design a system using **Arduino Ethernet Shield** to control a remotely placed bot using internet

Mar'11

# RELEVENT COURSES

- Network Theory: Studied different method to solve linear and nonlinear circuits
- Electrical Machines and Power Electronics: Studied different circuits to drive different types of motors
- Digital Systems: Studied how to implement a system using finite state machine
- Analog Circuits: Studied the internal circuitry of OPAMP and different amplifiers and oscillators
- **Microprocessors**: Studied the architecture and programming of microprocessor chip 8085 by intel, ARM microprocessor, microcontrollers
- **Communication systems**: Studied different types of modulations and time division multiplexing
- VLSI Design: Studied different architectures to implement the circuit on a chip in 180nm technology. Designed and simulated basic logic gates, counters, adders using magic and spice

# **COMPUTER PROFICIENCY**

- **Programming Languages**: C, C++, HTML, Python
- Software Proficiency: MicroSoft Office, MATLAB, scilab
- Operating System: Windows, Linux
- HDL and Circuit Simulation: verilog, spice, magic, eagle

# POSITION OF RESPONSIBILITY

• Coordinator: Techfest '11 team

Dec-Jan'11

Handled a team of 12 organizers under Technoholix team to plan and execute a night show. The show saw crowd of over 5000 with performers coming from Virginia.

• Coordinator: Mood-Indigo '10 team

Dec '11

Was responsible for handling zorbing and All-terrain vehicle events under Informal

# OTHER INFORMATION

• Languages known: English, Marathi, Hindi and beginner Sanskrit