

SHRUTI HIRAY

CONTACT INFORMATION	AS6 - 04/25 School of Computing National University of Singapore	Email: shrutihiray15@gmail.com Homepage: shrutihiray.github.io
EDUCATION	National University of Singapore PhD, Computer Science Advisor: Dr. Prateek Saxena	<i>August 2020 - Present</i>
	Indian Institute of Technology Bombay Bachelor and Master of Technology, Electrical Engineering Minor degree, Computer Science Advisor: Dr. Saravanan Vijayakumaran CGPA: 8.58/10	<i>July 2014 - June 2019</i>
PROFESSIONAL EXPERIENCE	Visiting Scholar at <i>School of Computing, NUS</i> <ul style="list-style-type: none">Implemented a type inference system using unification and constraint generation.Inferring variable types in binaries with type rules.Developed using Datalog and Ghidra tool. Summer Risk Analyst at <i>American Express</i> <ul style="list-style-type: none">Built dynamic pricing engine for Membership Rewards using Gradient Boost.Achieved 80% accuracy of prediction of expected points redeemed by customer.Received pre-placement offer for remarkable work in the internship. Android Application Developer at <i>Carnot Technologies</i> <ul style="list-style-type: none">Developed APIs for data sync with hardware device via BLE communication.Analyzed user activity with CleverTap and crashes with Crashlytics.Developed JUnit Test Cases using Espresso Test Framework to test the app features on various mobile devices present in Google Cloud Testing Lab.	<i>Autumn 2019</i> <i>Summer 2017</i> <i>Summer 2016</i>
RESEARCH PROJECTS	Lightning Network Simulator - Dual Degree Thesis Guide: Prof. Saravanan Vijayakumaran <ul style="list-style-type: none">Implemented a discrete event simulator for efficient analysis in PythonSuccessfully mirrors subset of messaging protocol of lightning network.Scales upto ten thousand nodes on a 8 GB RAM machine. Multisource and Multipath Content Transfer Guides: Prof. D. Manjunath & Prof. Nikhil Karamchandani <ul style="list-style-type: none">Parallel download through Cellular and WiFi from multiple sources.Implemented browser plugin using Javascript & Google Native Client.Parts of file downloaded from cache in WiFi access points reduces network congestion considerably and increases throughput.	<i>Spring 2017 - Spring 2019</i> <i>Autumn 2018</i>
TEACHING ASSISTANTSHIP	EE720: Number Theory & Cryptography EE308: Communication Systems	Prof. Saravanan Vijayakumaran Prof. S. N. Merchant

AWARDS	<ul style="list-style-type: none"> • Awarded NUS Research Scholarship • Excellence in Teaching Assistantship for EE720: Number Theory & Cryptography • Recipient of IIT Bombay Heritage Foundation Scholarship • Dhirubhai Ambani Scholarship for exceptional performance at Intermediate Level • Awarded Kishore Vaigyanik Protsahan Yojana (KVPY) mentorship.
KEY ACADEMIC PROJECTS	<p>Algorand Simulator <i>Spring 2019</i></p> <ul style="list-style-type: none"> • Discrete event simulator of Algorand using Gossip, Cryptographic sortition. • Tested for Fail-Stop adversary, Byzantine adversary. <p>Chord based Dictionary <i>Spring 2019</i></p> <ul style="list-style-type: none"> • Implemented a distributed word dictionary using a Distributed Hash Table. • Performed insert and lookup operations using the Chord protocol. <p>Optimal Relay Node Placement in LTE-A Cellular System <i>Spring 2018</i></p> <ul style="list-style-type: none"> • Jointly optimized for downlink and uplink for coverage extension. • Analyzed the effect of decoding threshold and probability of subcarrier activity. <p>Speech Watermarking <i>Autumn 2017</i></p> <ul style="list-style-type: none"> • Developed watermark extraction algorithm for proving ownership, tampering. • Implemented using Autoregressive model & Psychoacoustic model. <p>Automatic Speech Recognition system <i>Autumn 2017</i></p> <ul style="list-style-type: none"> • Classified speech signals of spoken numerical digits using labelled data. • Used Cepstrum, K-means clustering, Dynamic time warping. <p>Microprocessor Design <i>Autumn 2016</i></p> <ul style="list-style-type: none"> • Designed a 16 bit - 8 register RISC computer system of 14 instructions. • A 6 staged pipeline processor with hazard mitigation using data-forwarding.
RELATED COURSEWORK	<p>Computer Science: Data Structures & Algorithm, Operating Systems, Machine Learning, Blockchain Technology, Number Theory & Cryptography, Network Security, Computer Networks, Discrete Structures, Computer Graphics</p> <p>Electrical: Error Correcting Codes, Speech Processing, Wireless Communications, Advanced Communication Networks, Advanced Signal Processing, Microprocessors</p> <p>Others: Probability, Data Analysis, Economics.</p>