

Date: 26/06/2024

Certificate of Completion

This is to certify that **Mr. Durgesh Gupta** has successfully completed an internship from **24/05/2024** to **24/06/2024** at the Novel Integrated Electronics (NINE) Labs (Under MeitY, Govt. of India) Indian Institute of Technology Guwahati. During this period he made contributions to the project entitled “**Design and Development of AI/ML Co-Processor and Post Quantum Cryptography Co-Processor (An Initiative towards Electronics System Design and Manufacturing in North-East Region)**”

Internship Details:

Duration: 30 Days

Project Topics: RTL to GDSII Flow

Tools and Technologies Used

- **Programming Languages:** TCL, VIM Editor
- **Design Tools:** Cadance
- **Technology Node and Foundry:** GDPK 45 nm

Mr. Durgesh Gupta demonstrated exceptional skills showing a keen interest in learning and applying new technologies. He worked diligently and efficiently, contributing valuable insights and solutions to the project.

We wish **Mr. Durgesh Gupta** all the best in his future endeavours.



Dr. Hanumant Singh Shekhawat
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