21BDS0340

Abhinav Dinesh Srivatsa

Electronics Lab

Task 1

Exercise No.: 1 Date: 24/02/22

Introduction to the Hardware Equipment's, components to be used, Resistor coding, Capacitors, Multimeters, Function Generator and CRO

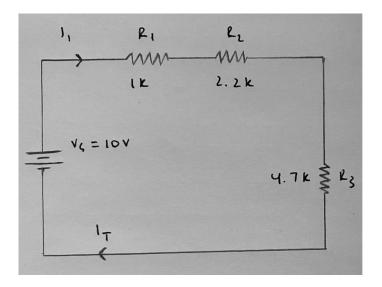
Part A: Reading Resistor by Colour Coding

No.	Bandi	Band 2	Band 3	band 4	Nominal Value
ı	orange	orange	black	gold	33 ± 5.1.
۷	orange	white	red	gold	3.9 k ± 5.1.
3	From	green	orange	9012	15k ± 5.1.
ч	169	violet	orange	9010	27 K ± 5.1.
5	orange	white	orange	9014	39 k ± T·1.
Ь	brown	black	gold	none	1 ± 20.1.
7	(14	violet	orange	9014	27k ± 5.1.
1	be own	black	red	gold	1k ± 5.1.
9	blue	grey	brown	gold	780 + 2·1
10	yellow	violet	red	gold	4.7k ± 5.1.
Į l	red	120	red	9012	2.2k ± 5.1.

Part B: Using Multimeter to Measure Resistance, Voltage and Current

Exercise 1

Circuit Diagram



Components Required

Basic – Resistors

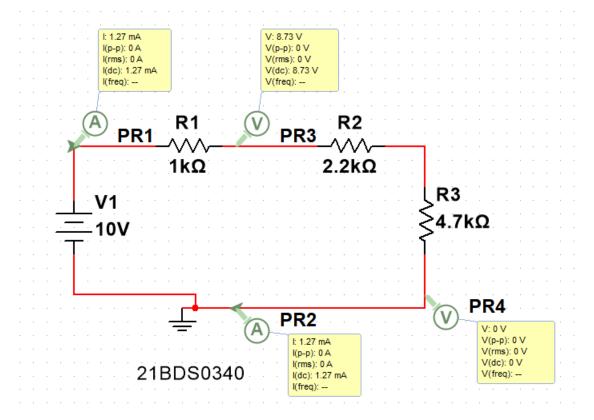
Sources – Power Sources – DC_Power, Ground

Manual Workings

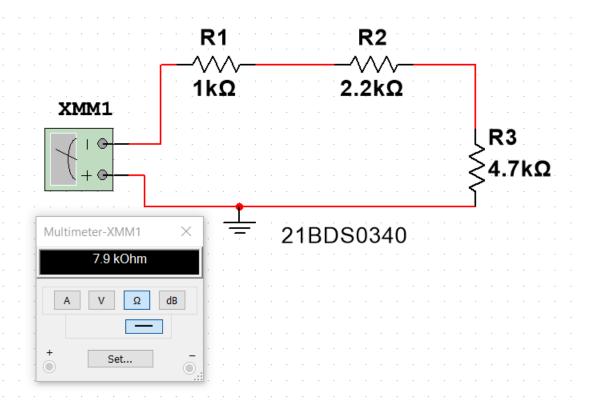
= 7900 onms

1. 11=17 because the recistors are in series

Simulated Circuit Diagram for Finding Current and Voltage



Simulated Circuit Diagram for Finding Resistance

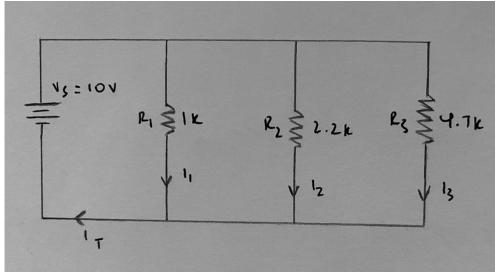


Results

NOTATION	MANUAL CALCULATIONS	SIMULATED RESULT
l ₁	1.26 mA	1.27 mA
I _T	1.26 mA	1.27 mA
V_{R1}	1.26 V	1.27 V
V_{R2R3}	8.74 V	8.73 V
R_{eq}	7900 ohms	7900 ohms

Exercise 2

Circuit Diagram



Components Required

Basic – Resistors

Sources – Power Sources – DC_Power, Ground

Manual Workings

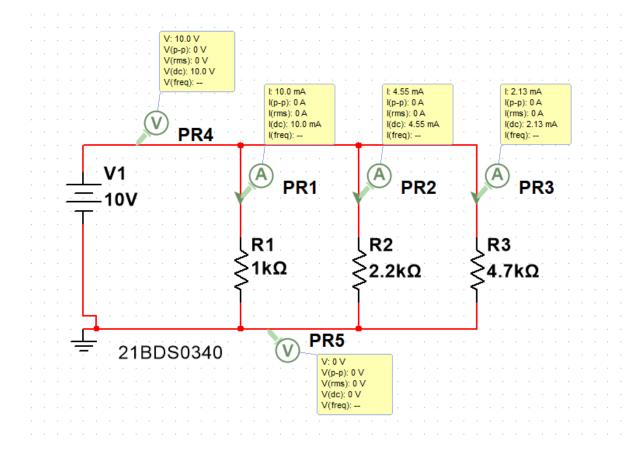
V5 = 10 V

$$1. \quad 1_1 = \frac{v_{\zeta}}{R_1}$$

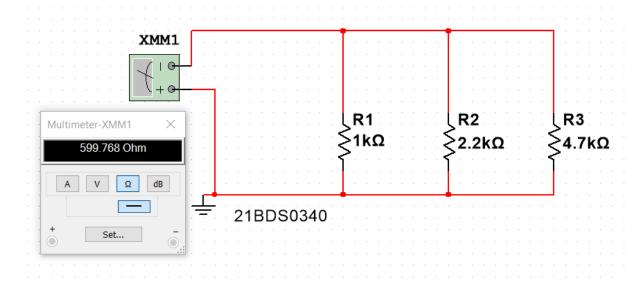
$$l_2 = \frac{v_5}{R_2}$$

2. 1 = 1, + 12 + 13 because they are in parallel.

Simulated Circuit Diagram for Finding Current and Voltage



Simulated Circuit Diagram for Finding Resistance

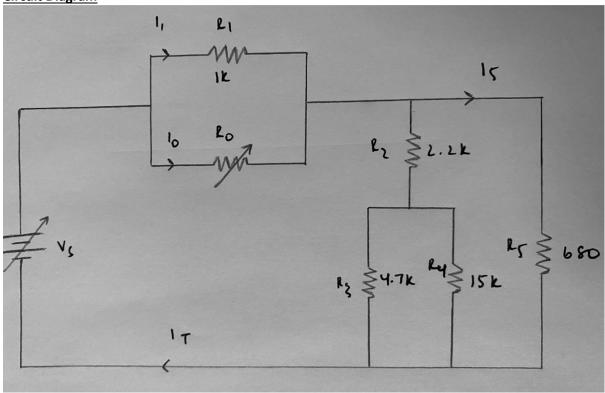


Results

NOTATION	MANUAL CALCULATIONS	SIMULATED RESULT
I ₁	10 mA	10 mA
l ₂	4.5 mA	4.5 mA
l ₃	2.1 mA	2.1 mA
I _T	16.6 mA	16.6 mA
$R_{\sf eq}$	599.78 ohms	599.78 ohms
V_{R1}	10 V	10 V
V_{R2}	10 V	10 V
V_{R3}	10 V	10 V
- 113		

Exercise 3

Circuit Diagram



Components Required

Basic – Resistors

Sources – Power Sources – DC_Power, Ground

Manual Workings

1.
$$V_{5} = 15V$$
 $R_{0} = 500 \text{ orms}$

2. $R_{XY} = \left(610^{-1} + \left(2200 + \left(4700^{-1} + 15000^{-1}\right)^{-1}\right)^{-1}\right)^{-1}$
 $= \frac{1}{1000} + \frac{1}{100} + \frac{1}{100} + 608.41$
 $= 15.93 \text{ mA}$
 $V_{1} = \frac{1}{1000} + \frac{1}{100} + 608.41$
 $V_{1} = \frac{1}{1000} + \frac{1}{100} = 5.31 \text{ mA} \text{ when } R_{0} = 500$
 $V_{1} = \frac{1}{1000} + \frac{1}{1000} = 10.62 \text{ mA} \text{ when } R_{0} = 500$
 $V_{1} = \frac{V_{1}}{1000} + \frac{1}{1000} + \frac{1}$

3.
$$V_{XY} = 15 - V_{ROFI}$$

$$= 15 - 17 \left(\frac{1}{1000} + \frac{1}{5000} \right)^{-1}$$

$$= 15 - 8.67$$

$$= 6.33 V$$

6.
$$l_0 = \frac{|T|R_1}{R_0 + R_1} - 0$$

and $l_T = \frac{V_S}{\left(\frac{1}{R_1} + \frac{1}{R_0}\right)^{-\frac{1}{4}} 608.41} - 2$

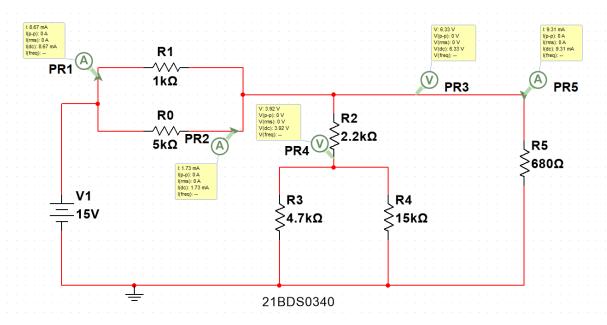
$$\frac{0}{R} \frac{\log (R_0 + R_1)}{\left(\frac{1}{R_1} + \frac{1}{R_0}\right)^{-1} + 608.41}$$

$$\frac{1}{R_{1}} \frac{1_{0} (R_{0} + R_{1})}{R_{1}} = \frac{V_{5}(R_{1} + R_{0})}{R_{1} R_{0} + 608.41(R_{1} + R_{0})}$$

=)
$$R_0 = \frac{v_5 R_1}{l_0} - 608.41 R_1$$

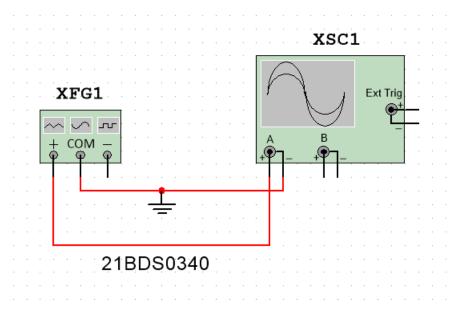
Putting values:

Simulated Circuit Diagram

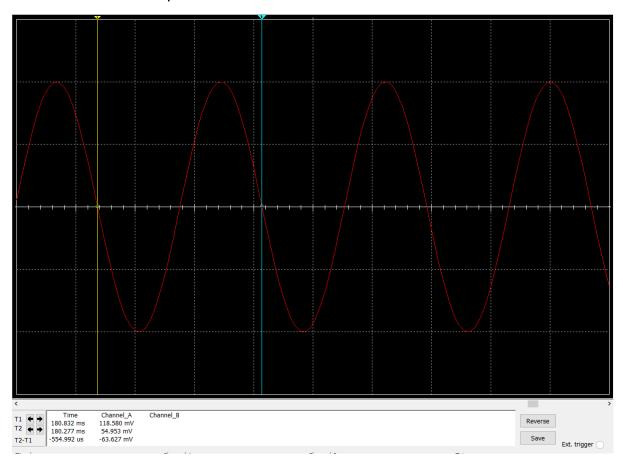


Part C: Study of Function Generator and CRO

Simulated Circuit Diagram



Simulated Sin Wave Output



Components Required

Simulate – Instruments – Oscilloscope, Function Generator

Result

	Function Generator			CRO		
S. No.	Waveform Type	Amp (V)	Freq (Hz)	Amp (V)	Time Period (s)	Freq (Hz)
1	Sin	10	1.8k	10	554.82u	1.8k
2	Triangle	34	95M	34	10.482n	95M
3	Square	20	28	20	71.344m	14

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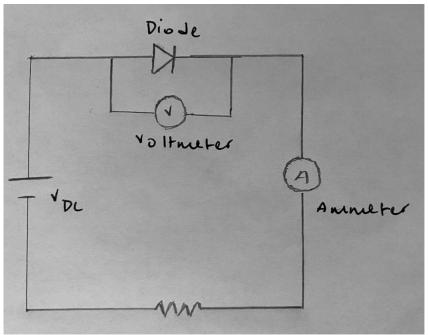


Exercise No.: 2 Date: 24/02/22

VI Characteristics of PN Junction Diode

Forward Bias

Circuit Diagram



Components Required

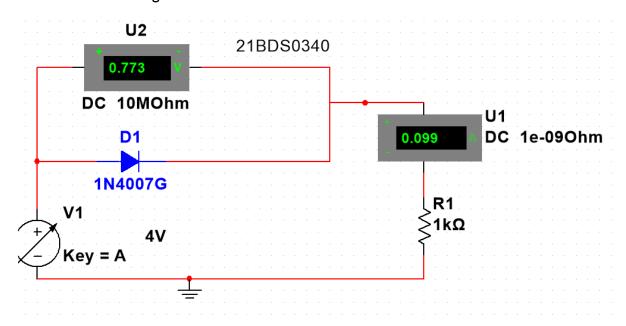
Basic – Resistors

Sources – Power Sources – DC_Power, Ground

Diodes – 1N4007G

Toolbars – Measurement Components – Ammeter, Voltmeter

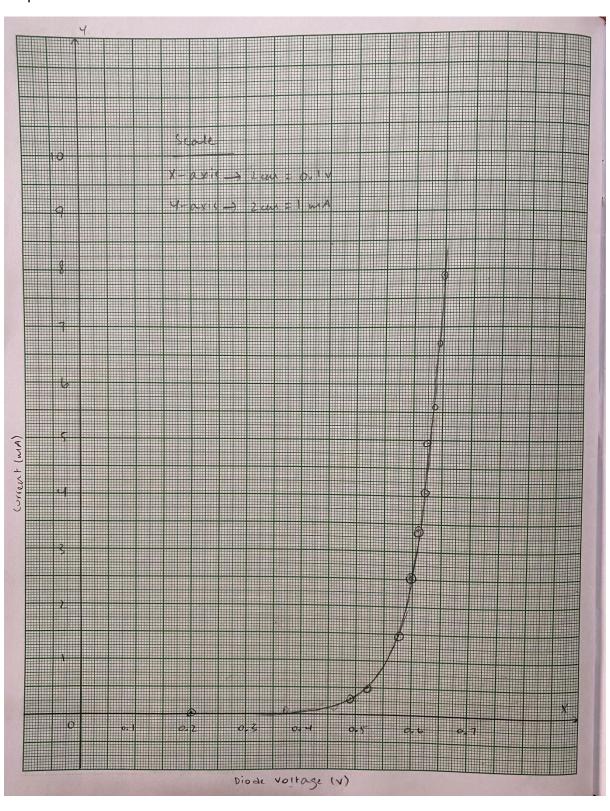
Simulated Circuit Diagram



Readings

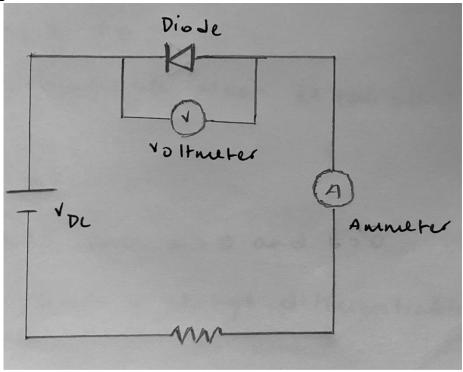
s. No.	Supply	Diode	lircuit wrent
1	voltage (v)		5.31 x 10-7
	0.2	0.199	
ι	0.4	0.377	2.3 x 10 - 5
3	0.6	0.462	1.38 x 10 -4
4	0.8	0. 499	3-01 × 10-4
5	1	0.521	4.8 x 10 -4
L	2	0.572	1.43 × 10-3
7	3	0.596	2.4 x 10-5
8	9	0.612	3.39 × 10 ⁻³
9	5	0.624	4.38 × 10-3
10	6	0.634	5.37 × 10-3
- 11	Т	0.642	6.36 x 10-3
12	8	0.648	7.35 x 10-5
13	٩	0.654	8.35 x 10 -5
14	lo	0.66	9.34 × 10 -3

Graph



Reverse Bias

Circuit Diagram



Components Required

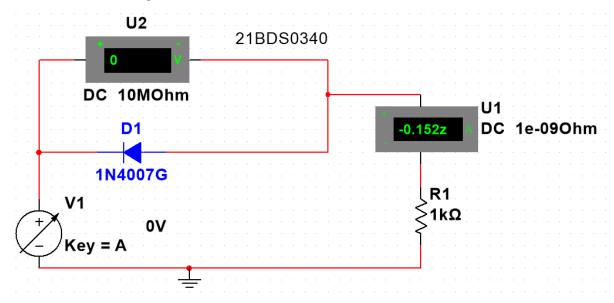
Basic – Resistors

Sources – Power Sources – DC_Power, Ground

Diodes – 1N4007G

Toolbars – Measurement Components – Ammeter, Voltmeter

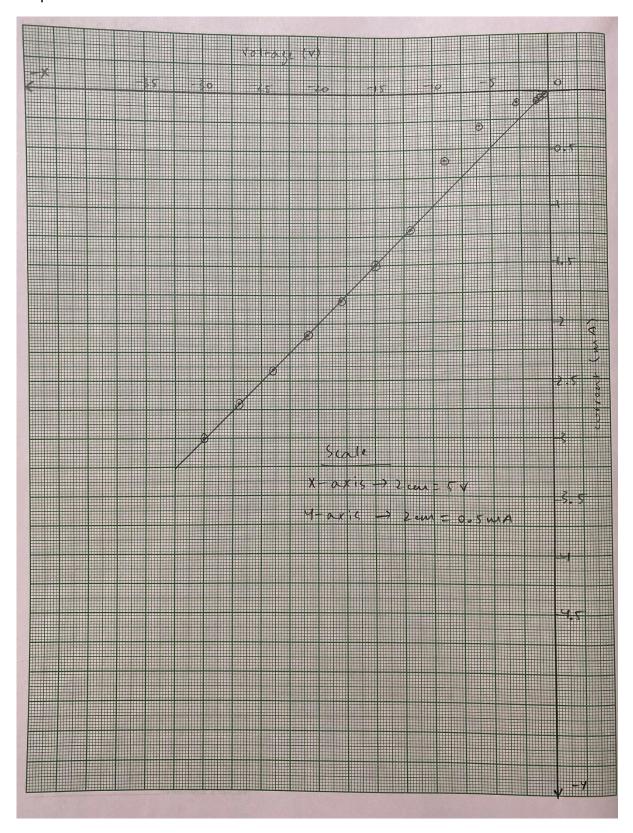
Simulated Circuit Diagram



Readings

S. No.	Supply Voltage (N)	Diode Voltage (-V)	Diode/circuit corrent (-A)		
ι	0.2	٥.٤	2.7 × 10 -8		
2	0.4	0.4	4.7 x 10 -8		
3	0.6	0.6	6.7 × 10 -8		
Ч	0.8	0.8	8.7 x 10 -8		
2	1	1	1. 07 x 10 -7		
6	3	3	3. 07 x 10 -7		
٦	6	5.999	6.07 × 10 -7		
8	9	8.999	9.07 × 10-7		
9	12	11. 999	1. ZI X10 -6		
lo	15	14.998	1.51 × 10 - 6		
11	18	17.998	1.81 x 10 -6		
12	21	20.998	2.11 × 10-6		
13	24	23.998	2.41 × 10-6		
14	27	26.997	2.71 × 10 -6		
15	30	29.997	3.01 × 10-6		

Graph



Verification Message



Reg. No: 21BDS0340 Name: Abhinav Dinesh Srivatsa Date: 27/09/2021