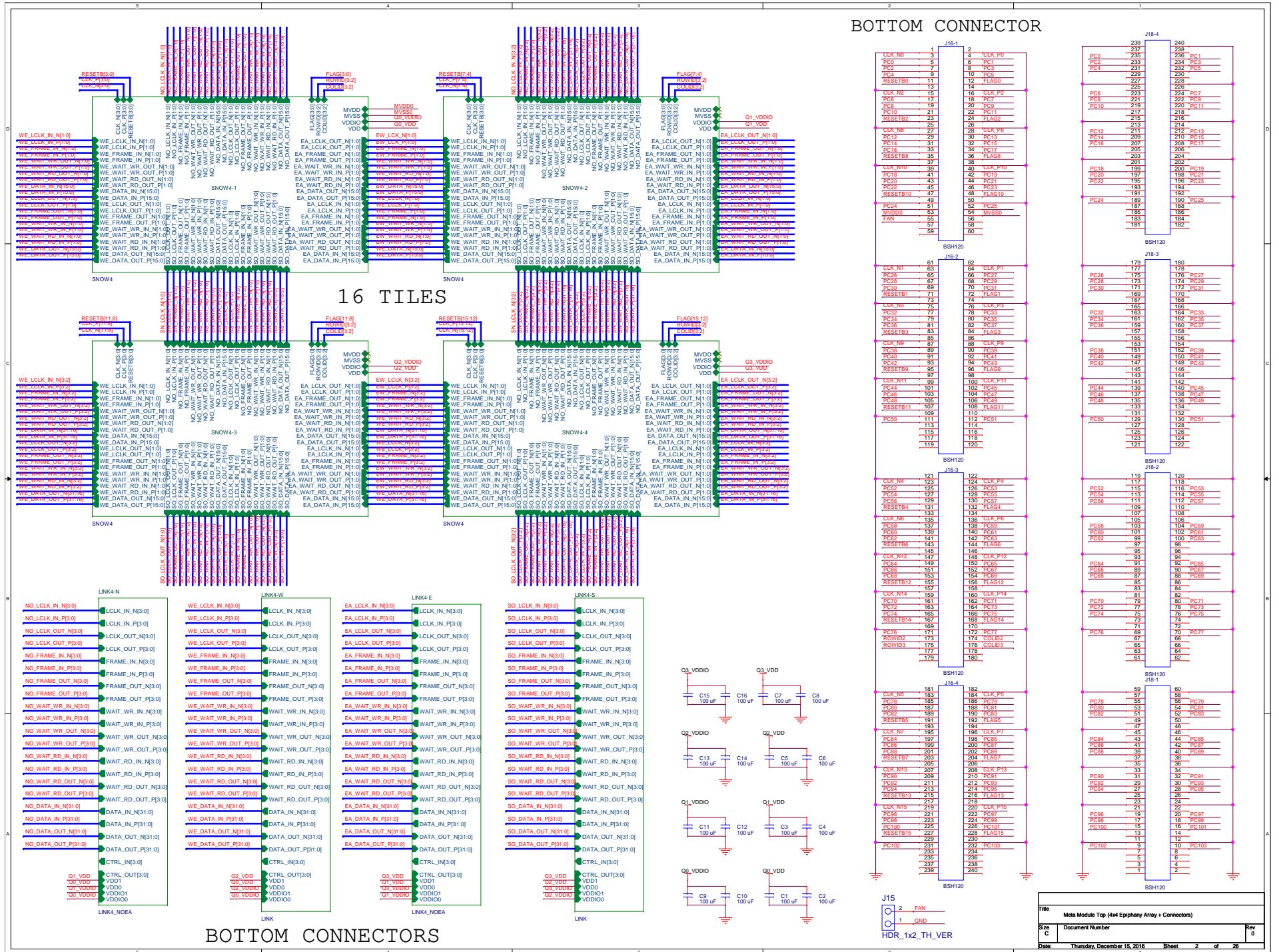
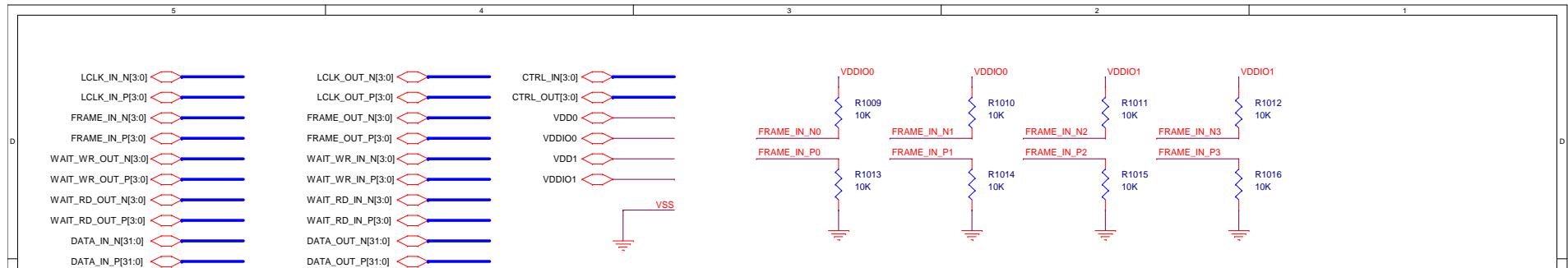


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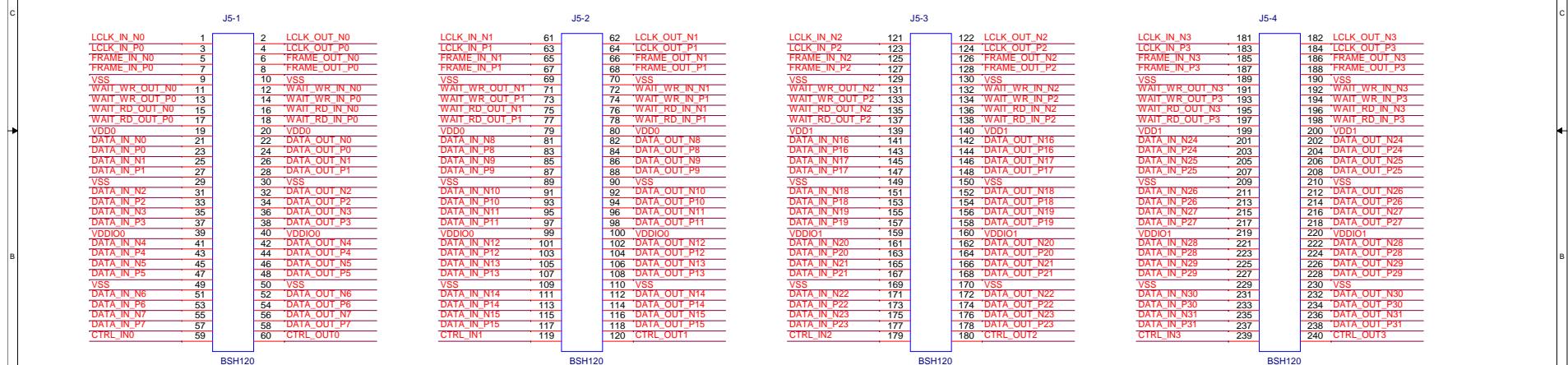
- * PCI104 mechanical foot print including mounting holes
- * 16 chips on top side
- * all other components on bottom side
- * optional one ERF connector on top side
- * signals with _N/_P are 100ohm differential signals
- * all BSH connectors should be placed same distance from edge
- * opposite BSH connectors should be aligned
- * BSH connectors should have extra pin alignment hole

Title		
Meta Module Cover Page		
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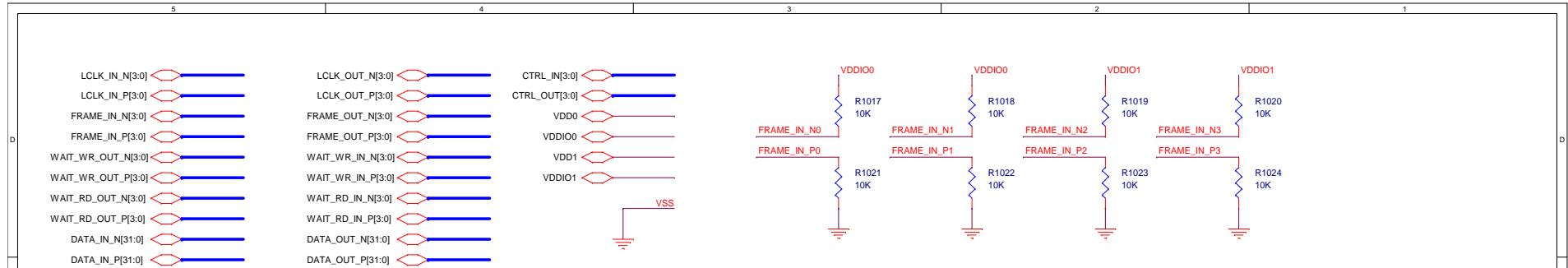




MAPPING BUSES TO 240 PIN BSH CONNECTOR



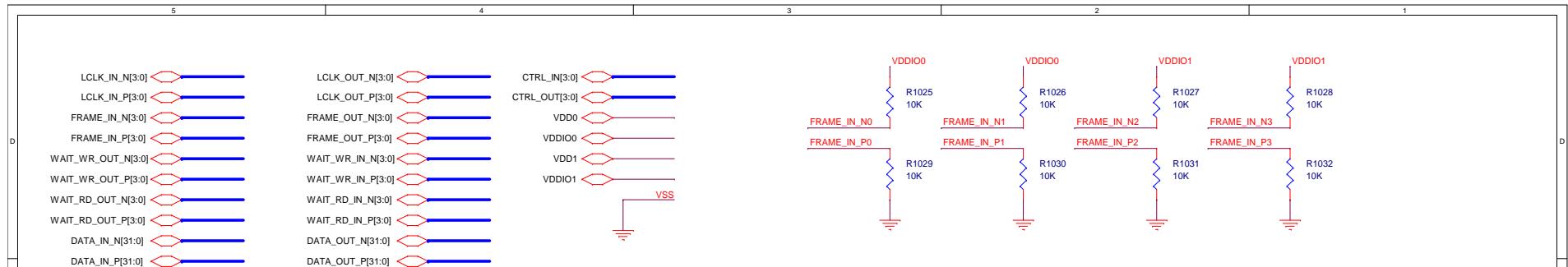
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MAPPING BUSES TO 240 PIN BSH CONNECTOR



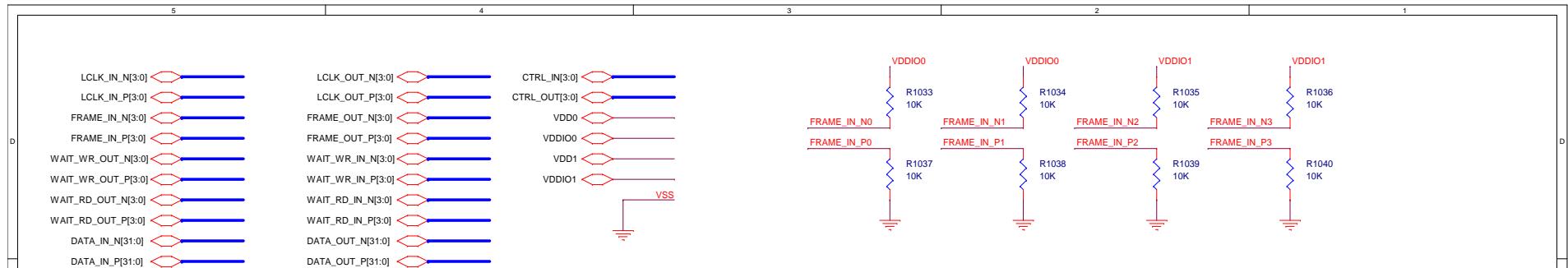
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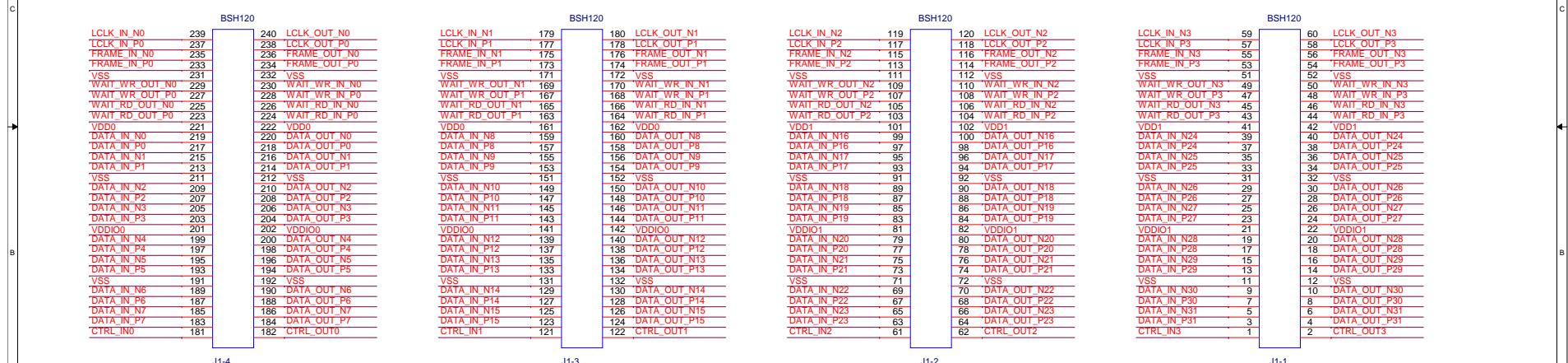
MAPPING BUSES TO 240 PIN BSH CONNECTOR



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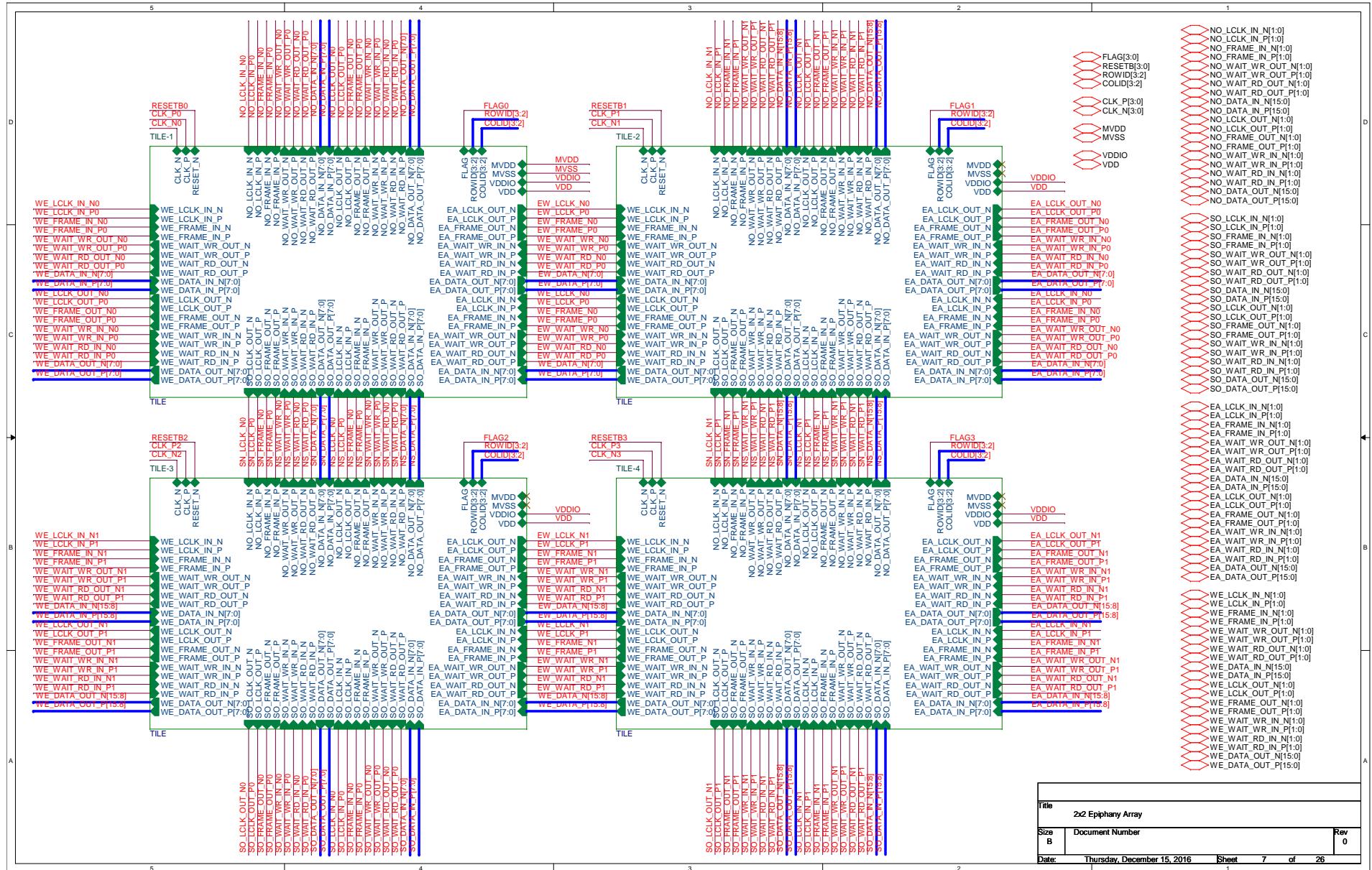


MAPPING BUSES TO 240 PIN BSH CONNECTOR

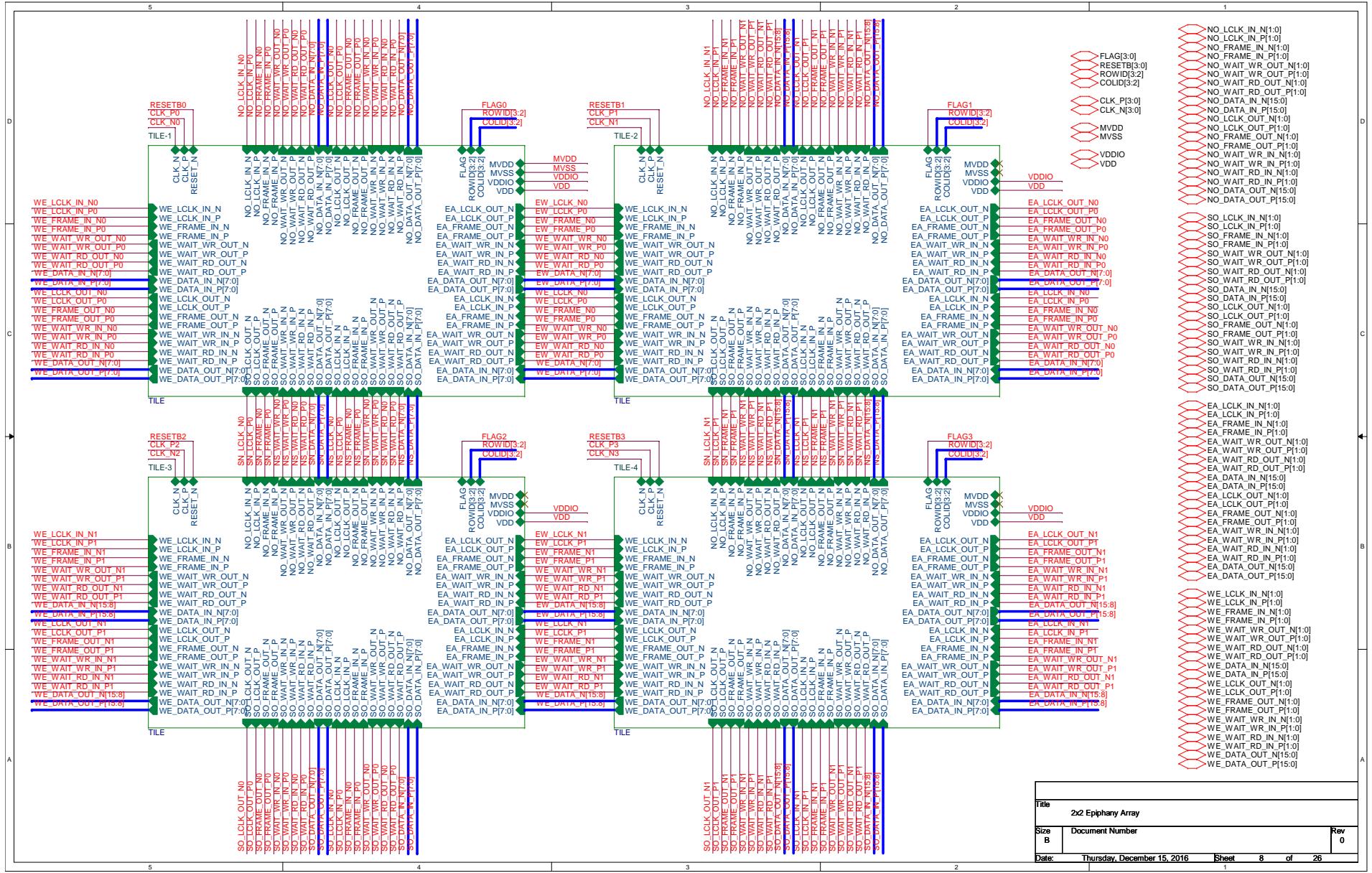


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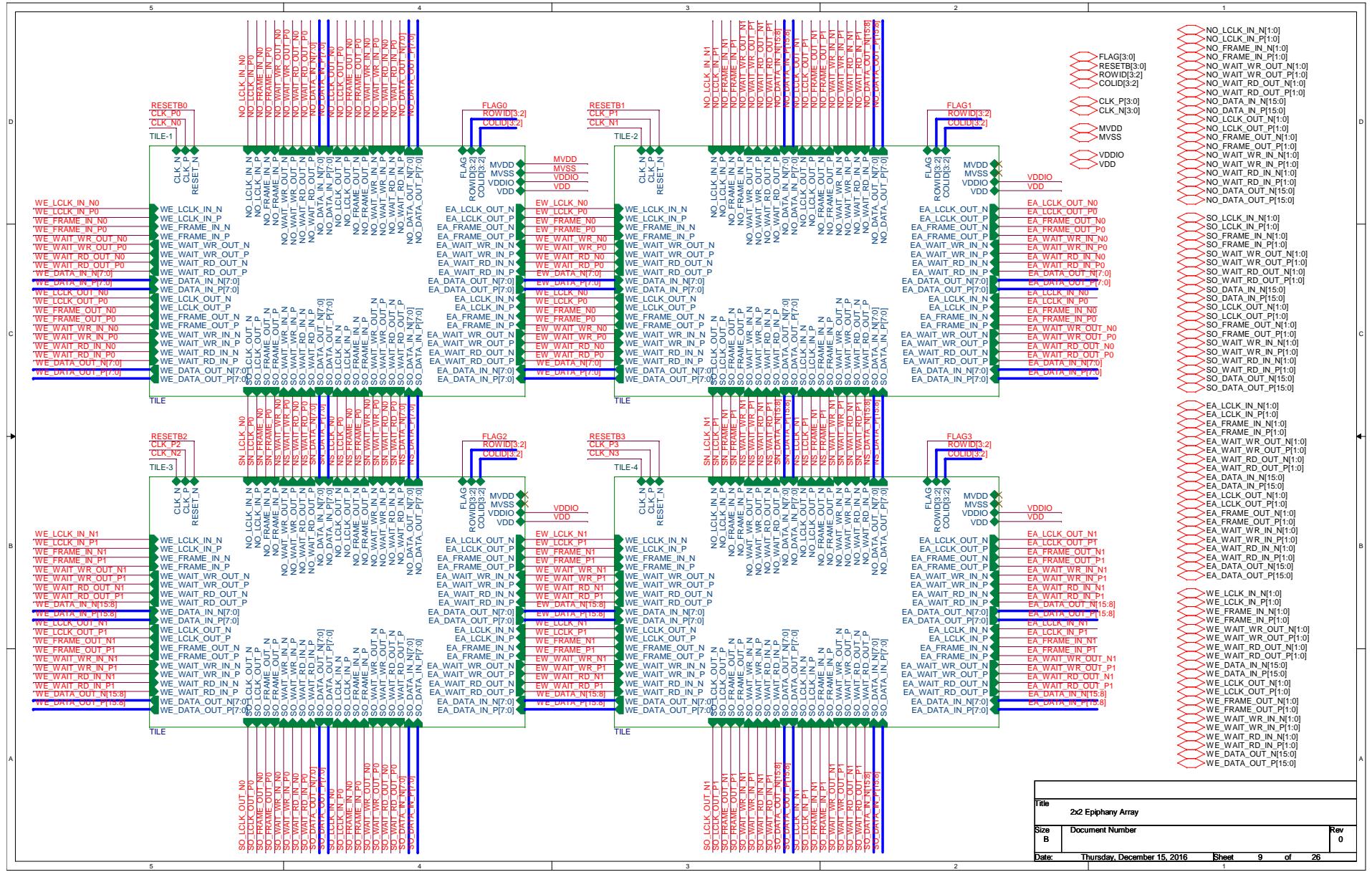
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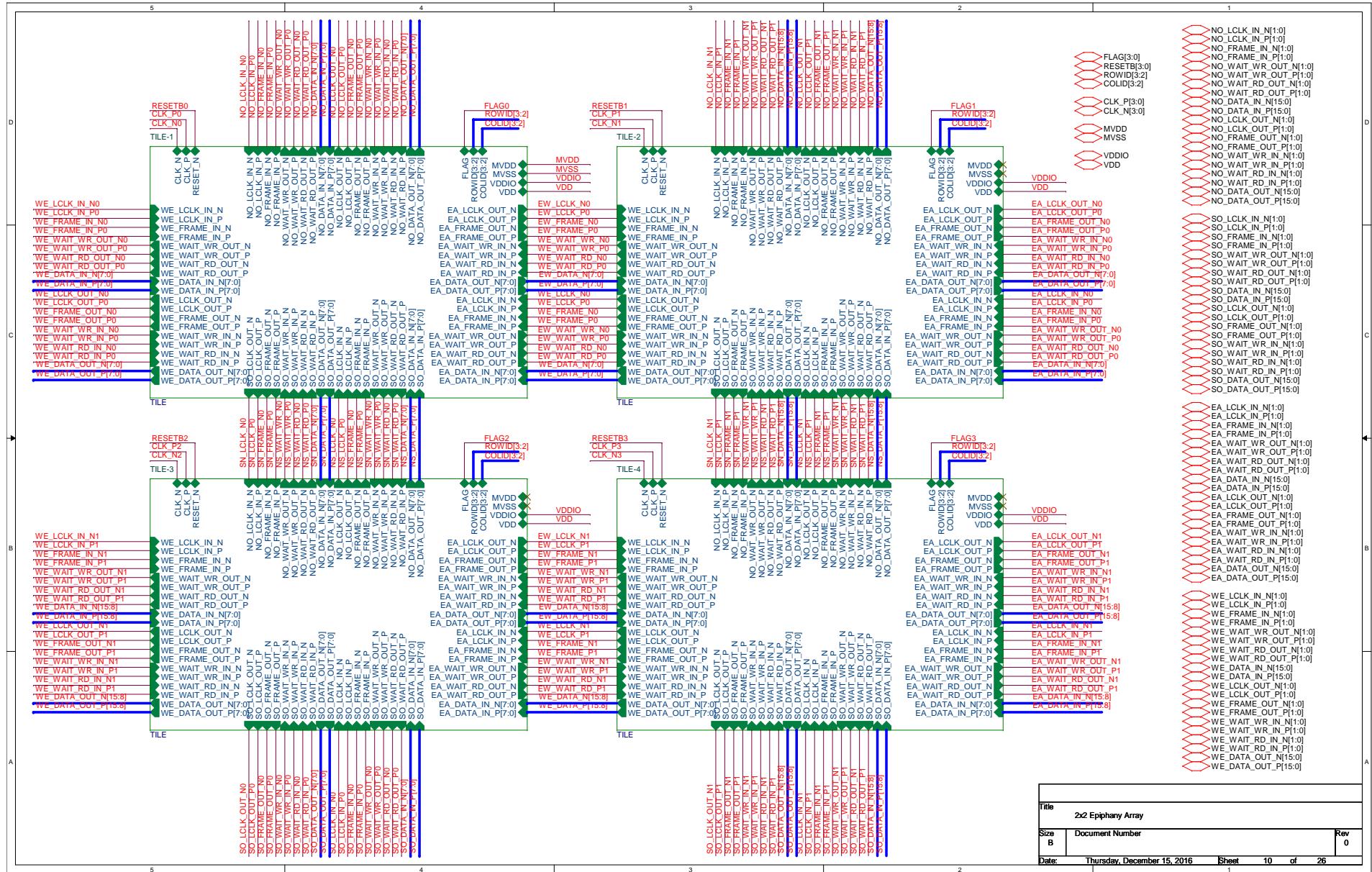
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Size B	Document Number	Rev 0
Date: Thursday, December 15, 2016	Sheet 7 of 26	1



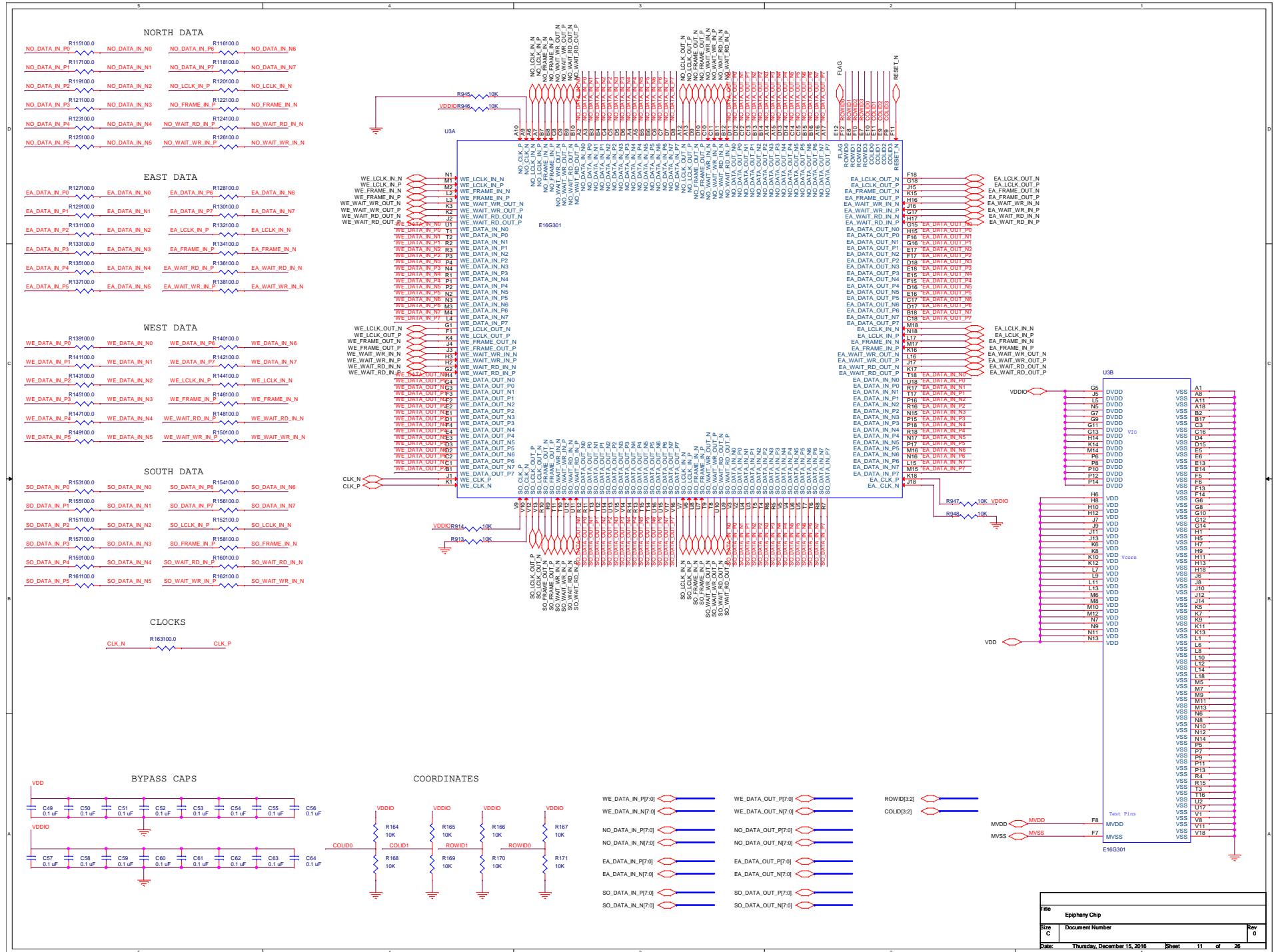
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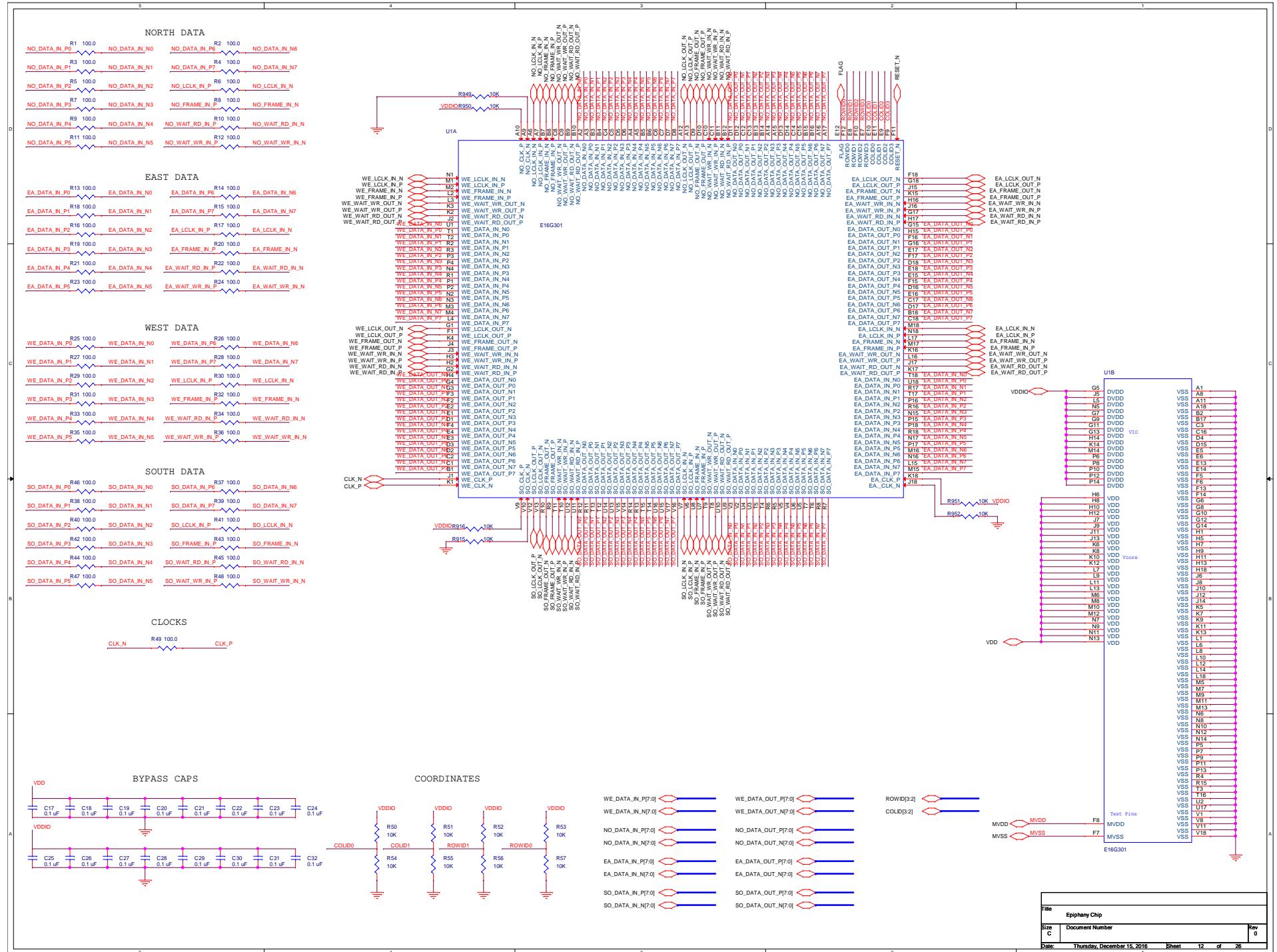


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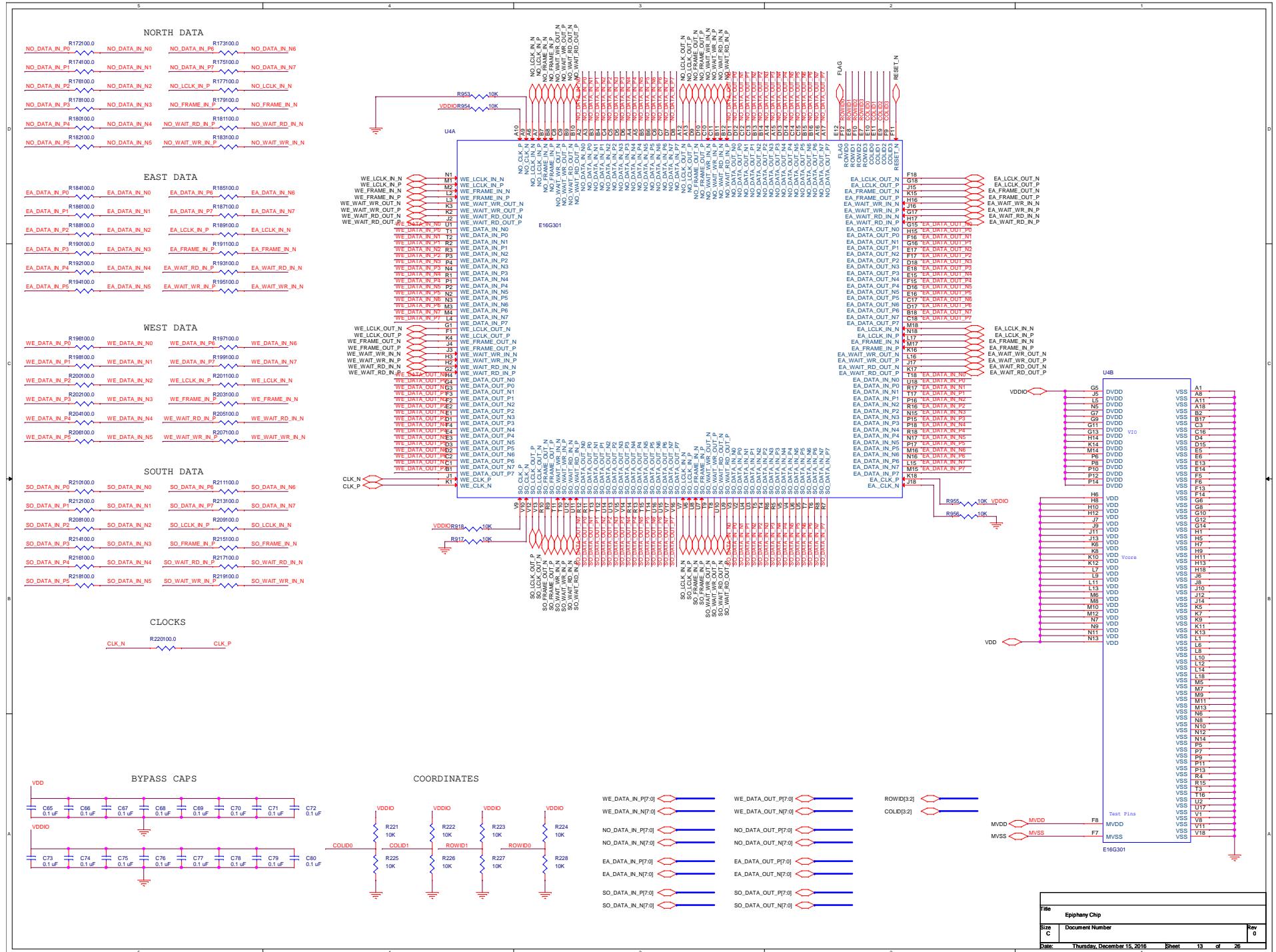


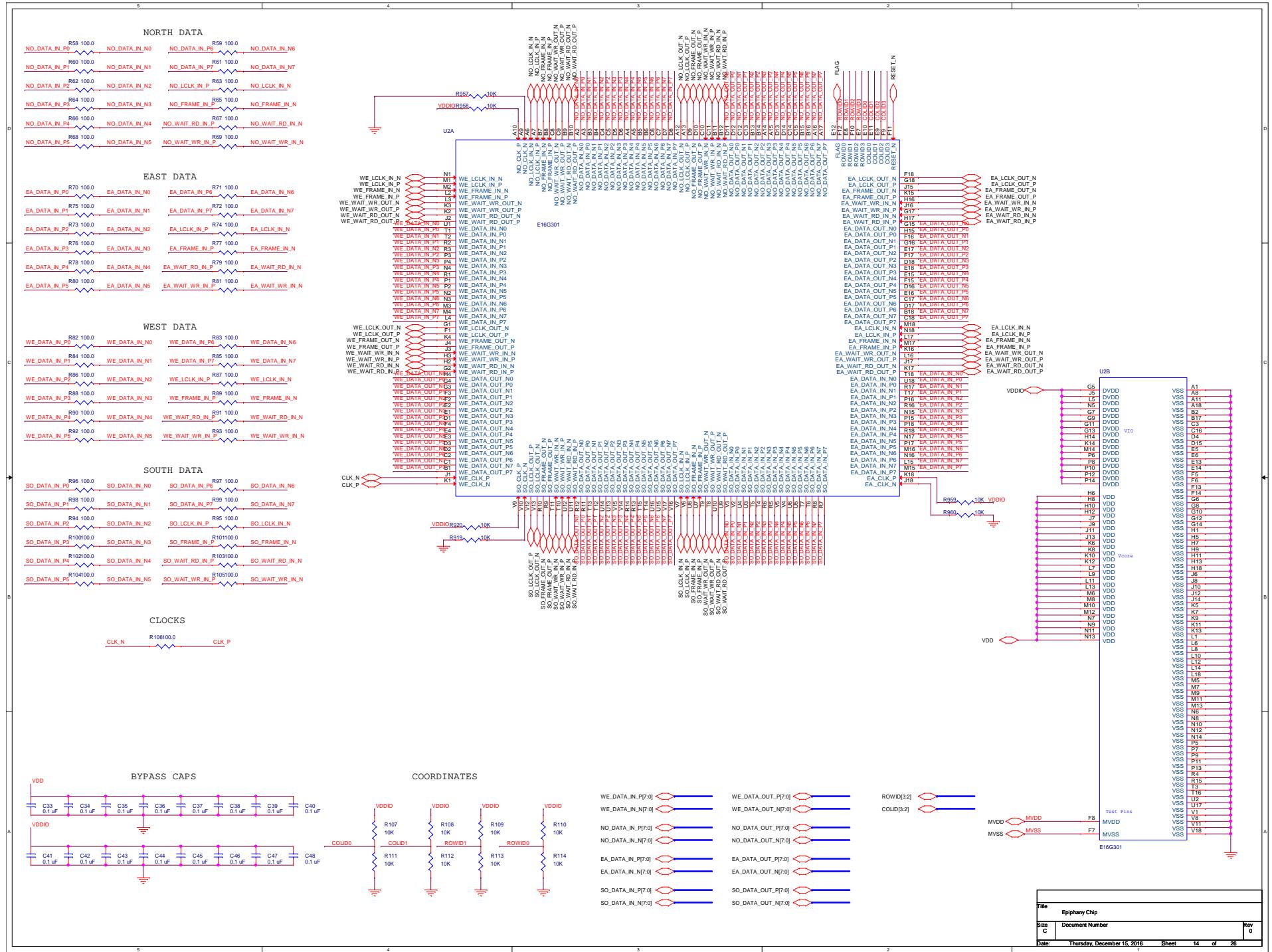
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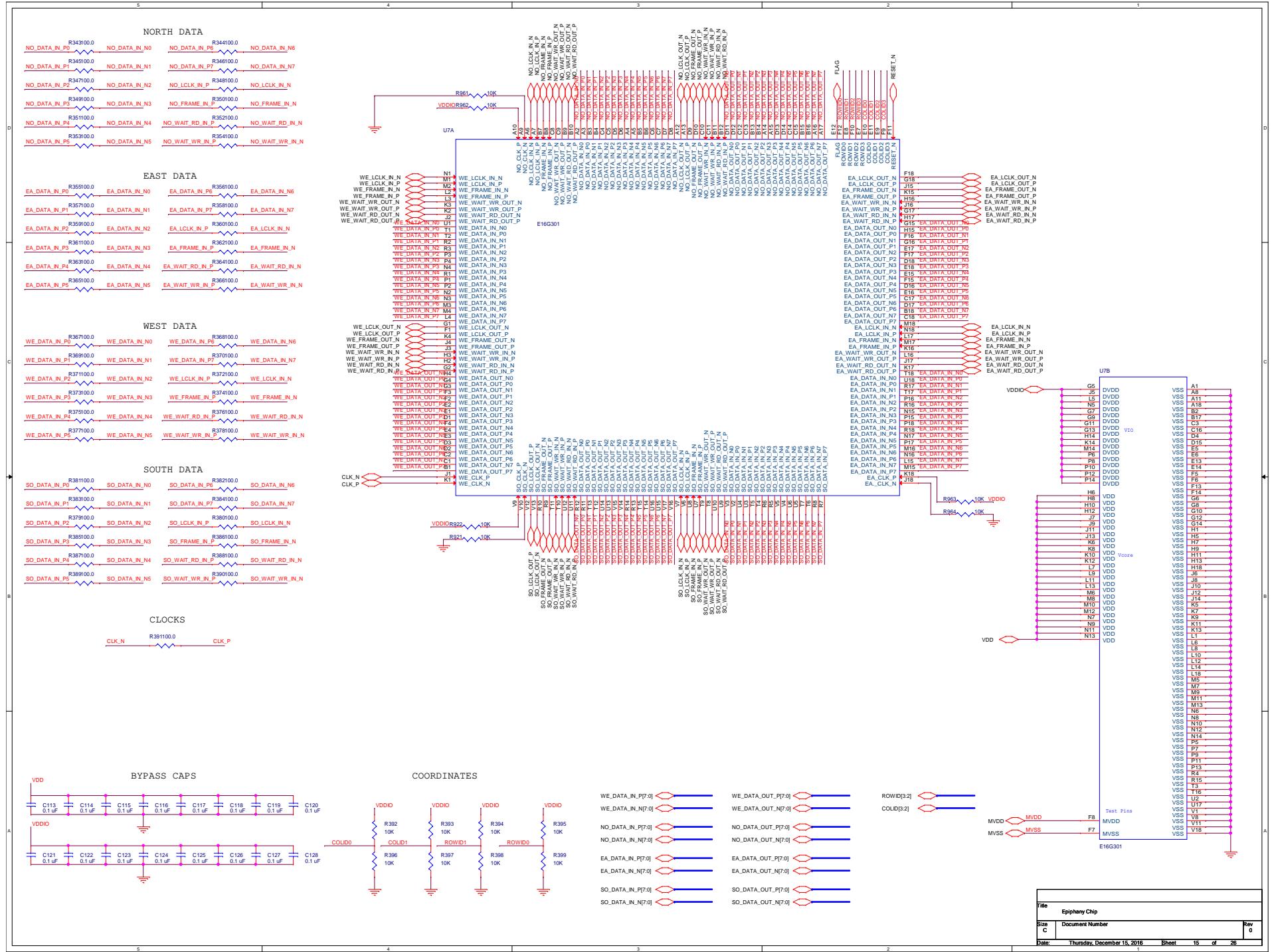




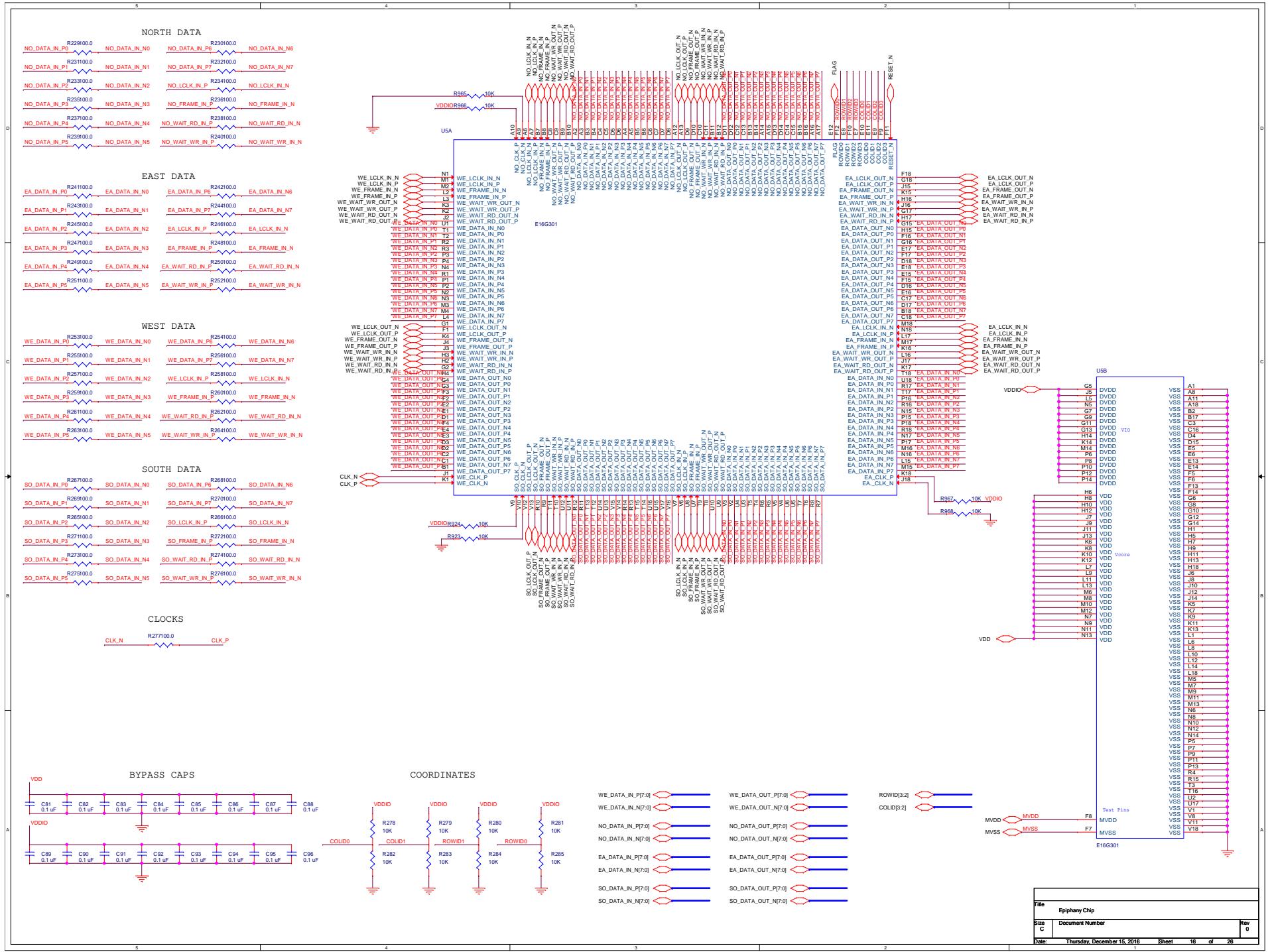
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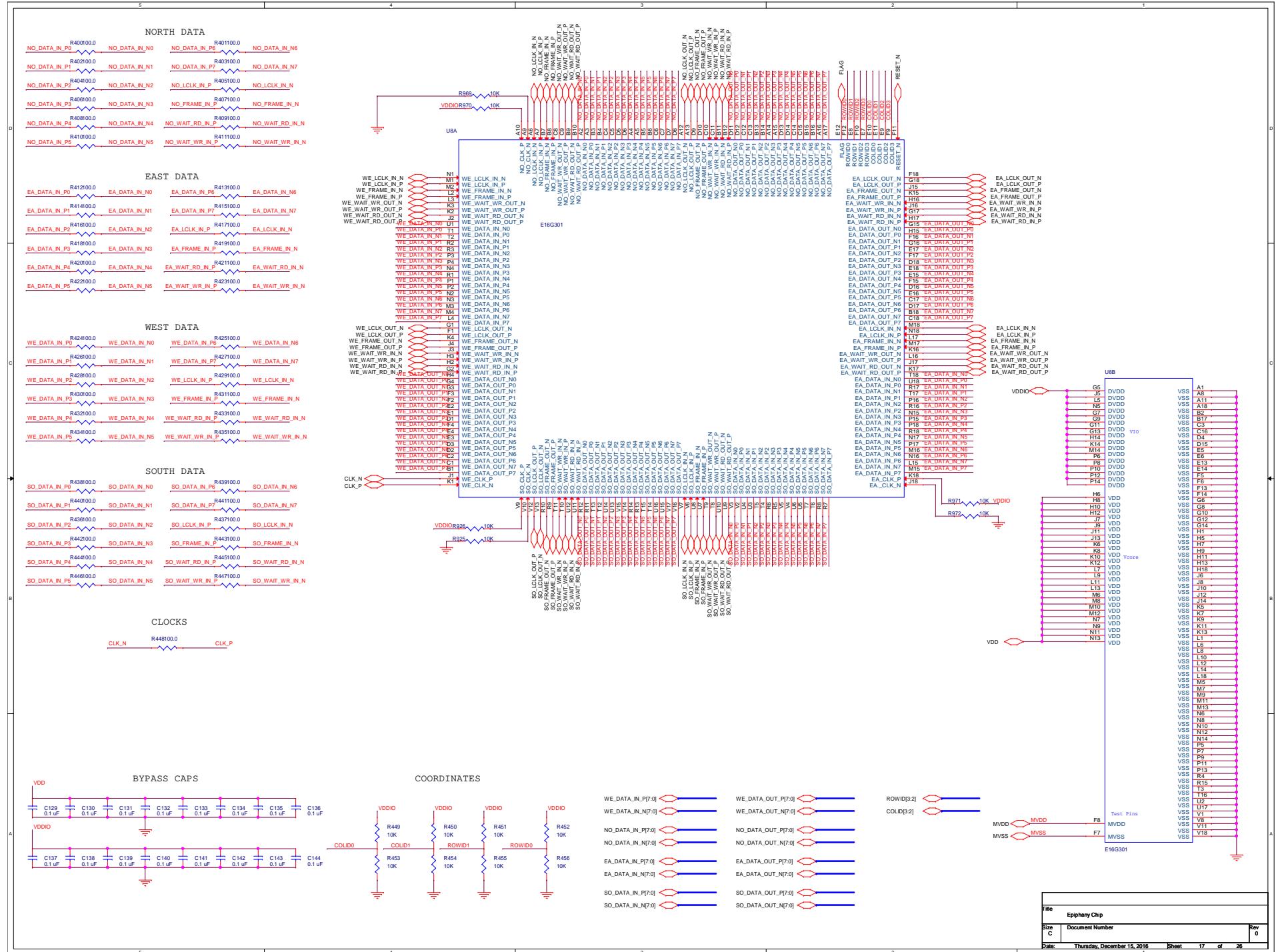


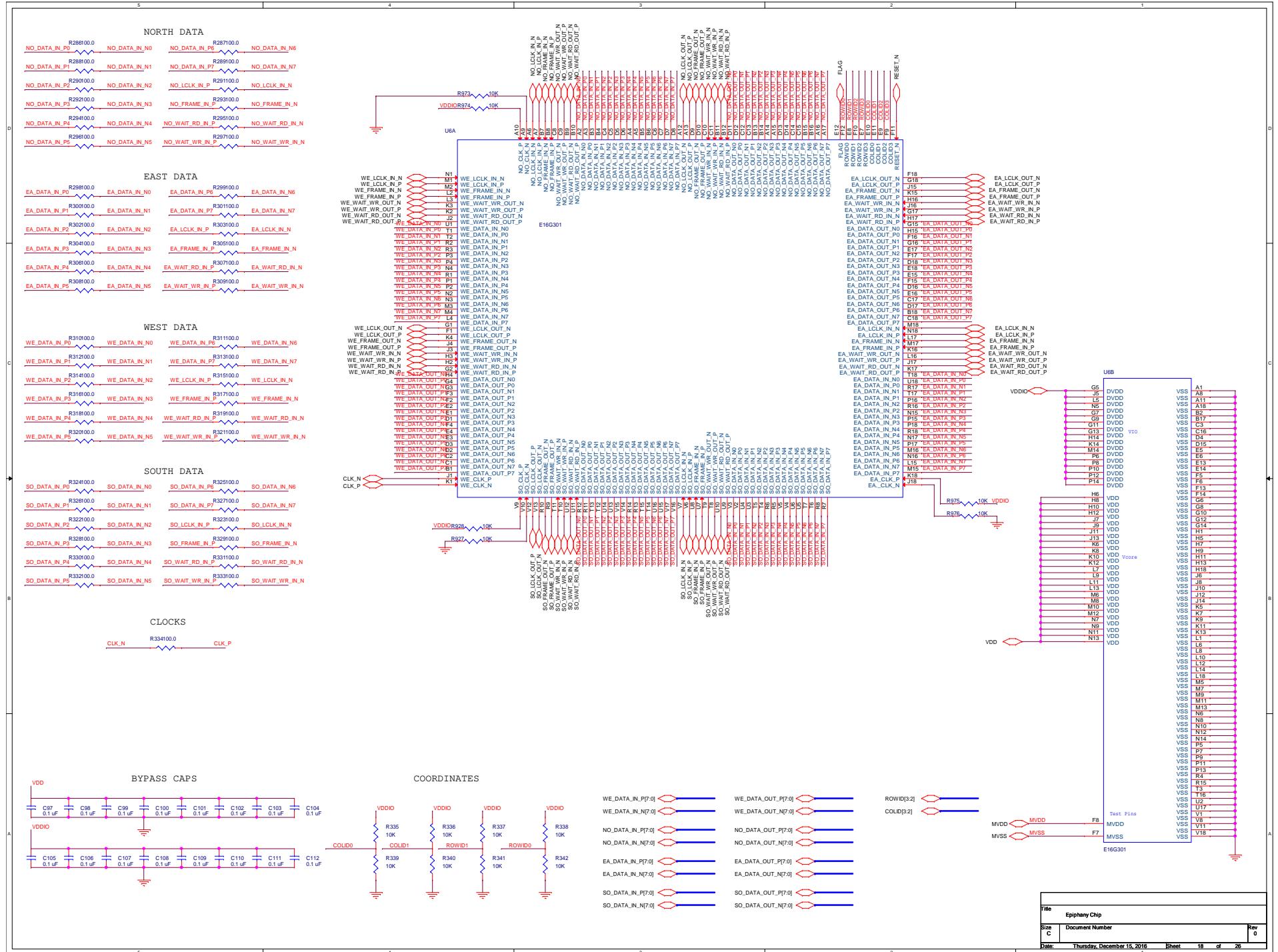


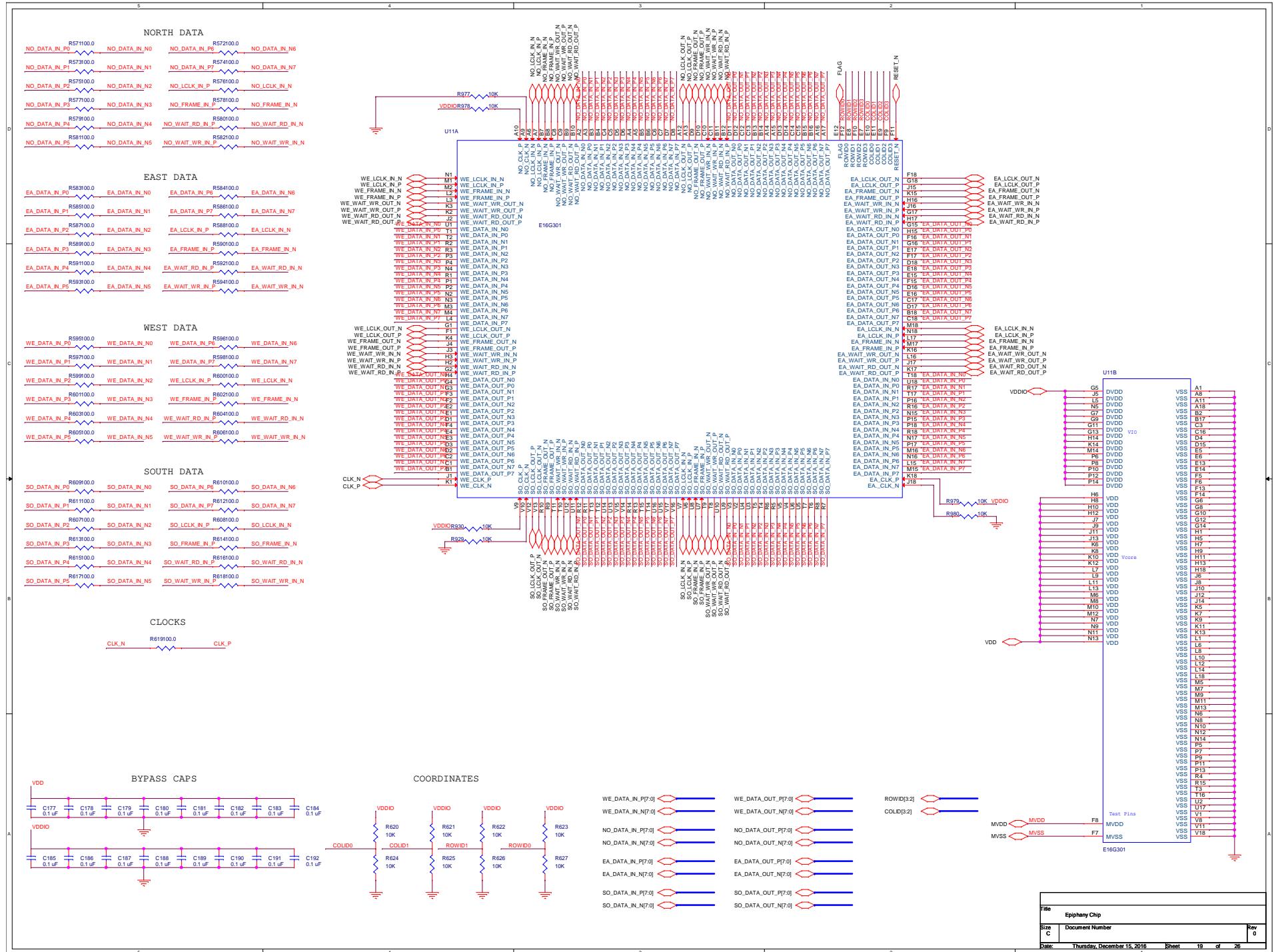


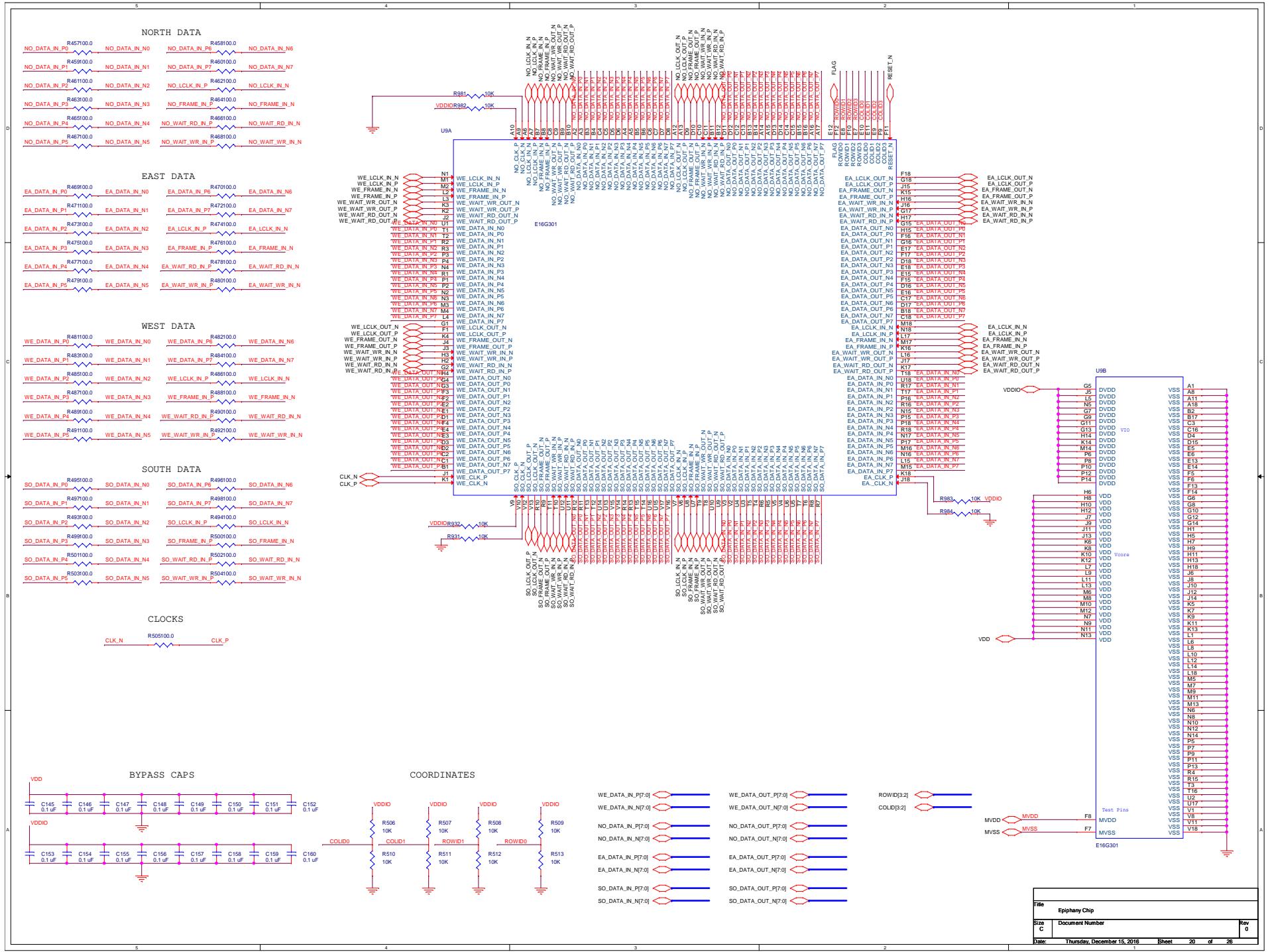
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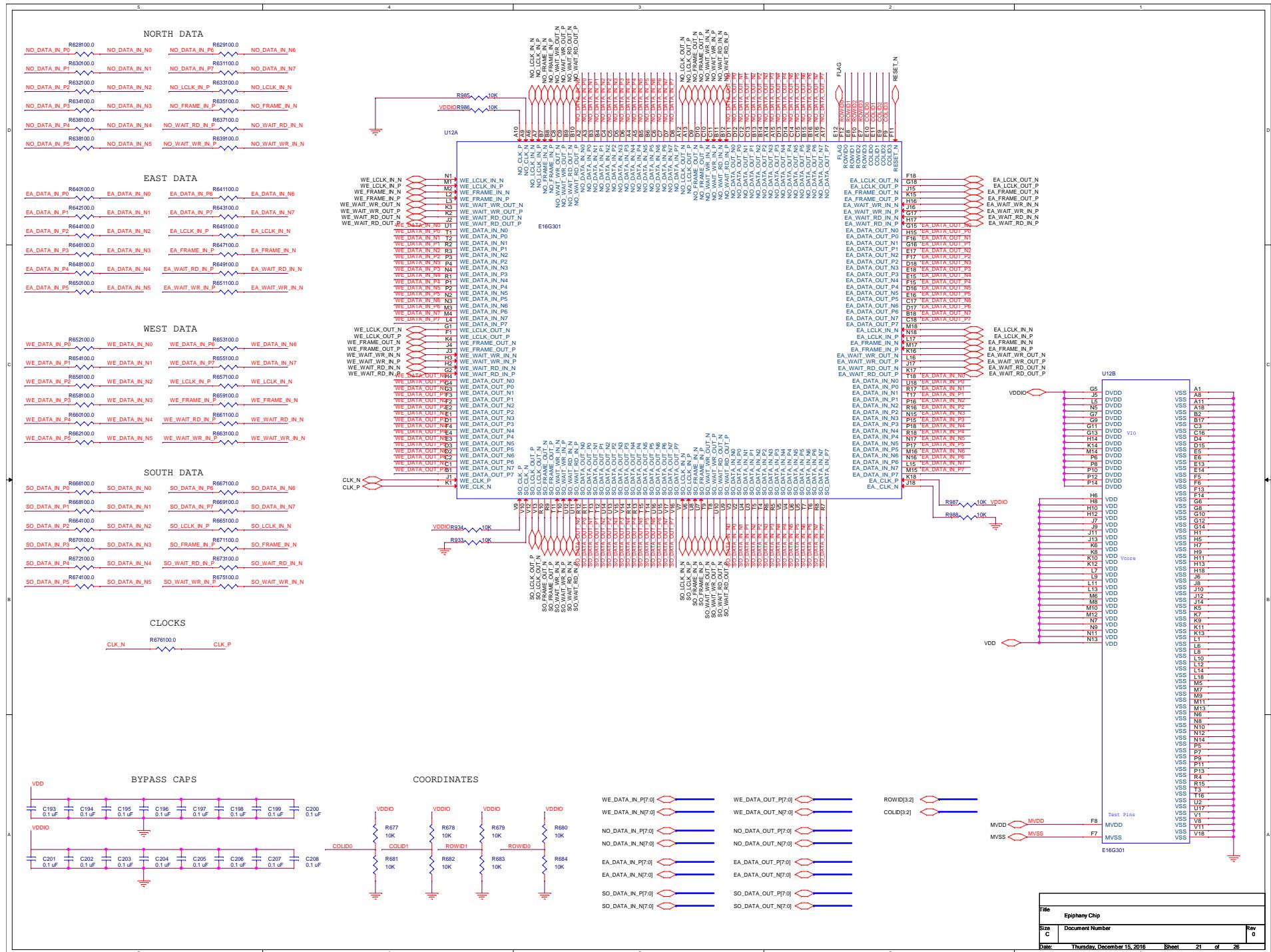








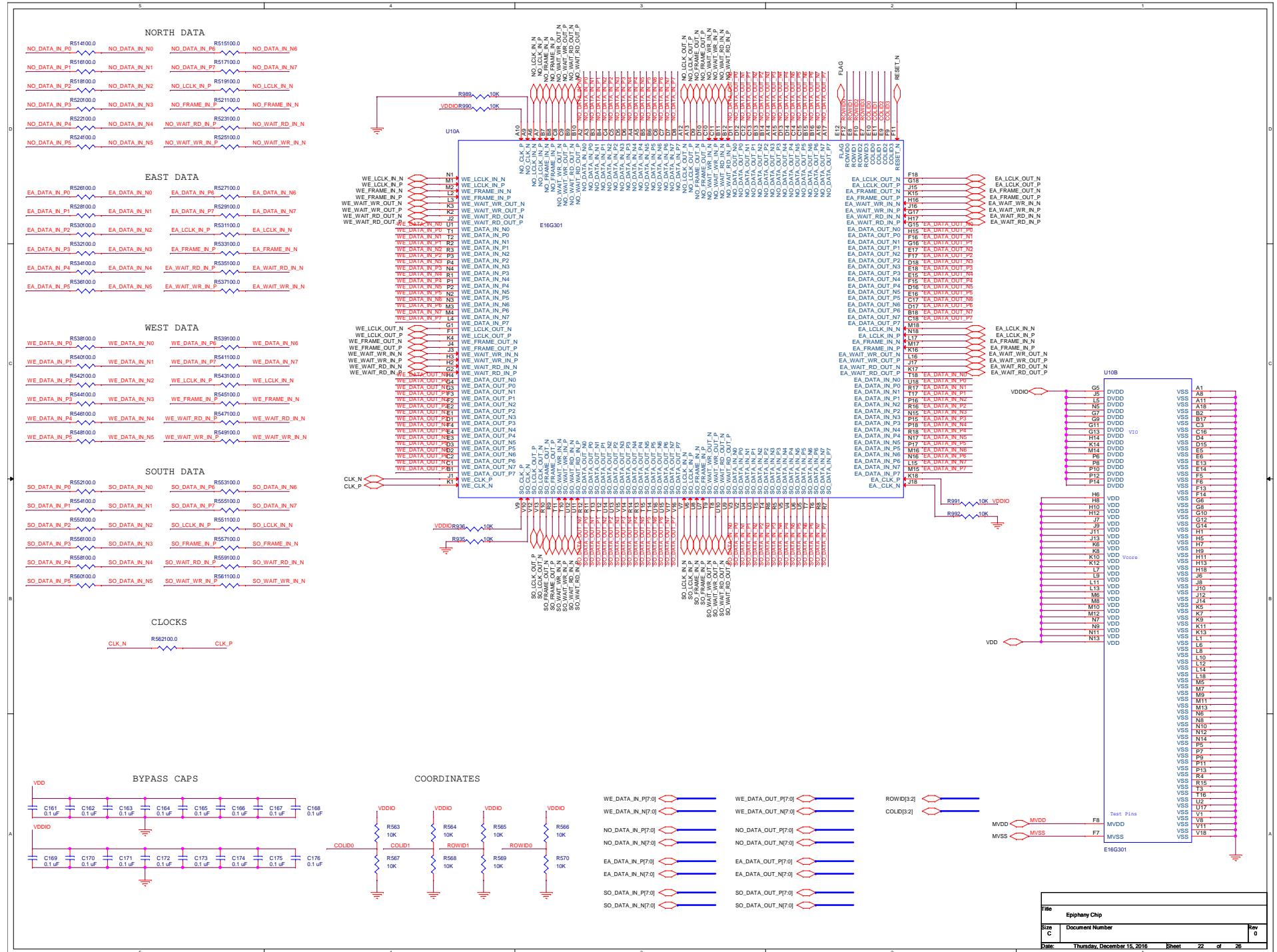


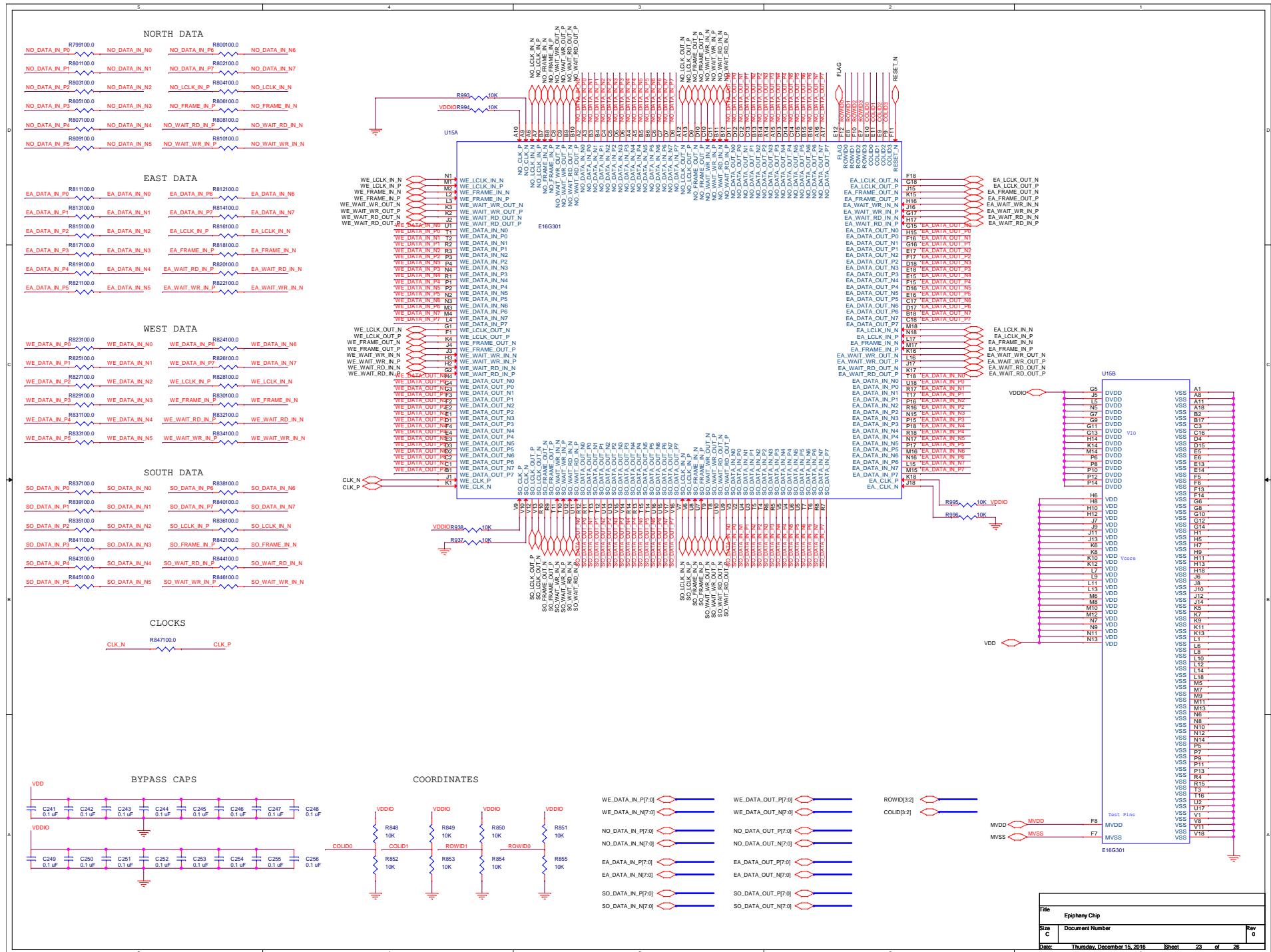


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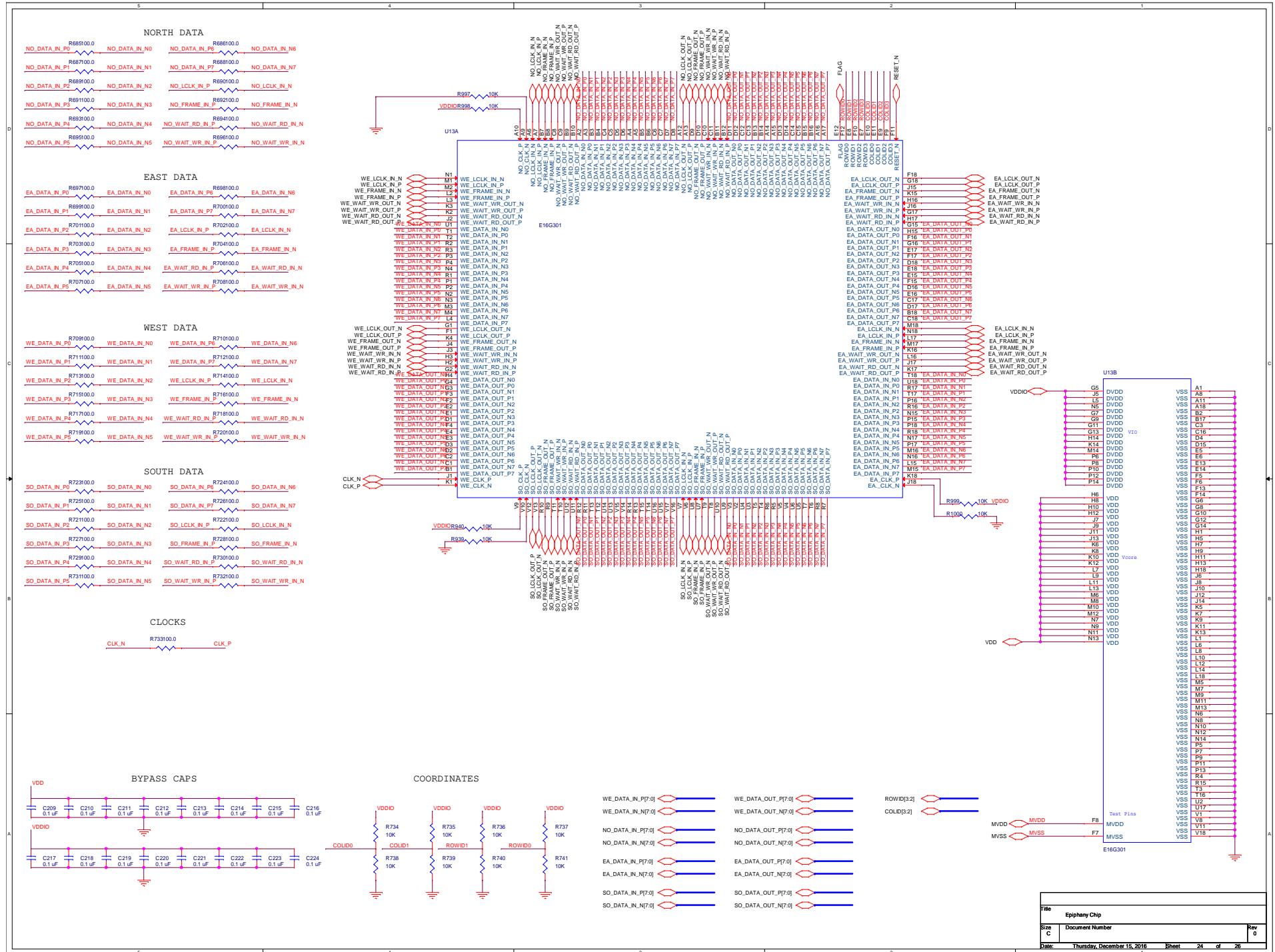
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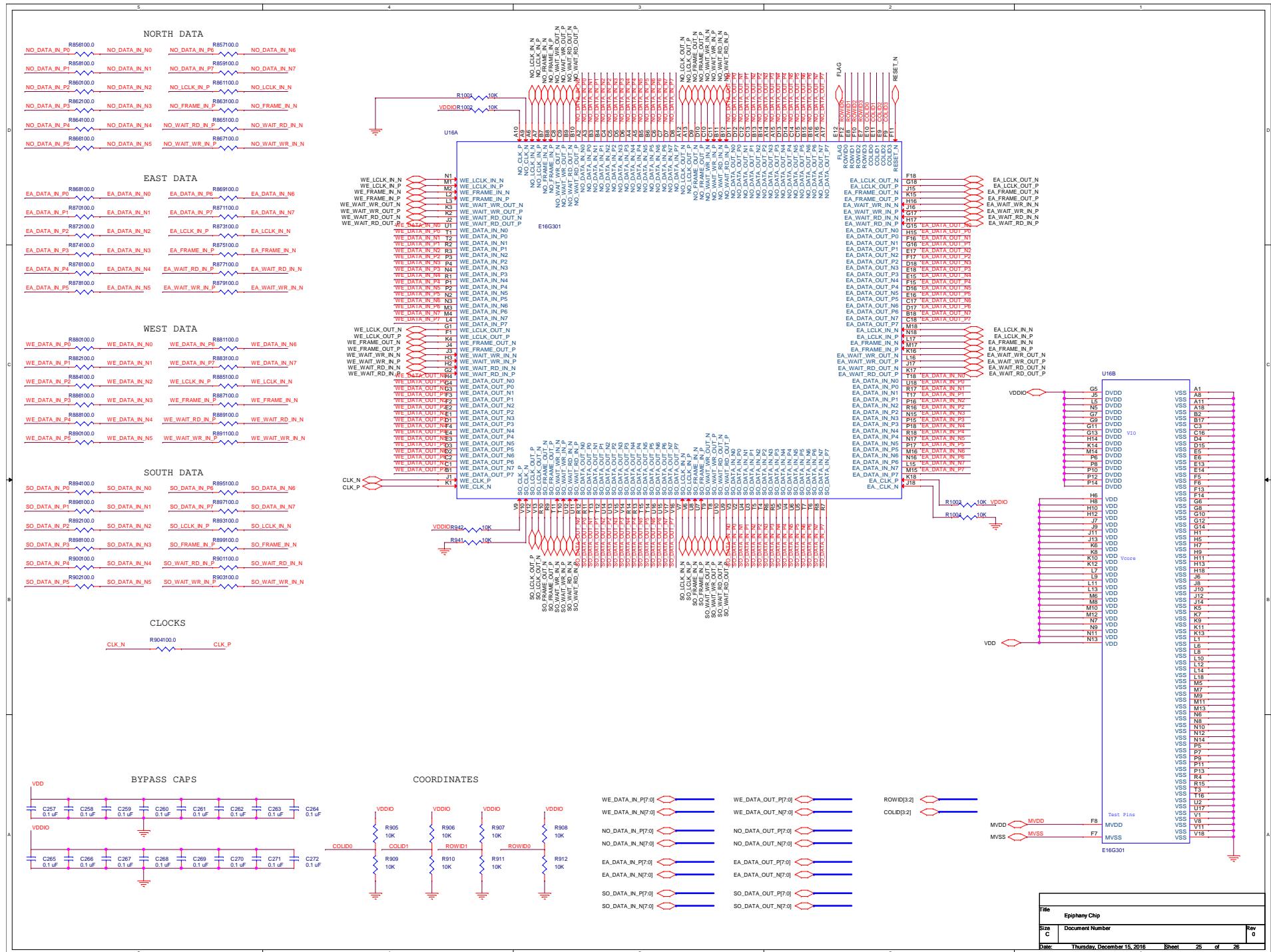


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A	Z	Test Pins	



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Epiphany Chip		
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File	Epiphany Chip
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